Engineering Dept Management Sheet							
	Program Counter (74LS161)	Workers					
Counter Division	Micro Step Counter w/ Decoder (74LS161, 74LS138)						
	Output Multiplexer (74LS107, 74LS139)						
	Arithmetic Unit (AU) (74LS283)	Workers					
Arithmetic Logic Unit (ALU) Division	Logic Unit (LU) Zero Flag (74LS32, 74LS00)	Workers					
	Error Detection (for Integer numbers) (all Basic Logic Gates)	Workers					
Input / Output	RAM Input 4-bit & 8-bit DIP Switches, (74LS189)	Workers					
Division	Output EEPROM (28HC16)	Workers					
	Instruction Decode for all Jump Instructions (J, JC, JZ) Hardwired (Basic Logic Gates)	Workers					
Control Division	Control EEPROM's (2)	Workers					

	(28HC16)	
Memory Division	Registers for ALU: 'A', 'B', 'FLAG' (74LS173's)	Workers
(Registers Only)	Registers for: 'Output', 'Instruction' & 'Memory Address' (74LS173's)	Workers
BUS Division	(74LS245's)	Workers
Clock Division	555 Timer, Switches with Debouncers (LM555's, Basic Logic Gates)	Workers

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