Engineering Dept Management Sheet

Counter Division	**Program Counter (74LS161)**	**Workers**
	Micro Step Counter w/ Decoder (74LS161, 74LS138)	
	Output Multiplexer (74LS107, 74LS139)	
Arithmetic Logic Unit (ALU) Division	**Arithmetic Unit (AU) (74LS283)**	**Workers**
	Logic Unit (LU) Zero Flag (74LS32, 74LS00)	**Workers**
	Error Detection (for Integer numbers) (all Basic Logic Gates)	**Workers**
Input / Output Division	**RAM Input 4-bit & 8-bit DIP Switches, (74LS189)**	**Workers**
	Output EEPROM (28HC16)	**Workers**
Control Division	**Instruction Decode for all Jump Instructions (J, JC, JZ) Hardwired (Basic Logic Gates)**	**Workers**
	Control EEPROM's (2)	**Workers**

	(28HC16)	
Memory Division (Registers Only)	**Registers for ALU: 'A', 'B', 'FLAG' (74LS173's)**	**Workers**
	Registers for: 'Output', 'Instruction' & 'Memory Address' (74LS173's)	**Workers**
BUS Division	**(74LS245's)**	**Workers**
Clock Division	**555 Timer, Switches with Debouncers (LM555's, Basic Logic Gates)**	**Workers**

Dates		QC Results	Redesign Dates		QC Results	
Started	Finished		Started	Finished		
Dates		**QC Results**	**Redesign Dates**		**QC Results**	
Started	Finished		Started	Finished		
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Started	**Finished**		**Started**	**Finished**		

Sheet1