Page 2
SODIMM-200 (to OEM board) connector and current measurement.

Page 3
Expansion connectors (one specifically for external memory bus).

Page 4
Parallel NOR FLASH and 16-bit registers (with LEDs) on external memory bus.

Page 5
Debug interfaces (JTAG/SWD/Trace).

Page 6
Ethernet interface connector, SD memory card interface and VBAT/ALARM handling.

Page 7
I2C-peripherals; config. e2prom, temp. sensor, 3-axis accel., port expander with pushbuttons and LEDs.

Page 8
Analog input, Push-button, Joystick and Serial Interface Expansion Connector and Jennic RF-module

Page 9
UART multiplexing, RS232 and RS422/485 interface.

Page 10
CAN and IrDA interfaces.

Page 11
USB-1 interface: OTG or Host.

Page 12
USB-2 interface: Host or Device or OTG.

Page 13
Power supply and UART-to-USB bridge.

Page 14
LCD Expansion Connector.

Page 15
I2S-Stereo Audio Codec.

UL = UnLoaded = normally not mounted component.

Default jumper settings are indicated in the schematic.
However, always check jumper positions on actual boards since there is no guarantee that all jumpers are in default place.

Rev C
Added C118-C124, D12, D13, R156, R160, R167, U6, Q7.
Changed C100 and C104.

Rev B-B2
Just layout update. No schematic change from rev PB1.
Added info about LPC4088 OEM board pinning.
Added info about LPC4357 OEM board pinning.

Rev PB1
Added support for LPC3250 (JP34). Changed R163 to UL.
Better EMC filter on LCD expansion connector.

Rev A
First revision to release.

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TITLE: OEM Base Board rev C

Document Number:

Date: 2013-02-23 15:47:33 | Sheet: 1/15

OEM board connector (200 pos SODIMM, 1V8 key)

J1	DDR2_1.8V	LPC2478/1788	LPC3250	LPC4088	LPC4357
1		ETH_TXP	ETH_TXP	ETH_TXP	ETH_TXP
2		ETH_RXP	ETH_RXP	ETH_RXP	ETH_RXP
3		ETH_TXN	ETH_TXN	ETH_TXN	ETH_TXN
4		ETH_RXN	ETH_RXN	ETH_RXN	ETH_RXN
5		ETH_VDD	ETH_VDD	ETH_VDD	ETH_VDD
6		ETH_GND	ETH_GND	ETH_GND	ETH_GND
7		ETH_LED1	ETH_LED1	ETH_LED1	ETH_LED1
8		ETH_LED2	ETH_LED2	ETH_LED2	ETH_LED2
9		VBAT_IN	VBAT_IN	VBAT_IN	VBAT_IN
10		ALARM	ONSW	ALARM	ALARM
11		RESET_IN	RESET_IN	RESET_IN	RESET
12		RESET_OUT	RESET_OUT	RESET_OUT	RESET
13		ETH_PHY_PD/SD_CD	ETH_PHY_PD	(SD_CD)	PE_14
14		JTAG_DBGEN/P5.0	JTAG_DBGEN	P5.0	JTAG_DBGEN
15		JTAG_TCK	JTAG_TCK	TCK_SWDCLK	TCK_SWDCLK
16		JTAG_RTCK/P5.4	JTAG_RTCK	P5.4	ADC7
17		JTAG_TRST	JTAG_TRST	JTAG_TRST	JTAG_TRST
18		JTAG_TMS	JTAG_TMS	TMS_SWDIO	TMS_SWDIO
19		JTAG_TDI	JTAG_TDI	JTAG_TDI	JTAG_TDI
20		JTAG_TDO	JTAG_TDO	TDO_SWO	TDO_SWO
21		V3A	VDDA	V3A	VDDA
22		VREF	NC	VREF	ADC6
23		VSSA	VSSA	VSSA	VSSA
24		GND	GND	GND	GND
25		P2.0	GPO_10	P2.0	P7_7
26		P2.1	GPO_12	P2.1	P7_0
27		P2.2	GPO_13	P2.2	P4_7
28		P2.3	GPO_15	P2.3	P4_5
29		P2.4	GPO_16	P2.4	P4_6
30		P2.5	GPO_18	P2.5	P7_6
31		P2.6	P0.2	P2.6	P8_7
32		P2.7	P0.3	P0.10	P8_6
33		P2.8	P0.4	P2.8	P8_5
34		P2.9	P0.5	P2.9	P8_4
35		P2.10	GPI_01	P2.10	ISP_EN
36		P2.11	U7_HCTS	P2.11	PF_4
37		VCC	VCC	VCC	VCC
38		GND	GND	GND	GND
39		VCC	VCC	VCC	VCC
40		GND	GND	GND	GND
41		P0.29	NC	P0.29	USB1_DP
42		P0.31	USB_CONN_DP	P0.31	USB0_DP
43		P0.30	NC	P0.30	USB1_DM
44		USBB-DM	USB_CONN_DN	USBB-DM	USB0_DM
45		P2.12	GPO_06	P2.12	P7_2
46		P2.13	PWMOUT2	P2.13	P7_1
47		P0.0	U6_IRTX	P0.0	P3_1
48		P0.1	U6_IRRX	P0.1	P3_2
49		P0.2	U5_TX	P0.2	PF_10
50		P0.3	U5_RX	P0.3	PF_11
51		P0.4	GPO_02	P0.4	P4_1
52		P0.5	GPO_03	P0.5	P4_4
53		P0.6	GPO_08	P0.6	P7_5
54		P0.7	GPO_09	P0.7	P4_8
55		P0.8	PWMOUT1	P0.8	P7_4
56		P0.9	HICORE	P0.9	P7_3
57		P0.10	U1_TX	P4.22	P9_3
58		P0.11	U1_RX	P4.23	P2_4
59		P0.12	USB_VBUS_CTRL	P0.12	inverted P2_3
60		P0.13	GPO_17	P0.13	P9_4
61		P0.14	GPO_20	P0.14	P9_5
62		P0.15	SPI1_CLK	P5.2	PF_0
63		P0.16	GPO_04	P5.3	PF_1
64		P0.17	SPI1_DATIN	P5.1	PF_2
65		P0.18	SPI1_DATIO	P5.0	PF_3
66		P0.19	GPO_05	P0.19	P4_0
67		P0.20	GPO_11	P0.20	PE_15
68		P0.21	TS_XP	P0.21	ADC5
69		P0.22	TS_YP	P0.22-SPIFI_CLK	ADC4
70		P0.23	AIN0	P0.23	ADC1
71		P0.24	AIN1	P0.24	ADC2
72		P0.25	AIN2	P0.25	ADC3
73		P0.26	GPO_00	P0.26	ADC0/DAC
74		P0.27	I2C1_SDA	P0.27	I2C_SDA
75		P0.28	I2C1_SCL	P0.28	I2C_SCL
76		GND	GND	GND	GND
77		GND	GND	GND	GND
78		P1.2	MCICLK	P1.2	PF_5
79		P1.3	MCICMD	P1.3	PC_11
80		P1.5	GPO_01	P1.5	PC_3
81		P1.6	MCIDAT0	P1.6	PD_4
82		P1.7	MCIDAT1	P1.7	PF_7
83		P1.11	MCIDAT2	P1.11	PC_14
84		P1.12	MCIDAT3	P1.12	P2_5
85		P1.13	GPIO_05	P1.13	P8_8
86		P1.18	GPO_14	P1.18	P9_6
87		P1.19	GPI_03	P1.19	PF_6
88		P1.20	U7_RX	P1.20	P4_10
89		P1.21	U7_TX	P1.21	P4_9
90		P1.22	P0.6	P1.22	P8_3
91		P1.23	P0.7	P1.23	PB_6
92		P1.24	GPO_22	P1.24	PB_5
93		P1.25	SYSCLKEN	P1.25	PB_4
94		P1.26	SPI2_DATIO	P1.26	PB_3
95		P1.27	SPI2_DATIN	P1.27	PB_2
96		P1.28	GPIO_04	P1.28	PB_1
97		P1.29	SPI2_CLK	P1.29	PB_0
98		P1.30	USB_CONN_VBUS	P1.30	USB0_VBUS
99		P1.31	GPI_02	P1.31	P8_0
100		NAND_FLASH_RDY	GPO_19	NAND_FLASH_RDY	NAND_FLASH_RDY
101					
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J1	LPC2478/1788	LPC3250	LPC4088	LPC4357
1	GND	GND	GND	GND
2	GND	GND	GND	GND
3	NC	I2S1TX_CLK	NC	P3_0
4	NC	I2S1TX_SDA	NC	PC_12
5	NC	I2S1TX_WS	NC	PC_13
6	NC	P0.0	NC	P6_0
7	NC/P5.4	P0.1	P5.4	P6_1
8	NC/P5.3	GPI_00	P5.3	P6_2
9	NC/P5.2	I2C2_SDA	P5.2	WAKEUP3
10	NC	I2C2_SCL	NC	WAKEUP2
11	NC/P1.16	GPI_04	P1.16	WAKEUP1
12	NC	GPI_06	NC	WAKEUP0
13	NC/BCS1	USB_CONN_ID	BCS1	USB0_ID
14	NC/P4.30	POWER_ON	P4.30	SAMPLE
15	NC/P1.16	TST_CLK2	P1.16	CLK2_OUT
16	P2.14	P2.7	P2.14	P9_2
17	P2.15	GPIO_00	P2.15	P8_1
18	P2.19	GPIO_01	P2.19	P8_2
19	P2.21	GPI_07	P2.21	PC_2
20	P2.22	P2.0	P2.22	PA_1
21	P2.23	P2.1	P2.23	PA_2
22	P2.25	P2.2	P2.25	PA_3
23	P2.26	P2.3	P2.26	P9_0
24	P2.27	P2.4	P2.27	P9_1
25	P2.30	P2.5	P2.30	PF_8
26	P2.31	P2.6	P2.31	PF_9
27	P4.28	GPO_07	P4.28	P4_3
28	P4.29	GPO_21	P4.29	P4_2
29	GND	GND	GND	GND
30	GND	GND	GND	GND
31	BA15	BA15	BA15	BA15
32	BDQM1/BCS2	BCS3	BCS2	BCS2
33	BA14	BA14	BA14	BA14
34	BDQM0/BCS0	BCS2	BCS0	BCS0
35	BA13	BA13	BA13	BA13
36	BCAS/BBLS3	BCS1	BBLS3	BBLS3
37	BA12	BA12	BA12	BA12
38	BRAS/BBLS2	BCS0	BBLS2	BBLS2
39	BA11	BA11	BA11	BA11
40	BBLS1	BBLS1	BBLS1	BBLS1
41	BA10	BA10	BA10	BA10
42	BBLS0	BBLS0	BBLS0	BBLS0
43	BA9	BA9	BA9	BA9
44	BWE	BWE	BWE	BWE
45	BA8	BA8	BA8	BA8
46	BOE	BOE	BOE	BOE
47	BA7	BA7	BA7	BA7
48	BA23	BA23	BA23	BA23
49	BA6	BA6	BA6	BA6
50	BA22	BA22	BA22	BA22
51	BA5	BA5	BA5	BA5
52	BA21	BA21	BA21	BA21
53	BA4	BA4	BA4	BA4
54	BA20	BA20	BA20	BA20
55	BA3	BA3	BA3	BA3
56	BA19	BA19	BA19	BA19
57	BA2	BA2	BA2	BA2
58	BA18	BA18	BA18	BA18
59	BA1	BA1	BA1	BA1
60	BA17	BA17	BA17	BA17
61	BA0	BA0	BA0	BA0
62	BA16	BA16	BA16	BA16
63	DBUS_EN/BCS3	NC	BCS3	BCS3
64	ABUF_EN/(NC)	ABUF_EN	GND	(PD_1)
65	VCC	VDD_EXT	VCC	VCC
66	GND	GND	GND	GND
67	BD15	BD15	BD15	BD15
68	BD31/P3.31	GPI_08	BD31/P3.31	BD31
69	BD14	BD14	BD14	BD14
70	BD30/P3.30	GPO_23	BD30/P3.30	BD30
71	BD13	BD13	BD13	BD13
72	BD29/P3.29	GPI_09	BD29/P3.29	BD29
73	BD12	BD12	BD12	BD12
74	BD28/P3.28	GPI_19	BD28/P3.28	BD28
75	BD11	BD11	BD11	BD11
76	BD27/P3.27	P2.8	BD27/P3.27	BD27
77	BD10	BD10	BD10	BD10
78	BD26/P3.26	P2.9	BD26/P3.26	BD26
79	BD9	BD9	BD9	BD9
80	BD25/P3.25	P2.10	BD25/P3.25	BD25
81	BD8	BD8	BD8	BD8
82	BD24/P3.24	P2.11	BD24/P3.24	BD24
83	BD7	BD7	BD7	BD7
84	BD23/P3.23	P2.12	BD23/P3.23	BD23
85	BD6	BD6	BD6	BD6
86	BD22/P3.22	GPI_28	BD22/P3.22	BD22
87	BD5	BD5	BD5	BD5
88	BD21/P3.21	U2_TX	BD21/P3.21	BD21
89	BD4	BD4	BD4	BD4
90	BD20/P3.20	U2_RX	BD20/P3.20	BD20
91	BD3	BD3	BD3	BD3
92	BD19/P3.19	GPI_05	BD19/P3.19	BD19
93	BD2	BD2	BD2	BD2
94	BD18/P3.18	U2_CTS	BD18/P3.18	BD18
95	BD1	BD1	BD1	BD1
96	BD17/P3.17	U3_RX	BD17/P3.17	BD17
97	BD0	BD0	BD0	BD0
98	BD16/P3.16	U3_TX	BD16/P3.16	BD16
99	VCC	VDD_EXT	VCC	VCC
100	GND	GND	GND	GND

GPIO0_TRACE_LCDPWR, GPIO1_TRACE_LCDLE, GPIO2_TRACE_LCDDCLK, GPIO3_TRACE_LCDFP, GPIO4_TRACE_LCDENAB, GPIO5_TRACE_LCDLP, GPIO6_TRACE_LCDVD4, GPIO7_TRACE_LCDVD5, GPIO8_TRACE_LCDVD6, GPIO9_TRACE_LCDVD7, GPIO10_IRQ_ISPEN, GPIO11_LCDCLKIN, VCC_MAIN

C1 22uF/10V

USB1-DP, USB2-DP, USB1-DM, USB2-DM, GPIO12_LCDVD18, GPIO13_LCDVD19, GPIO14_CANRD_TXD-IR, GPIO15_CANTD_RXD-IR, GPIO16_TXD, GPIO17_RXD, GPIO18_CANRD_I2SRX-BCK_LCDVD0, GPIO19_CANTD_I2SRX-WS_LCDVD1, GPIO20_I2SRX-DATA_LCDVD8, GPIO21_I2STX-BCK_LCDVD9, GPIO22_I2STX-WS_CDVD16, GPIO23_I2STX-DATA_LCDVD17, GPIO24_TXD, GPIO25_RXD, GPIO26_USB2-PWREN, GPIO27_USB2-UPLED, GPIO28_USB2-CONN, GPIO29_SPI-SCK, GPIO30_SPI-SSEL, GPIO31_SPI-MISO, GPIO32_SPI-MOSI, GPIO33_LCD-DC, GPIO34_TP-CS, GPIO35, GPIO36, GPIO37_AIN0, GPIO38_AIN1, GPIO39_AIN2, GPIO40_AOUT, I2C-SDA, I2C-SCL

MCICLK, MCICMD, MCIPWR, MCIDAT0, MCIDAT1, MCIDAT2, MCIDAT3, GPIO41, GPIO42_USB1-UPLED, GPIO43_USB1-PWREN, GPIO44_LCDVD10, GPIO45_LCDVD11, GPIO46_LCDVD12, GPIO47_LCDVD13, GPIO48_LCDVD14, GPIO49_LCDVD15, GPIO50_LCDVD20, GPIO51_USB1-INT_LCDVD21, GPIO52_USB1-SCL_LCDVD22, GPIO53_USB1-SDA_LCDVD23, GPIO54_USB2-VBUS, GPIO55_USB2-OVRCR, NAND_FLASH_RDY

GPIO56_I2STX-BCK, GPIO57_I2STX-DATA, GPIO58_I2STX-WS, GPIO59_I2SRX-BCK, GPIO60_I2SRX-WS, GPIO61_I2SRX-DATA, GPIO62, GPIO63, GPIO64, GPIO65, GPIO66, GPIO67, GPIO68_I2S-MCLK, GPIO69, GPIO70, GPIO71, GPIO72, GPIO73, GPIO74, GPIO75, GPIO76, GPIO77, GPIO78, GPIO79, GPIO80_LCDVD2, GPIO81_LCDVD3

BA15, BCSX, BA14, BCSY, BA13, BCAS-BCS1-BBLS3, BA12, BRAS-BCS0-BBLS2, BA11, BBLS1, BA10, BBLS0, BA9, BWE, BA8, BOE, BA7, BA23, BA6, BA22, BA5, BA21, BA4, BA20, BA3, BA19, BA2, BA18, BA1, BA17, BA0, BA16, DBUS_EN-BCS3, ABUF_EN, VCC_BUFFERS

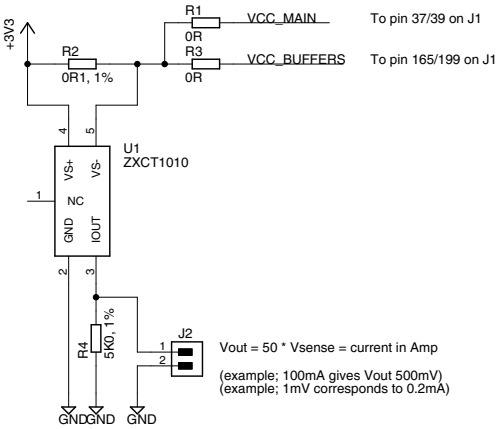
C2 22uF/10V

BD15, GPIO82_BD31, BD14, GPIO83_RTS_BD30, BD13, GPIO84_BD29, BD12, GPIO85_BD28, BD11, GPIO86_BD27, BD10, GPIO87_BD26, BD9, GPIO88_BD25, BD8, GPIO89_BD24, BD7, GPIO90_BD23, BD6, GPIO91_RI_BD22, BD5, GPIO92_DTR_BD21, BD4, GPIO93_DSR_BD20, BD3, GPIO94_DCD_BD19, BD2, GPIO95_CTS_BD18, BD1, GPIO96_RXD_BD17, BD0, GPIO97_TXD_BD16, VCC_BUFFERS

C3 22uF/10V

GND

Current Measurement



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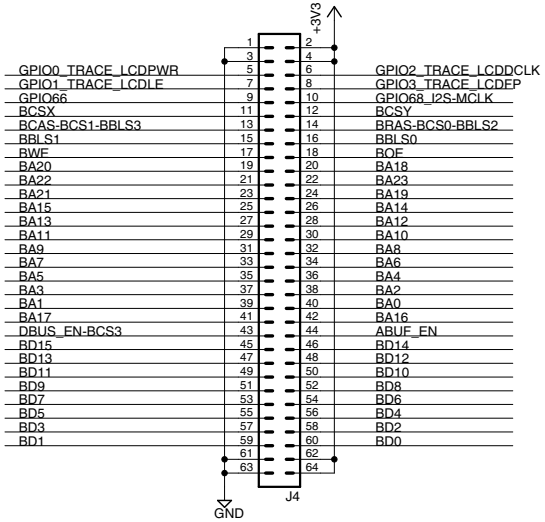
TITLE: OEM Base Board rev C

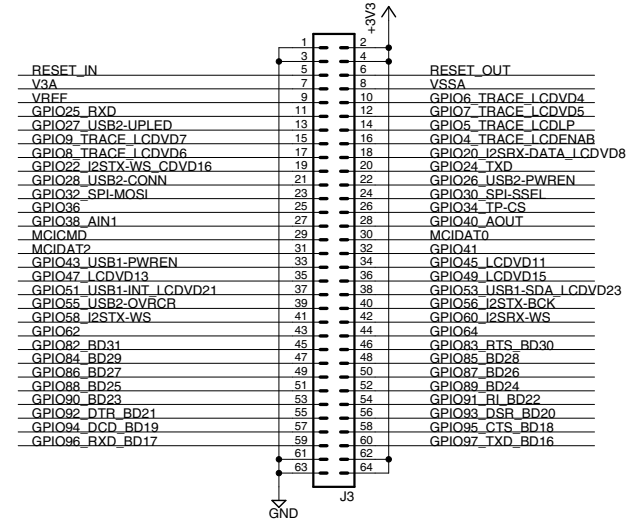
Document Number:

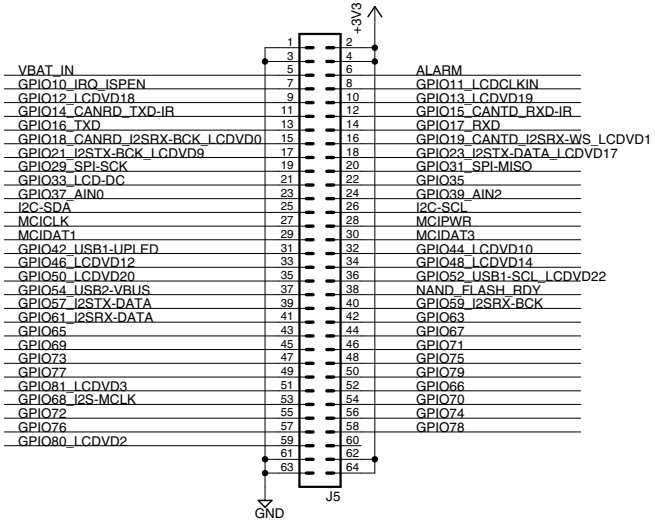
Date: 2013-02-23 15:47:33 | Sheet: 2/15

Expansion Connectors

Expansion connector for external memory bus (16-bit)







Possible connectors to mount: AWHW64G-0202-T-R from Assmann Electronics Inc (Digikey: HRP64H-ND) or XG4C-6431 from Omron (Digikey: OR970-ND)

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TITLE: OEM Base Board rev C	
Document Number:	
Date: 2013-02-23 15:47:33	Sheet: 3/15

External buffered memory bus

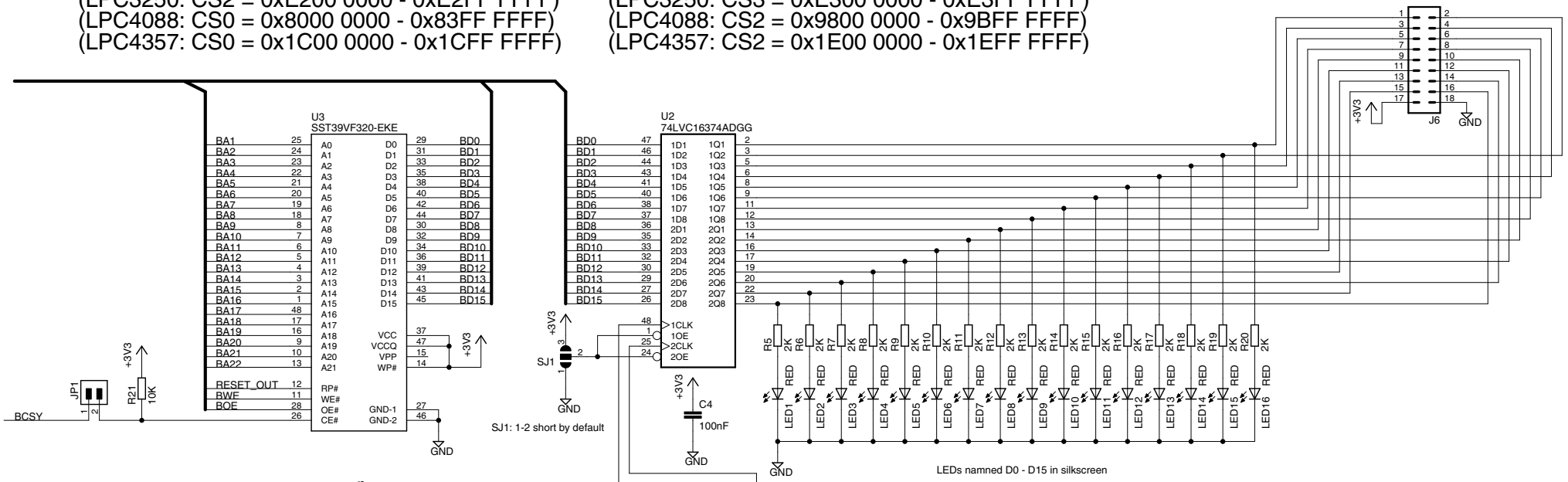
32Mbit (4MByte) NOR FLASH

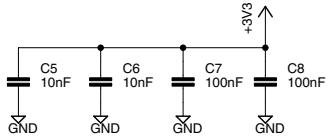
(LPC1788: CS0 = 0x8000 0000 - 0x83FF FFFF)
(LPC3250: CS2 = 0xE200 0000 - 0xE2FF FFFF)
(LPC4088: CS0 = 0x8000 0000 - 0x83FF FFFF)
(LPC4357: CS0 = 0x1C00 0000 - 0x1CFF FFFF)

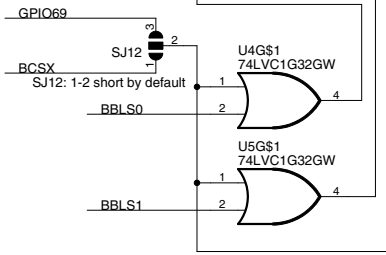
8+8 bit register on external memory bus

(LPC1788: CS2 = 0x9800 0000 - 0x9BFF FFFF)
(LPC3250: CS3 = 0xE300 0000 - 0xE3FF FFFF)
(LPC4088: CS2 = 0x9800 0000 - 0x9BFF FFFF)
(LPC4357: CS2 = 0x1E00 0000 - 0x1EFF FFFF)

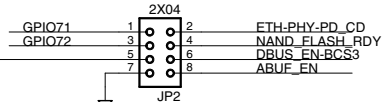
16-bit parallel output for logging



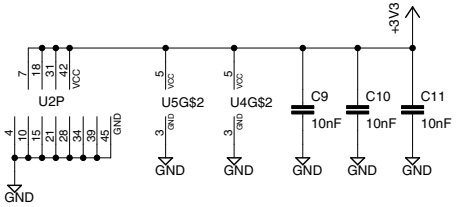




Optional Connections



Only used for LPC2478 OEM boards



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TITLE: OEM Base Board rev C	
Document Number:	
Date: 2013-02-23 15:47:33	Sheet: 4/15

Debug Interfaces

ARM 10-pin interface Serial Wire Mode

1-VCC	2-SWDIO
3-GND	4-SWCLK
5-GND	6-SWO
7-N/U	8-N/U
9-GND	10-RESET

ARM 10-pin interface JTAG Mode

1-VCC	2-TMS
3-GND	4-TCLK
5-GND	6-TDO
7-RTCK	8-TDI
9-GND	10-RESET

ARM 20-pin interface Serial Wire Mode

1-VCC (Vtref)	2-N/C
3-N/U	4-GND
5-N/U	6-GND
7-SWDIO	8-GND
9-SWCLK	10-GND
11-N/U	12-GND
13-SWO	14-GND
15-RESET	16-GND
17-N/C	18-GND
19-N/C	20-GND

ARM 20-pin interface JTAG Mode

1-VCC (Vtref)	2-N/C
3-N/C (TRST)	4-GND
5-TDI	6-GND
7-TMS	8-GND
9-TCLK	10-GND
11-RTCK	12-GND
13-TDO	14-GND
15-RESET	16-GND
17-N/C	18-GND
19-N/C	20-GND

10 pos (50 mil pitch) connector

J7
2x5 pos, 50mil pitch

20 pos (100 mil pitch) connector

J8
2x10 pos, 100mil pitch

R22 UL

ETM Interface (not mounted)

J9
MICTOR ETM connector

R23 50R GPIO0_TRACE_LCDPWR
R24 50R GPIO9_TRACE_LCDVD7
R27 50R GPIO8_TRACE_LCDVD6
R31 50R GPIO7_TRACE_LCDVD5
R32 50R GPIO6_TRACE_LCDVD4
R33 50R GPIO5_TRACE_LCDLP
R35 50R GPIO4_TRACE_LCDENAB
R36 50R GPIO3_TRACE_LCDFP
R38 50R GPIO2_TRACE_LCDDCLK
R39 50R GPIO1_TRACE_LCDLE

JTAG_TMS R34 50R
JTAG_TDI R37 50R
JTAG_TRST R40 50R
JTAG_TCK R41 50R
JTAG_RTCK R42 50R
JTAG_TDO R43 50R
RESET_IN R44 50R

R28 10K, R29 10K, R30 10K (+3V3)
R45 10K, R46 10K (GND)
R25 24K, R26 24K (GND)

D12 IP4221CZ6-S
D13 IP4221CZ6-S
C122 100nF
C123 100nF

Cortex Debug + ETM Interface

J10
2x10 pos, 50mil pitch

pin12: TRCCK - P2.6 on LPC1768
pin14: TRCO0 - P2.5 on LPC1768
pin16: TRCO1 - P2.4 on LPC1768
pin18: TRCO2 - P2.3 on LPC1768
pin20: TRCO3 - P2.2 on LPC1768

Enable ETM for LPC2478

JTAG_RTCK R47 4K7
JP3
Default: not inserted

Reset Push-button & LED

R49 100K
SW1 PUSH BUTTON
RESET_IN
R48 2K
LED17 RED
RESET_OUT

JTAG/SWD Control

JP4
R156 1K
JTAG_DBGEN
R50 3K

Open: Enable for LPC3250
Short: Enable for LPC2478
Short: Enable for LPC4357

LPC1788: other function
LPC4088: other function

Note: all JTAG/SWD and ETM signals routed with 50 ohm impedance, equal length and minimal stubs.

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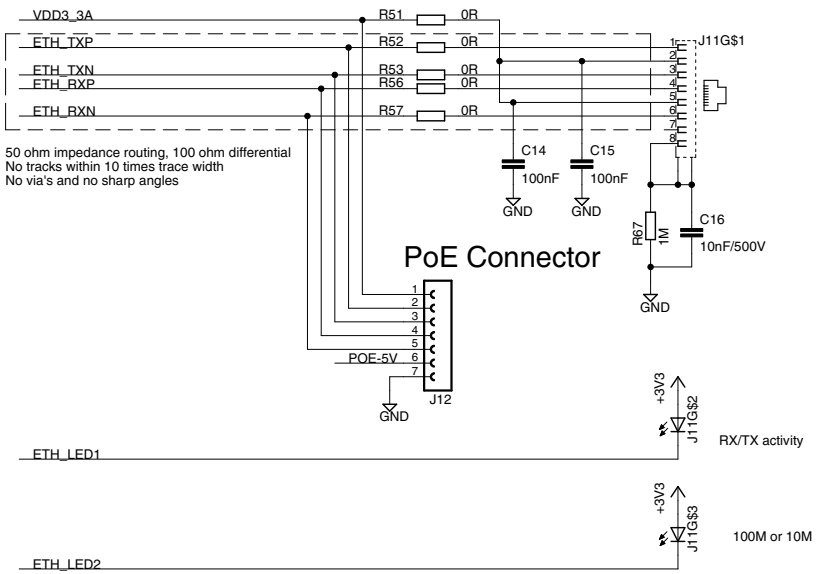
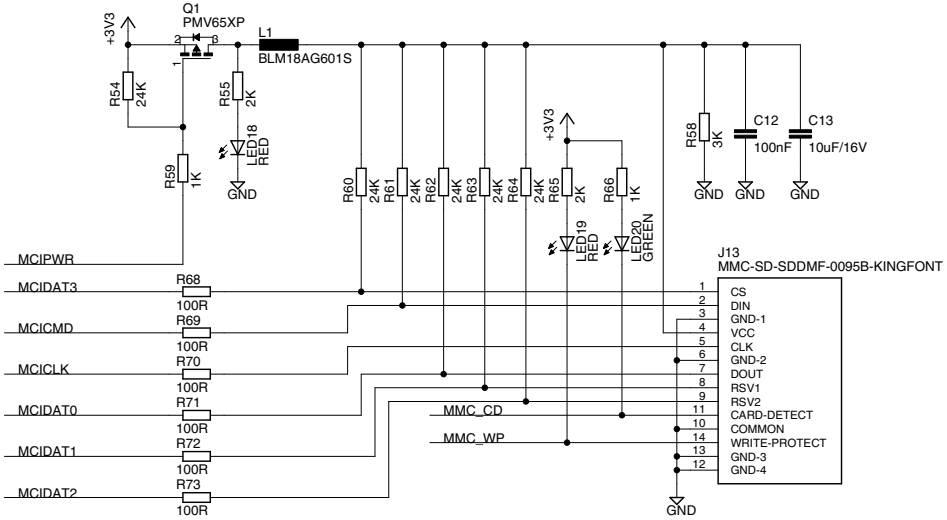
TITLE: OEM Base Board rev C	
Document Number:	
Date: 2013-02-23 15:47:33	Sheet: 5/15

Ethernet and SD/MMC interfaces, VBAT and Alarm

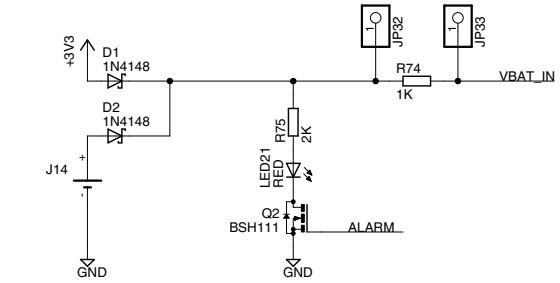
RJ45 Ethernet Connector



MMC/SD Memory Card I/F



VBAT and Alarm Handling



Battery holder
Note: battery (CR1025) not included.

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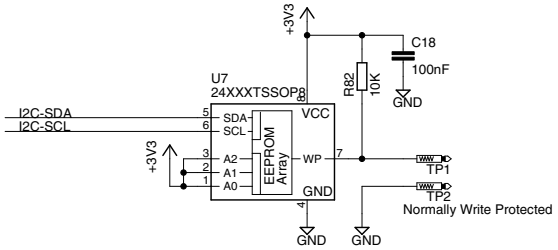
TITLE: OEM Base Board rev C	
Document Number:	
Date: 2013-02-23 15:47:33	Sheet: 6/15

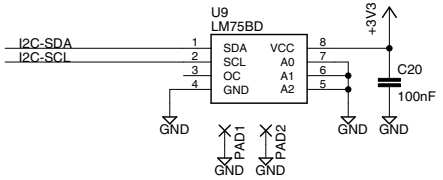
I2C-peripherals

64kbit Configuration E2PROM
(see User's Manual for details)



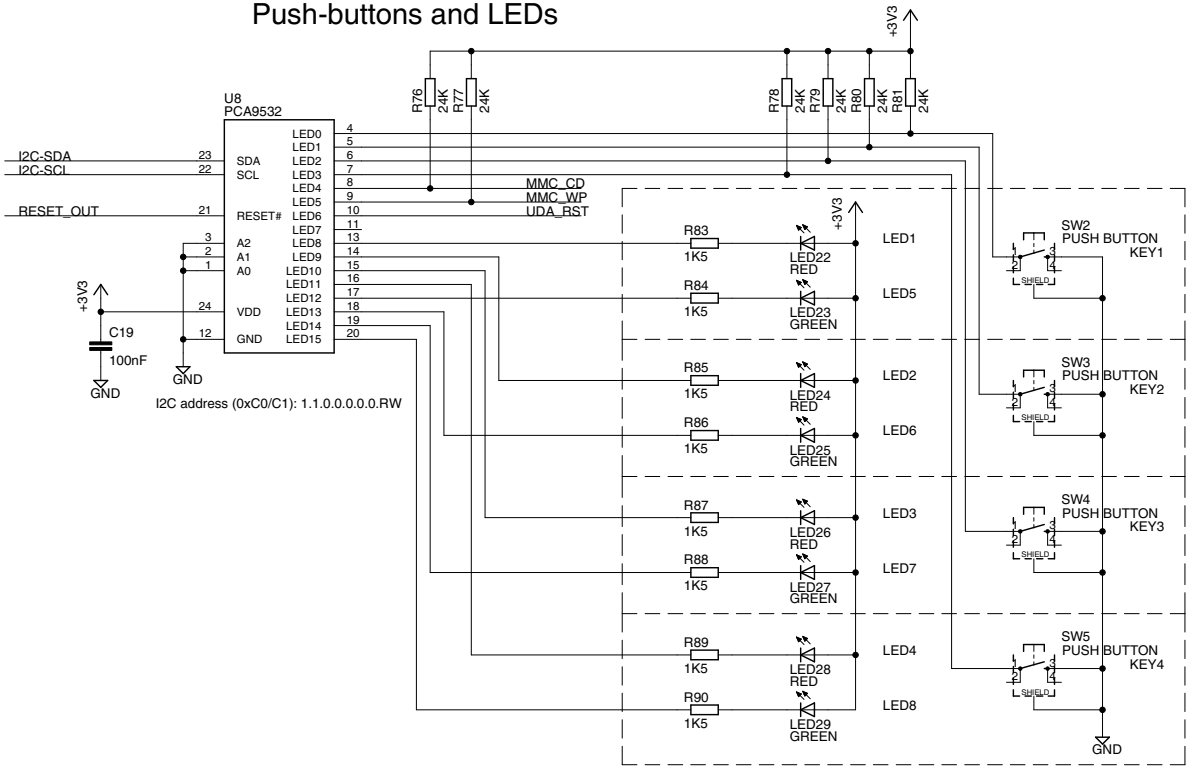
I2C address (0xAE/AF): 1.0.1.0.1.1.1.RW

LM75 I2C Temperature Sensor



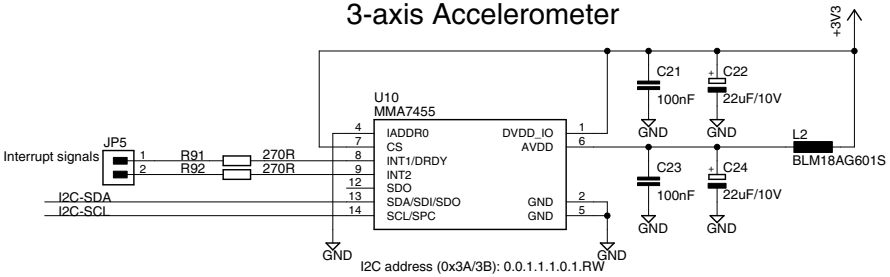
I2C address (0x90/91): 1.0.0.1.0.0.0.RW

Push-buttons and LEDs



I2C address (0xC0/C1): 1.1.0.0.0.0.0.RW

3-axis Accelerometer



I2C address (0x3A/3B): 0.0.1.1.1.0.1.RW

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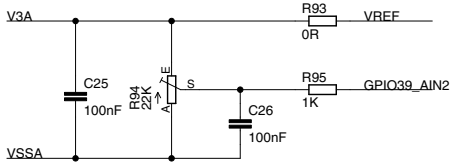
TITLE: OEM Base Board rev C	
Document Number:	
Date: 2013-02-23 15:47:33	Sheet: 7/15

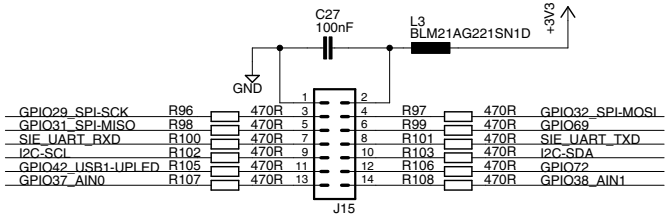
Analog input, Push-button, Joystick and Serial Expansion Connector and Jennic RF-module

Analog Input



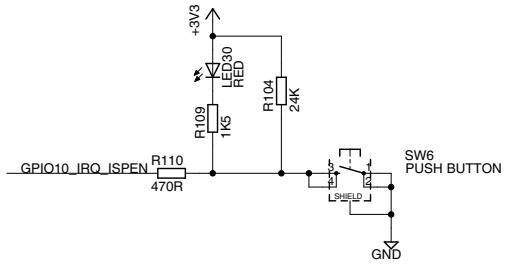
Serial Expansion Connector

1: GND	2: VCC (3.3V, max 250mA)
3: SPI-SCK (output)	4: SPI-MOSI (output)
5: SPI-MISO (input)	6: SPI-SSEL (output)
7: UART-RX (input)	8: UART-TX (output)
9: I2C-SCL (output)	10: I2C-SDA (bidirectional)
11: GPIO (bidirectional)	12: GPIO (bidirectional)
13: AIN0/GPIO (input)	14: AIN1/GPIO (input)

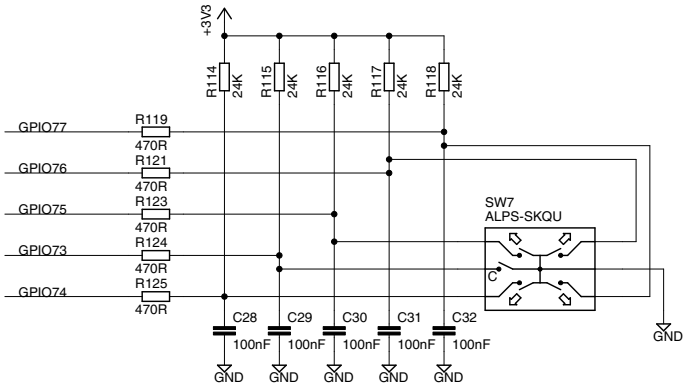


Interrupt Push-button

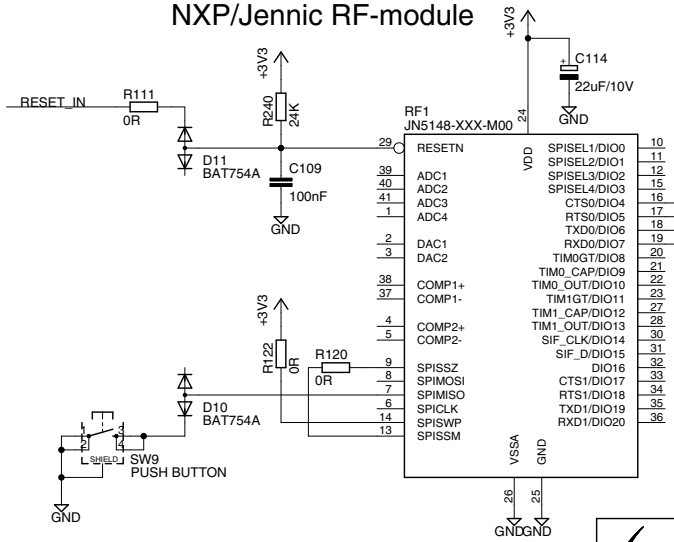
P2.10 on LPC1788/2478/4088
GPI_01 on LPC3250
ISP_EN on LPC4357



5-key Joystick Switch

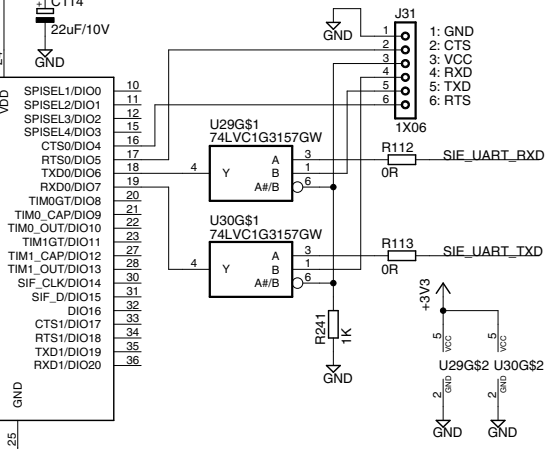


NXP/Jennic RF-module



Press key during reset to enter firmware download mode.

FTDI UART Connector



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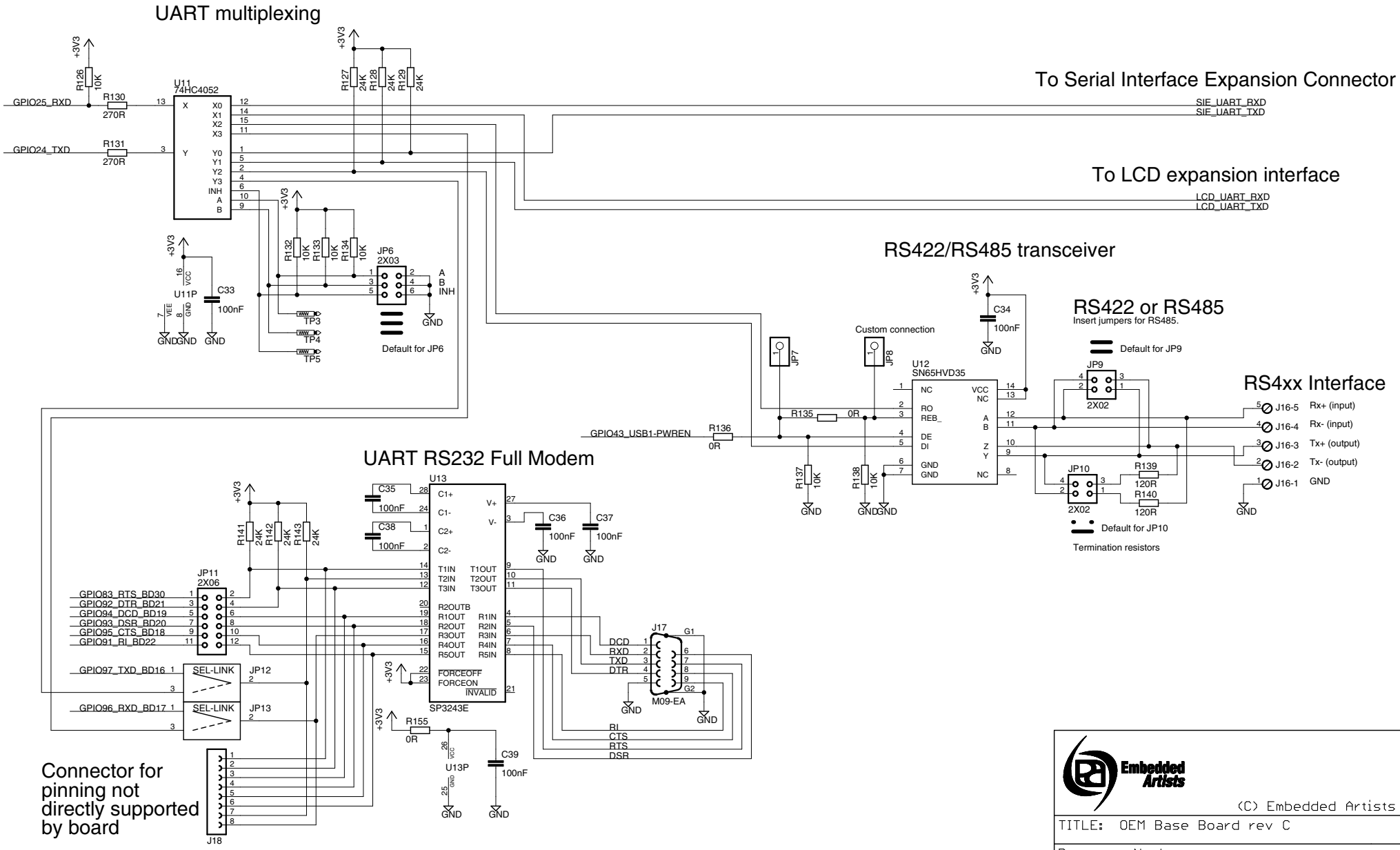
TITLE: OEM Base Board rev C	
Document Number:	
Date: 2013-02-23 15:47:33	Sheet: 8/15

UART multiplexing, RS232 and RS422/485 interface



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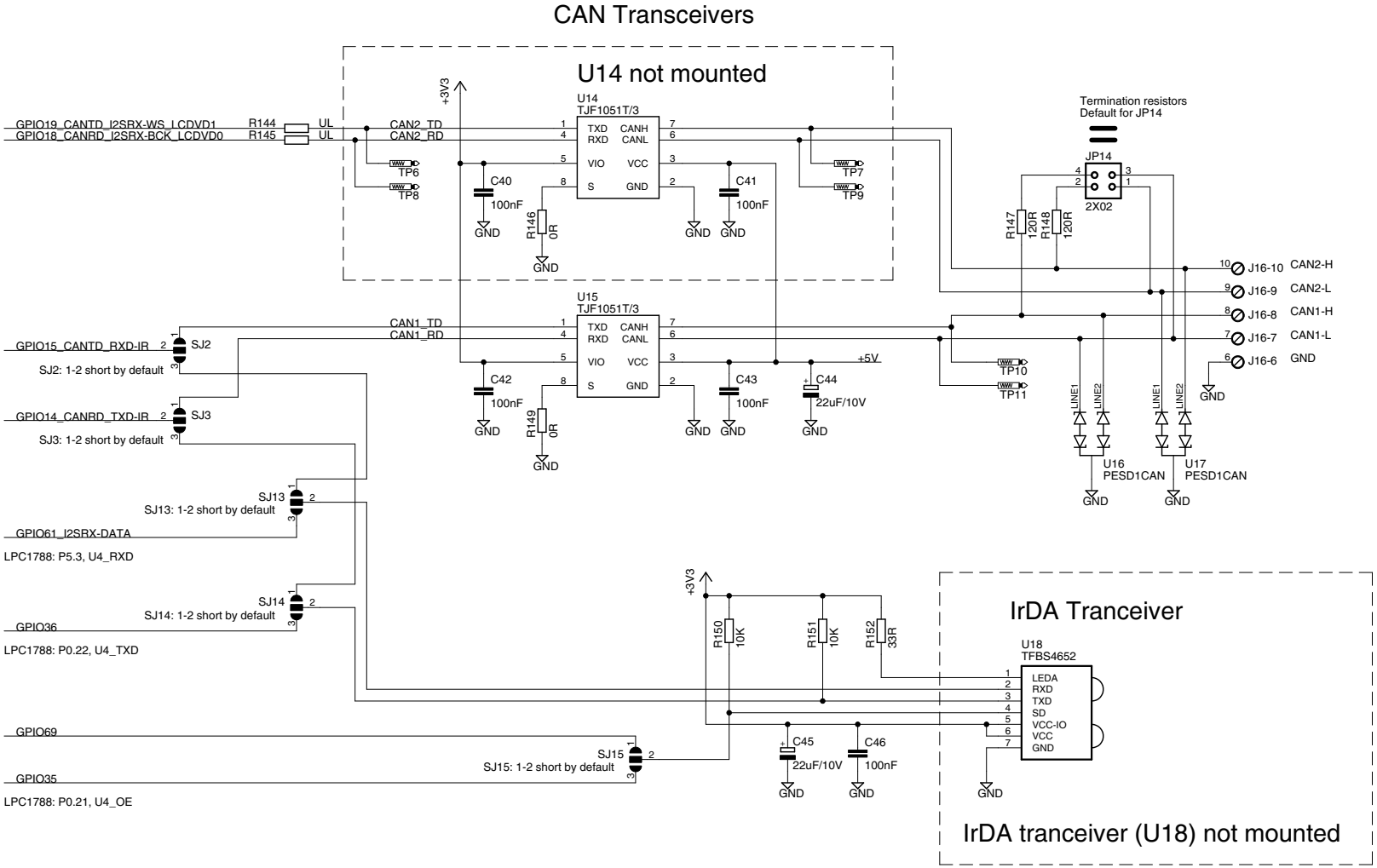
TITLE: OEM Base Board rev C	
Document Number:	
Date: 2013-02-23 15:47:33	Sheet: 9/15

CAN and IrDA interfaces



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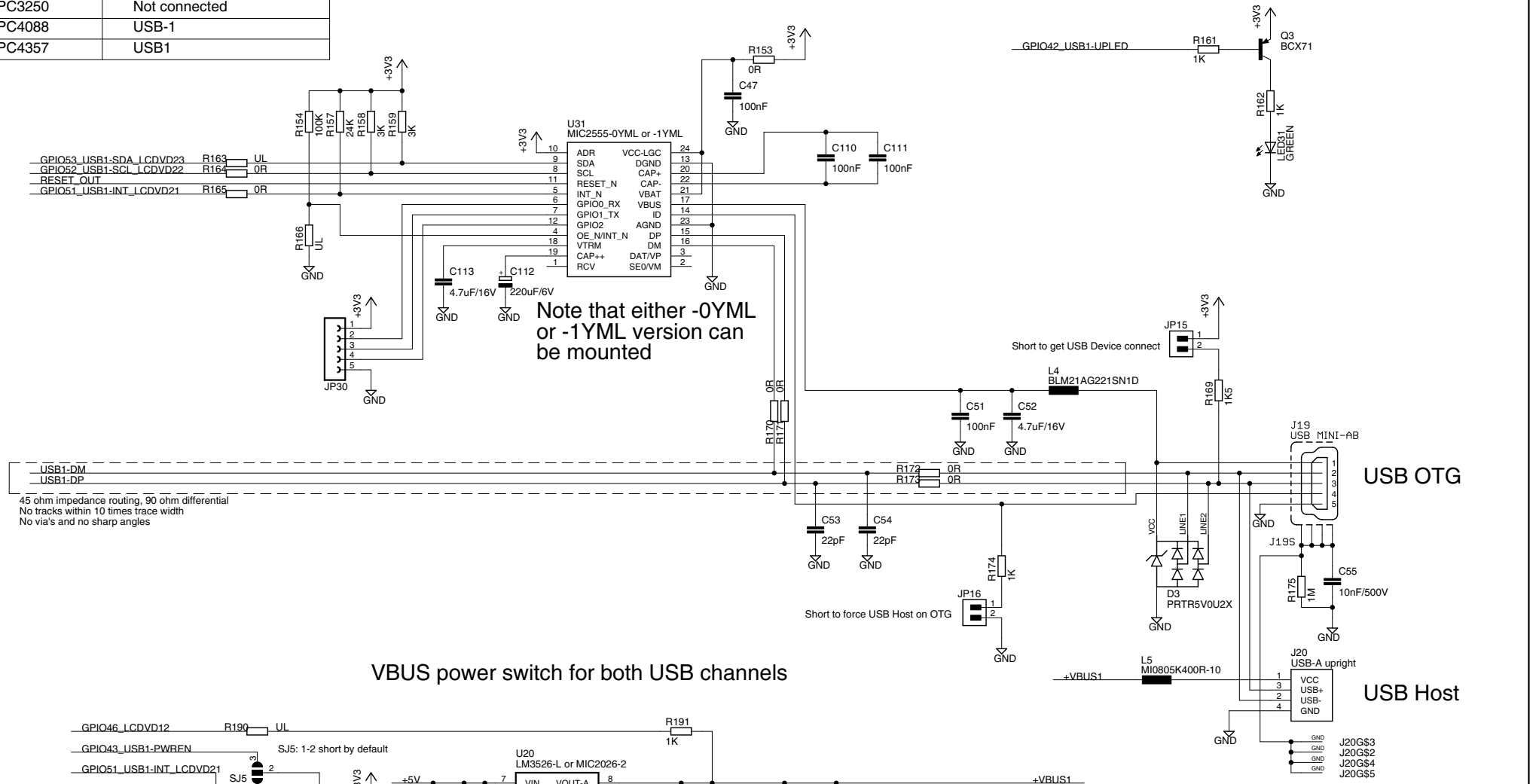
TITLE: OEM Base Board rev C

Document Number:

Date: 2013-02-23 15:47:33 | Sheet: 10/15

OEM Board	USB interface on chip
LPC1788	USB-1
LPC2478	USB-1
LPC3250	Not connected
LPC4088	USB-1
LPC4357	USB1

USB-1 interface: OTG and Host



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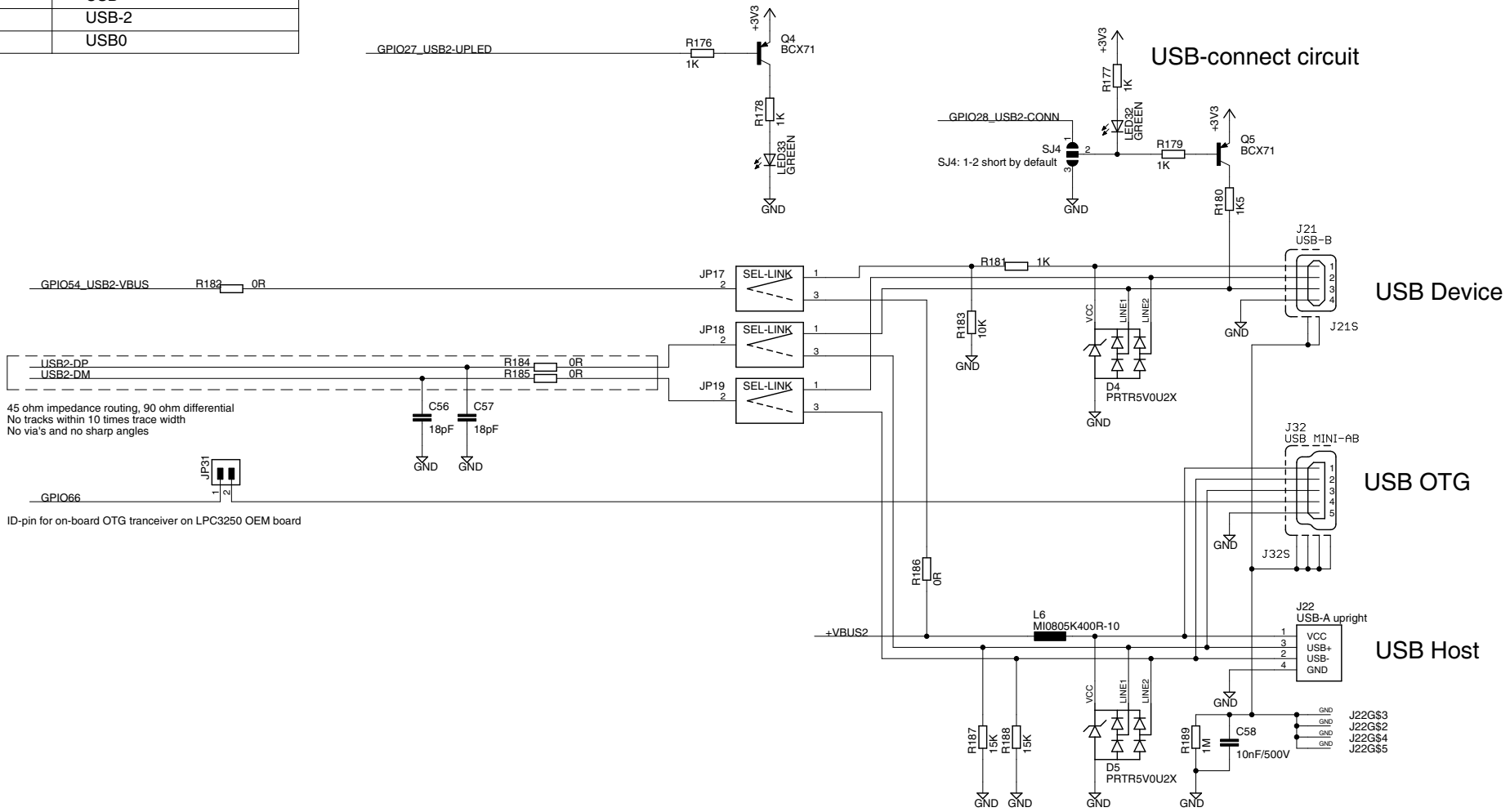
TITLE: OEM Base Board rev C

Document Number:

Date: 2013-02-23 15:47:33 Sheet: 11/15

OEM Board	USB interface on chip
LPC1788	USB-2
LPC2478	USB-2
LPC3250	USB
LPC4088	USB-2
LPC4357	USB0

USB-2 interface: Host or Device or OTG



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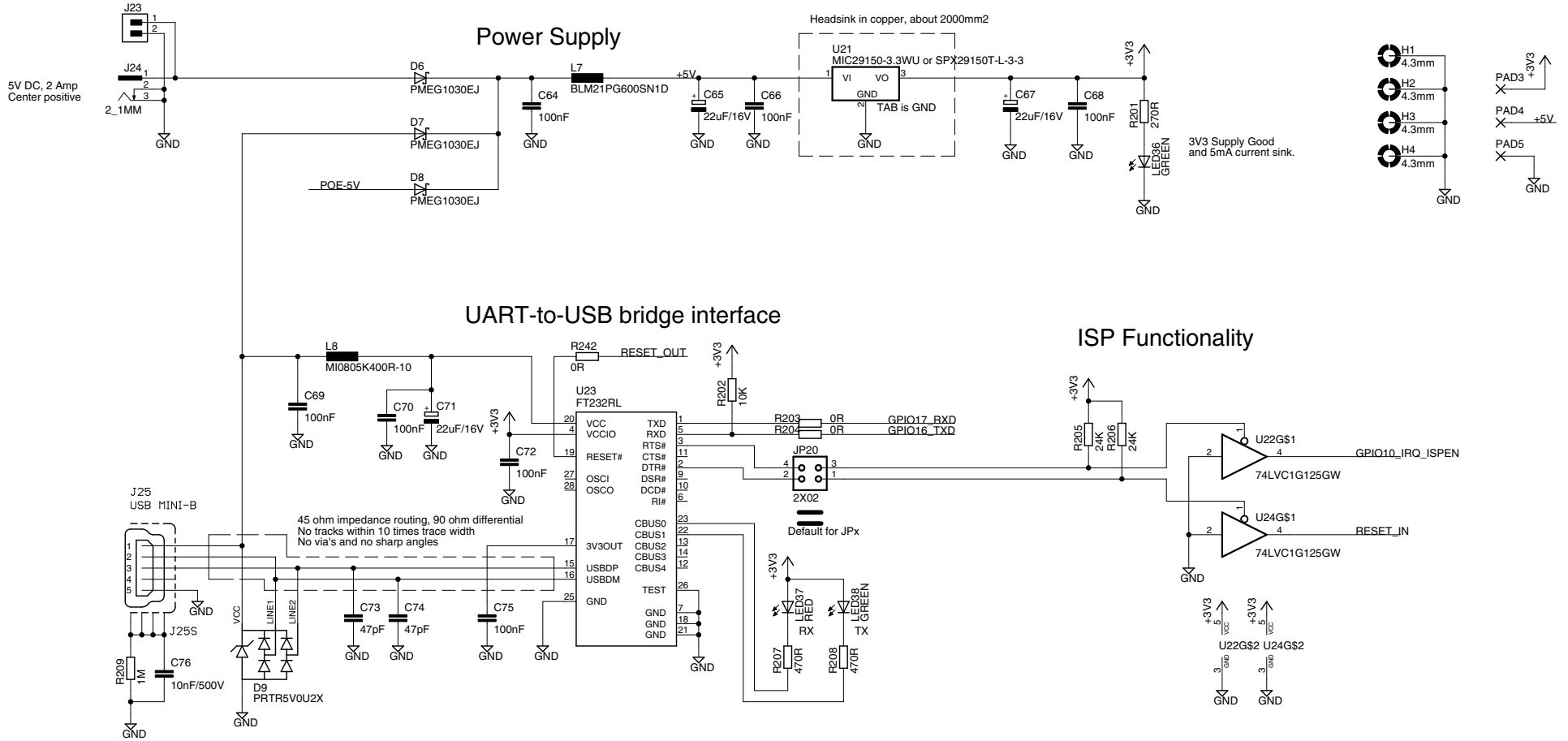
TITLE: OEM Base Board rev C

Document Number:

Date: 2013-02-23 15:47:33 | Sheet: 12/15

Power supply and UART-to-USB bridge



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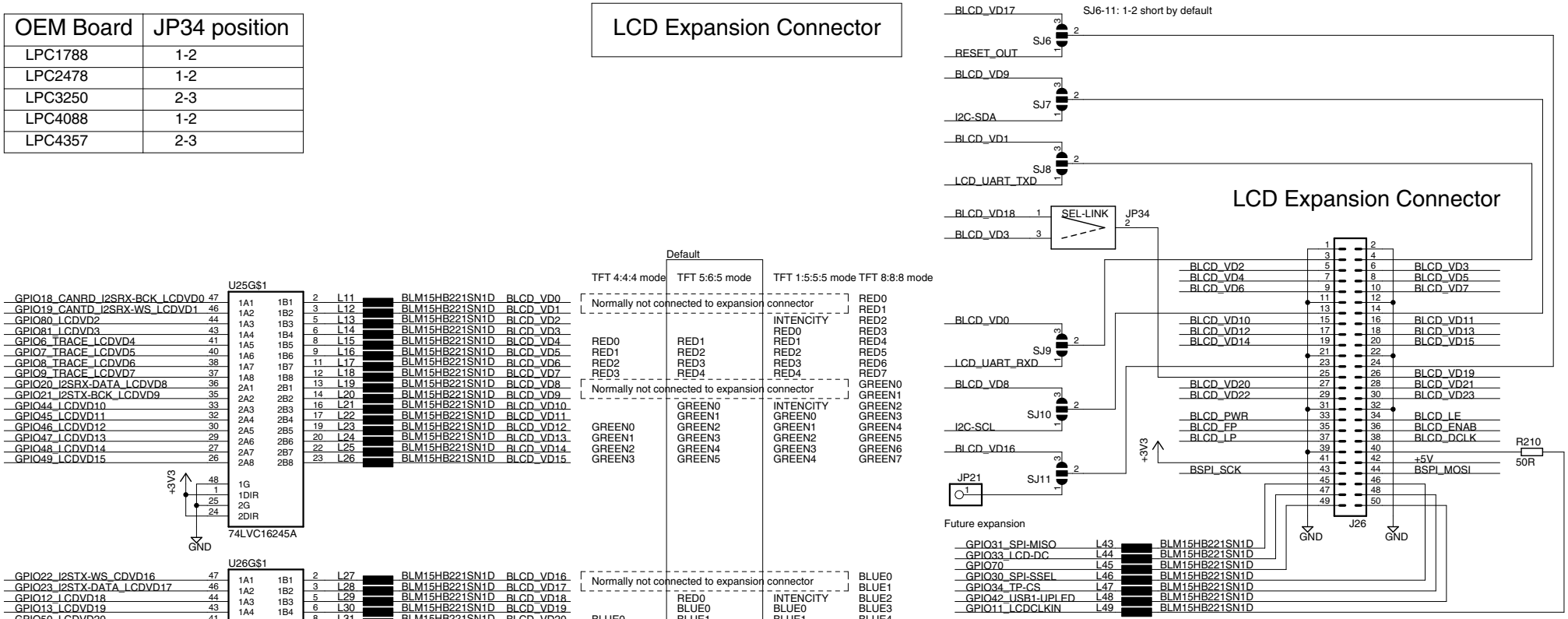
TITLE: OEM Base Board rev C	
Document Number:	
Date: 2013-02-23 15:47:33	Sheet: 13/15

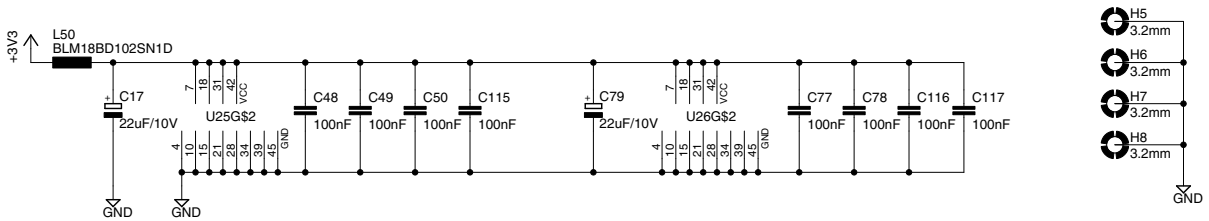
OEM Board	JP34 position
LPC1788	1-2
LPC2478	1-2
LPC3250	2-3
LPC4088	1-2
LPC4357	2-3

LCD Expansion Connector



GND	GND
UART-RXD (alt BLCD_VD0)	UART-TXD (alt BLCD_VD1)
BLCD_VD2	BLCD_VD3
BLCD_VD4	BLCD_VD5
BLCD_VD6	BLCD_VD7
GND	GND
I2C-SCL (alt BLCD_VD8)	I2C-SDA (alt BLCD_VD9)
BLCD_VD10	BLCD_VD11
BLCD_VD12	BLCD_VD13
BLCD_VD14	BLCD_VD15
GND	GND
SPARE (alt BLCD_VD16)	RESET (alt BLCD_VD17)
BLCD_VD18	BLCD_VD19
BLCD_VD20	BLCD_VD21
BLCD_VD22	BLCD_VD23
GND	GND
LCD_PWR	LCD_LE
LCD_FP	LCD_ENAB
LCD_LP	LCD_DCLK
GND	LCD_CLKIN
+3V3	+5V
SPI-SCK	SPI-MOSI
SPI-MISO	SPI-SSEL0
SPI-GPIO	SPI-SSEL1
IRQ	BL-CTRL

Mounting holes for LCD



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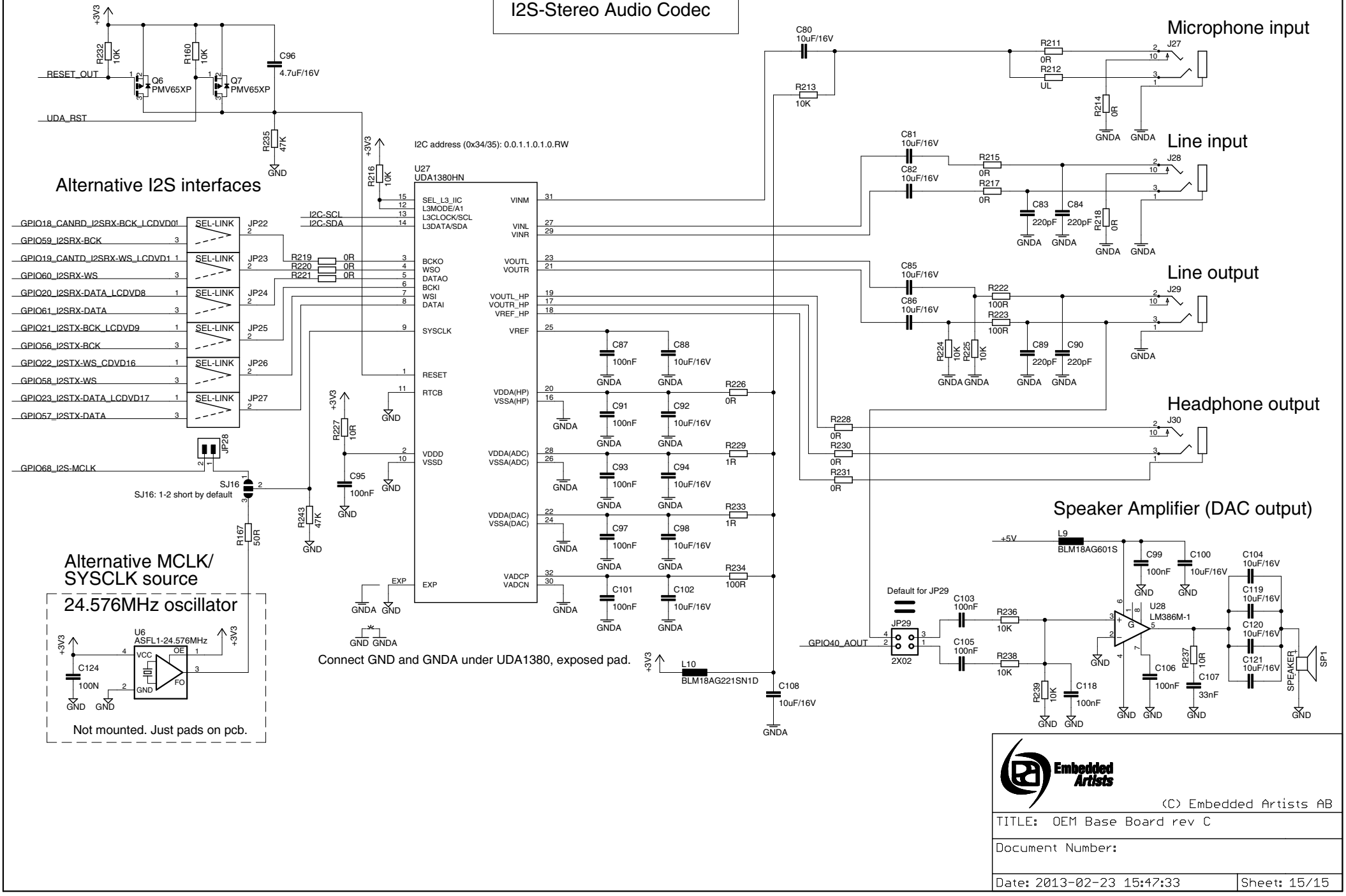
TITLE: OEM Base Board rev C

Document Number:

Date: 2013-02-23 15:47:33 | Sheet: 14/15

I2S-Stereo Audio Codec