

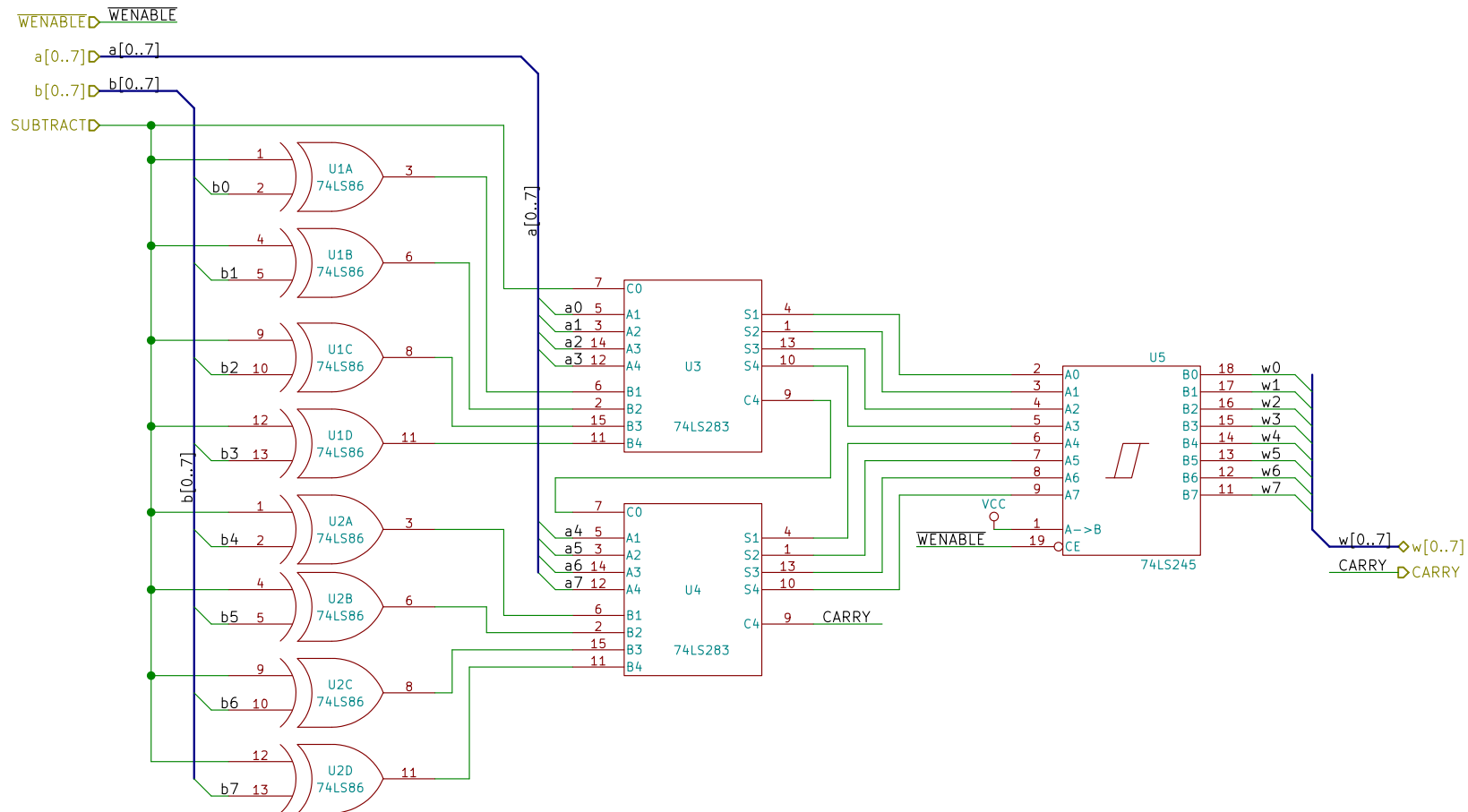
Main w bus and top level view
 Schematic capture by Vincent Sanders
 Schematic released as CC BY-SA

Sheet: /
 File: sap-be.sch

Title: Simple As Possible computer by Ben Eater

Size: A4 Date: 2017-05-10
 KiCad E.D.A. kicad 4.0.5+dfsg1-4bpo8+1

Rev: 0
 Id: 1/11



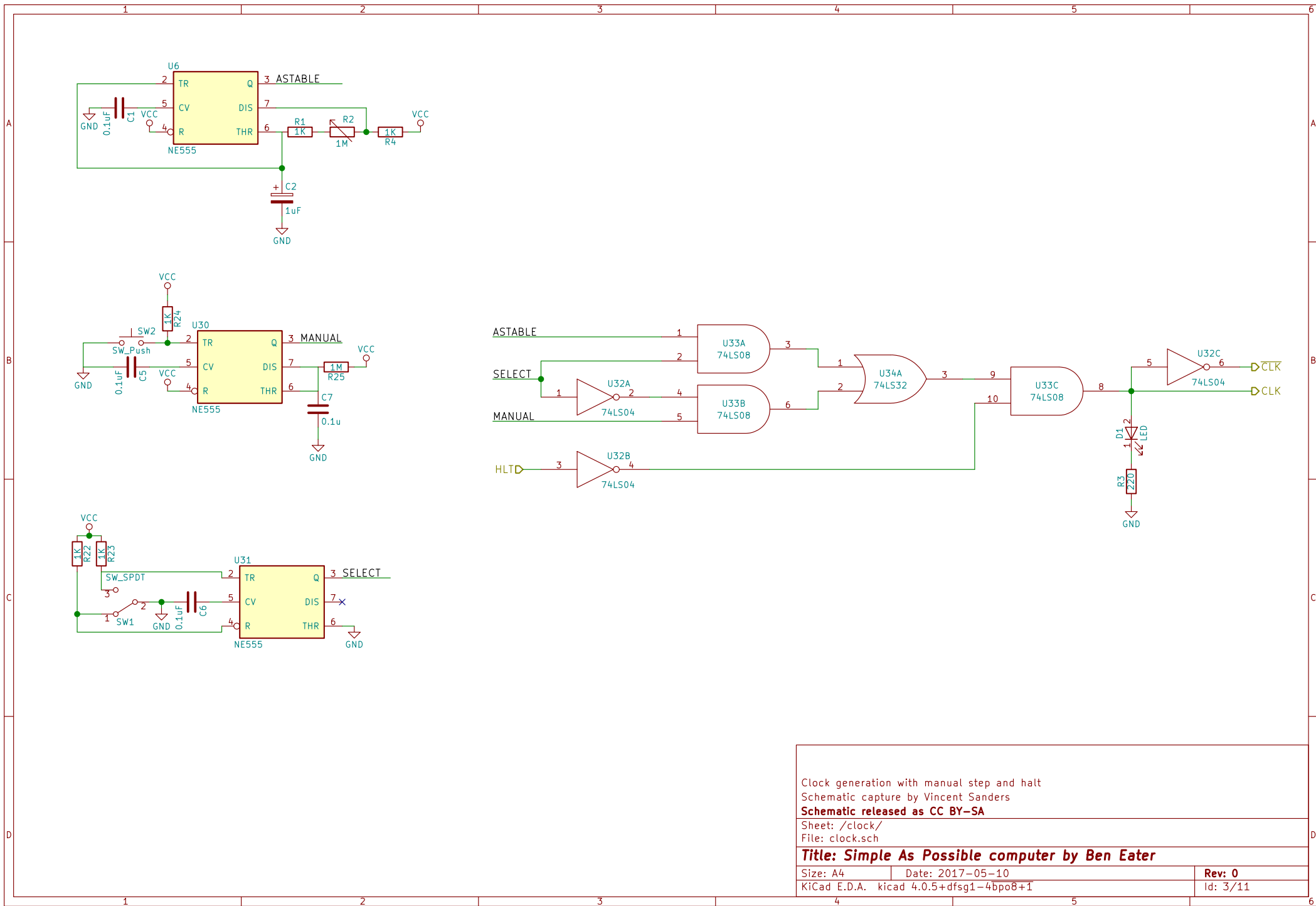
Arithmetic Logic Unit
 Schematic capture by Vincent Sanders
Schematic released as CC BY-SA

Sheet: /ALU/
 File: ALU.sch

Title: Simple As Possible computer by Ben Eater

Size: A4 Date: 2017-05-10
 KiCad E.D.A. kicad 4.0.5+dfsg1-4bpo8+1

Rev: 0
 Id: 2/11



Clock generation with manual step and halt

Schematic capture by Vincent Sanders

Schematic released as CC BY-SA

Sheet: /clock/

File: clock.sch

Title: Simple As Possible computer by Ben Eater

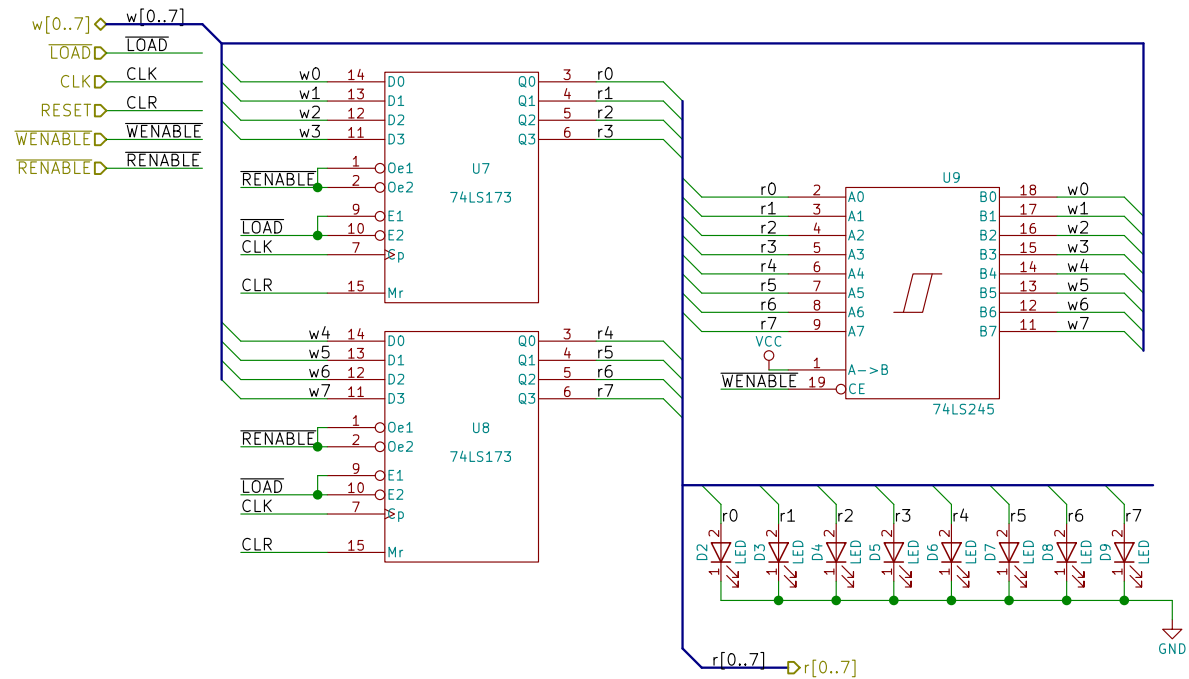
Size: A4

Date: 2017-05-10

Rev: 0

KiCad E.D.A. kicad 4.0.5+dfsg1-4bpo8+1

Id: 3/11



Schematic capture by Vincent Sanders

Schematic released as CC BY-SA

Sheet: /RegisterB/

File: register.sch

Title: Simple As Possible computer by Ben Eater

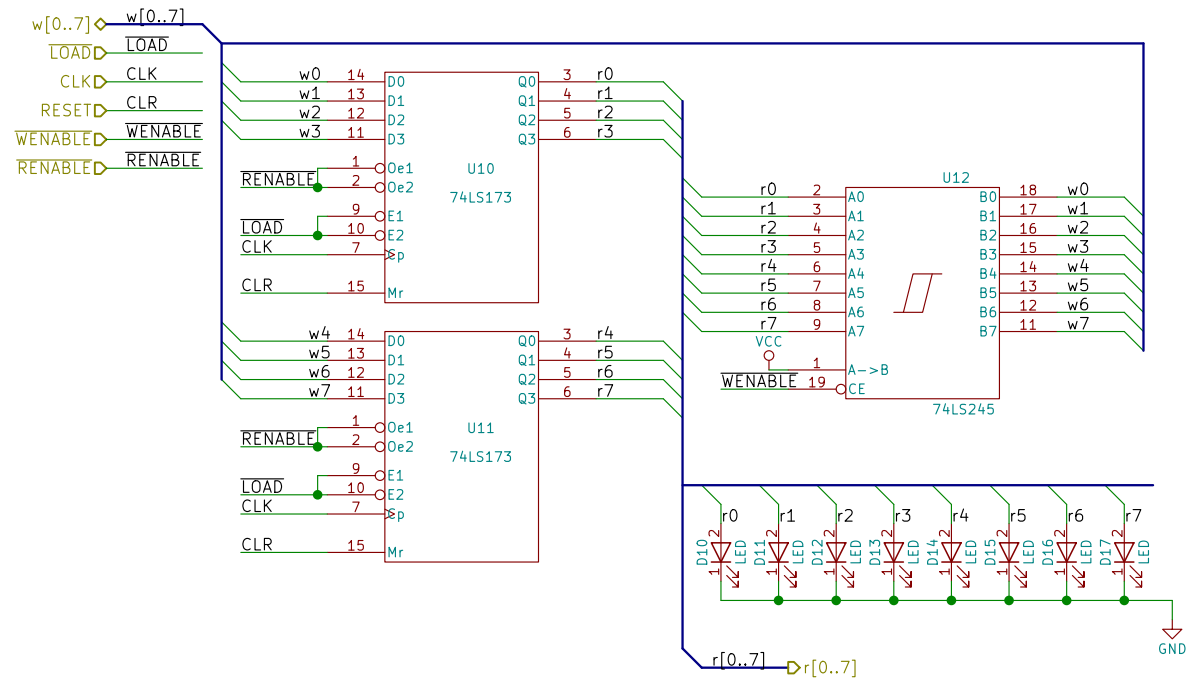
Size: A4

Date: 2017-05-10

Rev: 0

KiCad E.D.A. kicad 4.0.5+dfsg1-4bpo8+1

Id: 4/11



Schematic capture by Vincent Sanders

Schematic released as CC BY-SA

Sheet: /RegisterA/

File: register.sch

Title: Simple As Possible computer by Ben Eater

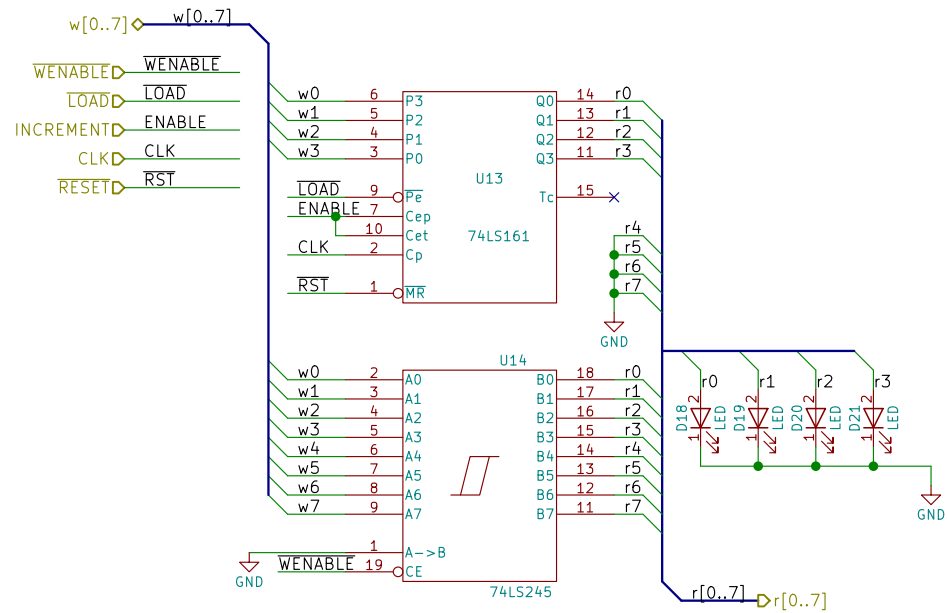
Size: A4

Date: 2017-05-10

Rev: 0

KiCad E.D.A. kicad 4.0.5+dfsg1-4bpo8+1

Id: 5/11



Schematic capture by Vincent Sanders

Schematic released as CC BY-SA

Sheet: /PC/

File: programcounter.sch

Title: Simple As Possible computer by Ben Eater

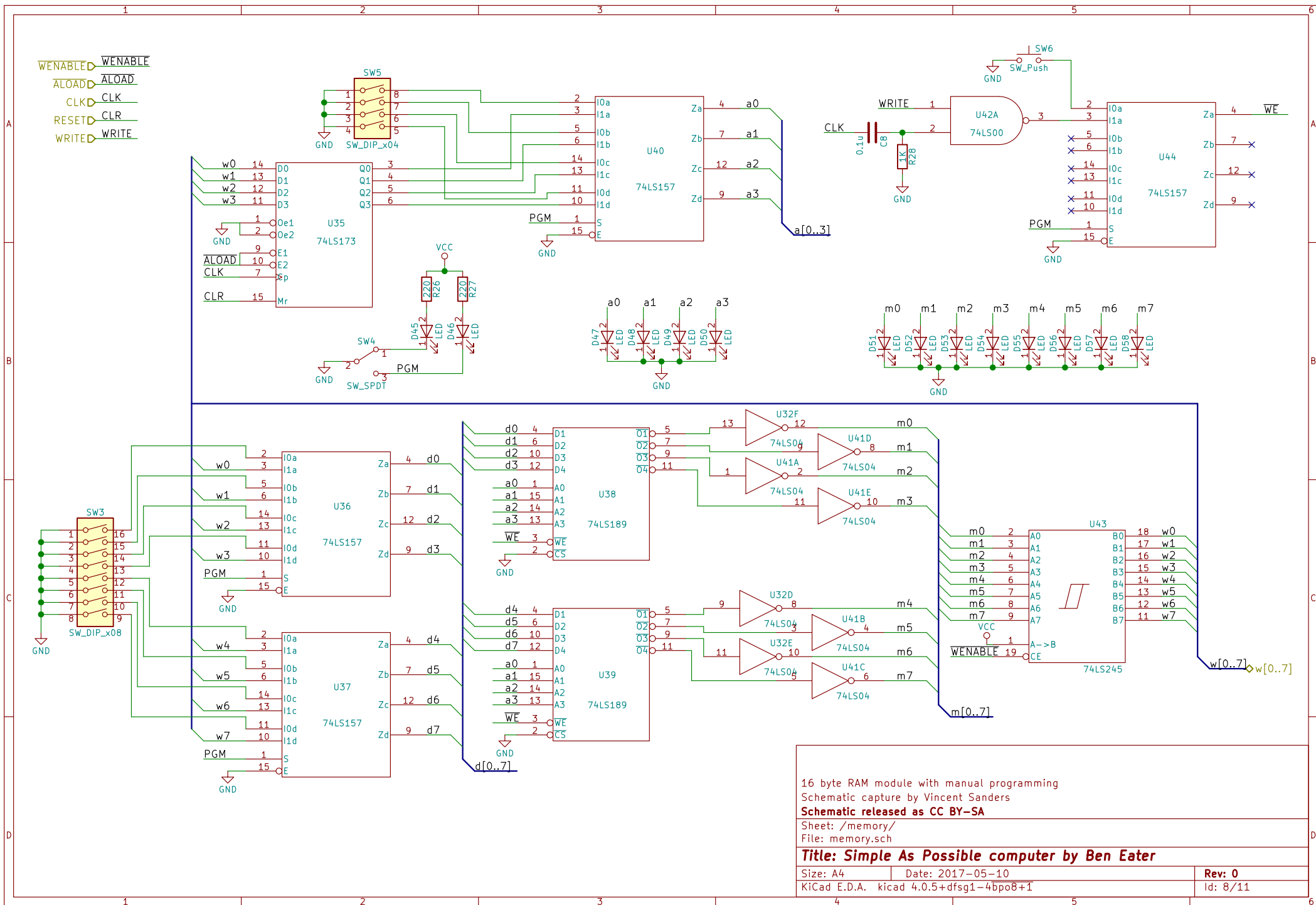
Size: A4

Date: 2017-05-10

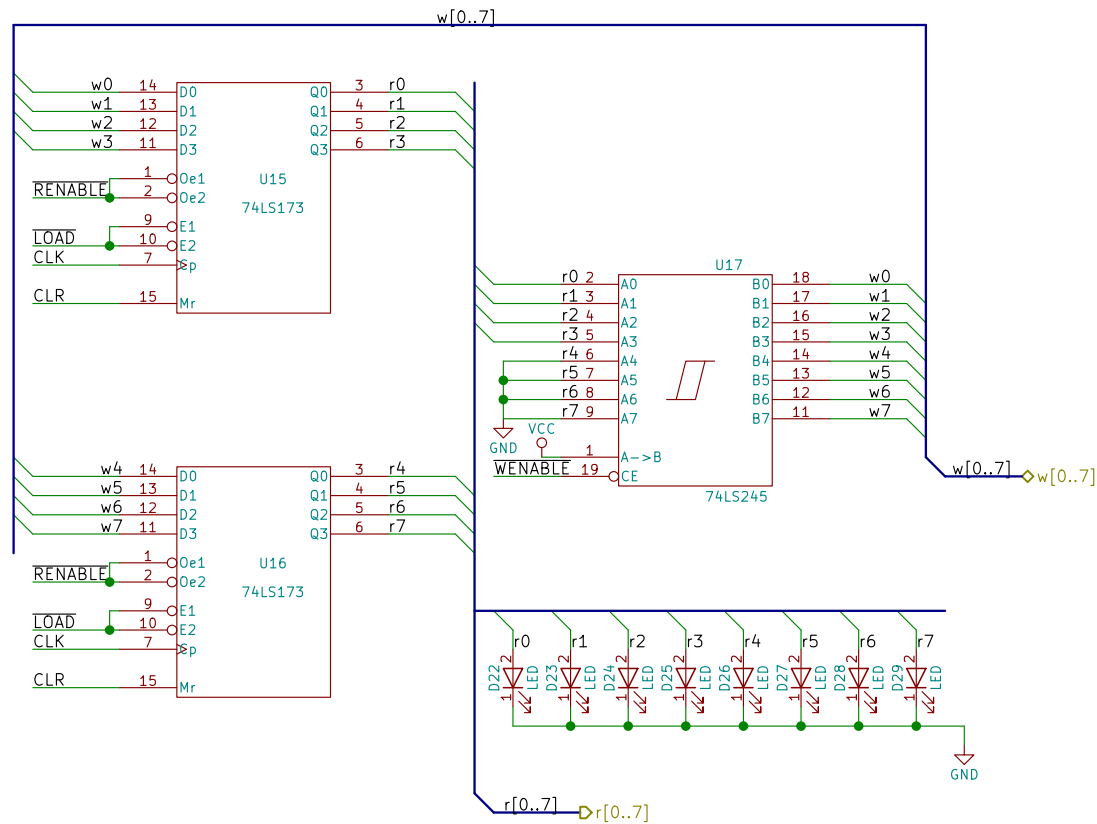
Rev: 0

KiCad E.D.A. kicad 4.0.5+dfsg1-4bpo8+1

Id: 6/11



LOAD \triangleright LOAD
 CLK \triangleright CLK
 RESET \triangleright CLR
 WENABLE \triangleright WENABLE
 RENABLE \triangleright RENABLE



Schematic capture by Vincent Sanders

Schematic released as CC BY-SA

Sheet: /IR/

File: instructionregister.sch

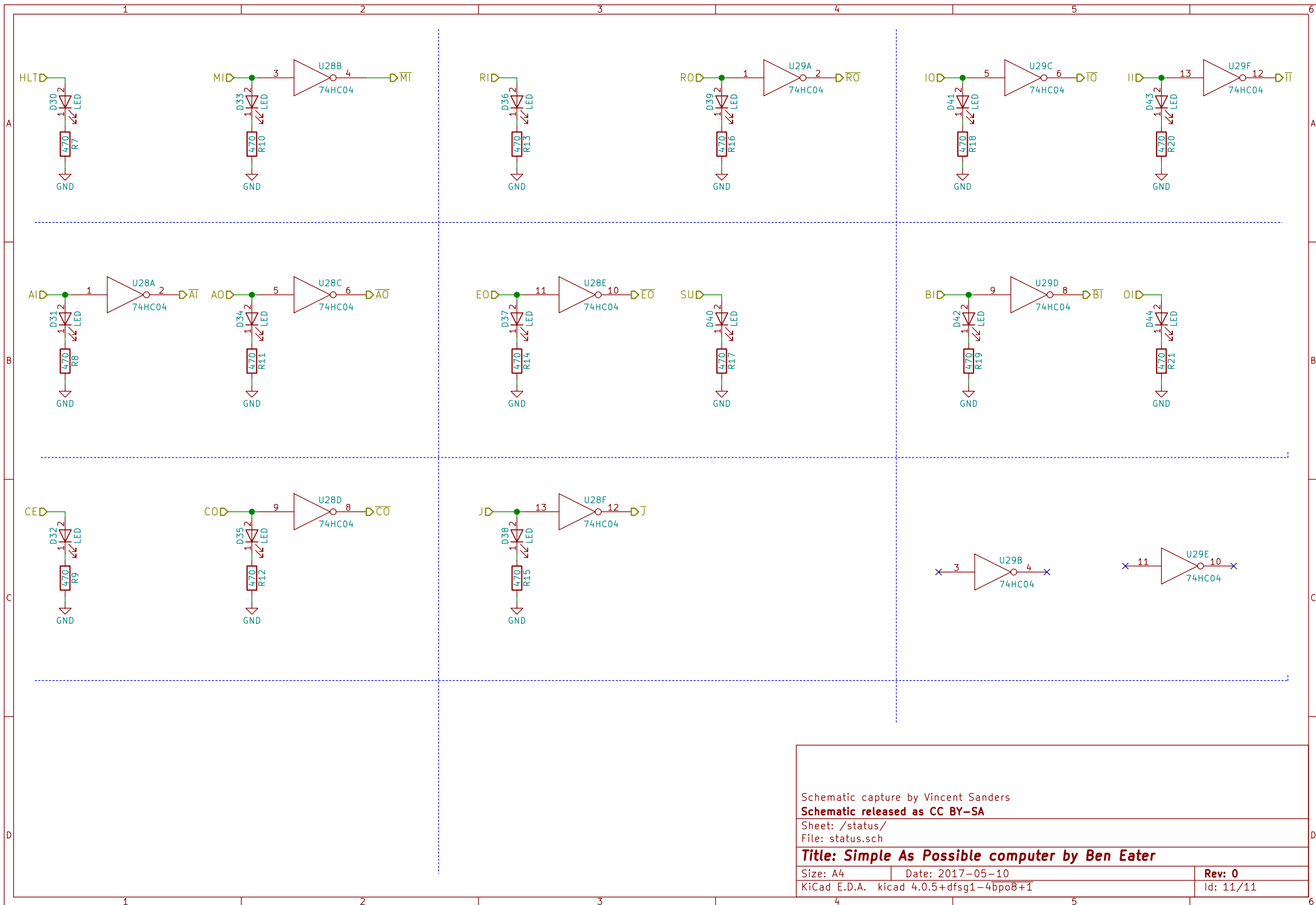
Title: Simple As Possible computer by Ben Eater

Size: A4 Date: 2017-05-10

KiCad E.D.A. kicad 4.0.5+dfsg1-4bpo8+1

Rev: 0

Id: 10/11



Schematic capture by Vincent Sanders

Schematic released as CC BY-SA

Sheet: /status/

File: status.sch

Title: Simple As Possible computer by Ben Eater

Size: A4

Date: 2017-05-10

Rev: 0

KiCad E.D.A. kicad 4.0.5+dfsg1-4bpo8+1

Id: 11/11