EE 435 Homework 2 Spring 2024

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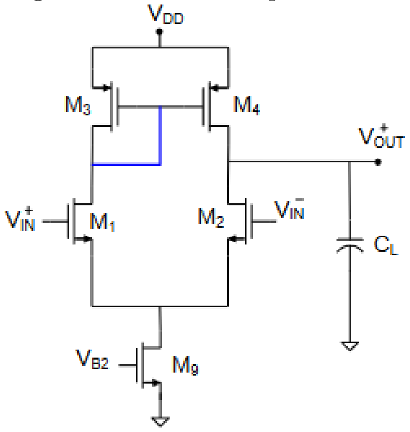
GitHub Page

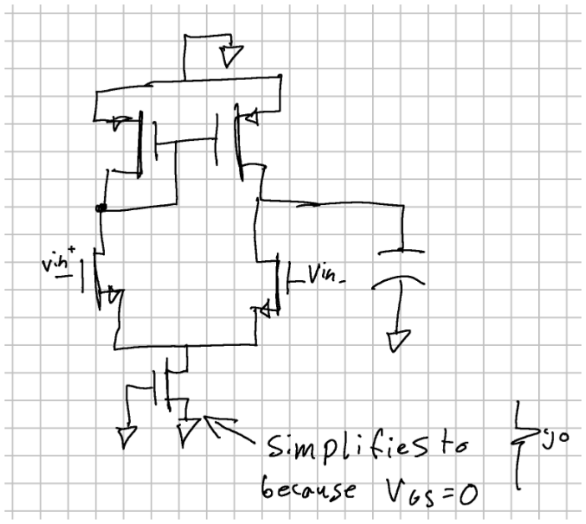
Problem 1 and 2

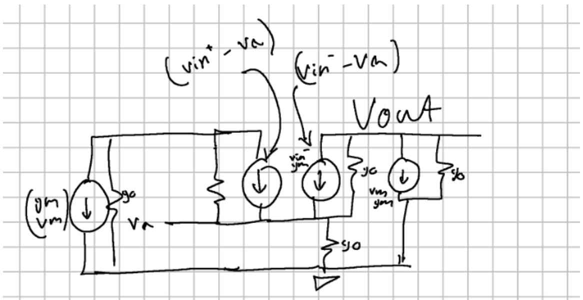
Consider the following operational amplifier. The goal is to obtain an expression for the small-signal output voltage in terms of the input variables INV+ and INV-.



a)

Write a complete set of small-signal equations that can be solved to obtain V_{OUT}. Assume the small-signal parameter g_o is present in all MOS devices.





$$\frac{V_{out}}{g_o} + (V_m) g_m - \frac{V_{out} - V_a}{g_o} - (V_{in^-} g_m) = 0$$

$(v_{in}^- - V_a)$

$$\boxed{1} \quad \frac{V_{out}}{g_o} + V_m g_m = \left(\frac{V_{out} - V_a}{g_o} + (v_{in}^- - V_a) g_m \right)$$

$$\boxed{2} \quad \frac{V_m}{g_o} + V_m g_m = \left(\frac{V_m - V_a}{g_o} + (v_{in}^+ - V_a) g_m \right)$$

$$\frac{V_a}{g_o} = (v_{in}^+ - V_a) g_m + (v_{in}^- - V_a) g_m + \frac{V_m - V_a}{g_o} + \frac{V_{out} - V_a}{g_o}$$

$$\frac{V_a}{g_o} = \frac{V_m}{g_o} + V_m g_m + \frac{V_{out}}{g_o} + V_m g_m$$

$$\frac{V_a}{g_o} = \frac{V_m + V_{out}}{g_o} + 2(V_m)(g_m)$$

$$\boxed{3} \quad \boxed{V_a = V_m + V_{out} + 2(g_m)(V_m)(1/g_o)}$$

2 unknowns and 3 equations

$$\frac{V_{out}}{g_0} + (V_m)gm = \frac{V_{out} - V_a}{g_0} + V_{in-}g_m \tag{1}$$

$$\frac{V_{out}}{g_0}\frac{V_{out} - V_a}{g_0} = (V_m)gm + V_{in-}g_m \tag{2}$$

$$V_{out} * \frac{2}{g_0} = (V_{in-} + V_m - V_a)g_m + \frac{V_a}{g_0} \tag{3}$$

$$V_{out} = \frac{(V_{in-} + V_m - V_a)g_m}{2} + \frac{V_a}{2} \tag{4}$$

$$V_m = (\frac{-V_{out}}{g_0} + V_{in-}g_m) * \frac{1}{\frac{1}{g_0} - g_m + 2g_m^2 g_0} \tag{5}$$

$$V_a = V_m + V_{out} + 2(g_m)(V_m)(g_0) \tag{6}$$

$$V_a = V_{out} + V_m(1 + 2(g_m)(g_0)) \tag{7}$$

Now replace all V_a in the VOUT equation.

$$V_{out} = \frac{(V_{in-} + V_m - V_m + V_{out} + 2(g_m)(V_m)(g_0))g_m}{2} + \frac{V_m + V_{out} + 2(g_m)(V_m)(g_0)}{2} \tag{8}$$

$$V_{out}(\frac{1}{g_0} - \frac{1}{g_0}) = \frac{-V_a}{g_0} + V_{in-}g_m - (V_m)gm \tag{9}$$

b)

Solve these equations by hand for V_{OUT}. If you do not have a solution at the end of $\frac{1}{2}$ hour, stop, and comment on your progress and the amount of effort that you believe would be required to finish the solution.

c)

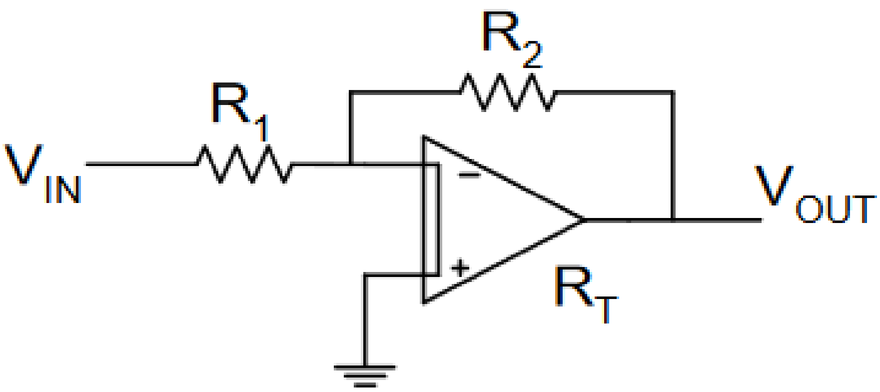
Obtain a parametric (symbolic) solution for the transfer function V_{OUT}/V_{IN} from this set of equations with MATLAB. How many total product terms appear in this solution? In this part, $V_{IN} = V_{IN+} - V_{IN-}$.

d)

Simplify the solution obtained with MATLAB under the assumption that all g_o terms are small compared to g_m terms.

Problem 3

A transresistance amplifier with a gain R_T is shown. Derive an expression for the voltage gain of the amplifier as a function of the transresistance gain R_T and determine what that reduces to if R_T is very large.



In this circuit we are using a transresistance amplifier which has a gain of $V_{out} = R_f * i_{in}$. The i_{in} for this circuit is $\frac{V_{in}}{R_1}$ because V_{neg} is a virtual ground. We can then do a KVL.

$$\frac{V_{in}}{R_1} + \frac{V_{out}}{R_2} - i_{in} = 0 \tag{1}$$

$$\frac{V_{in}}{R_1} + \frac{V_{out}}{R_2} - \frac{V_{out}}{(}R_f) = 0 \tag{2}$$

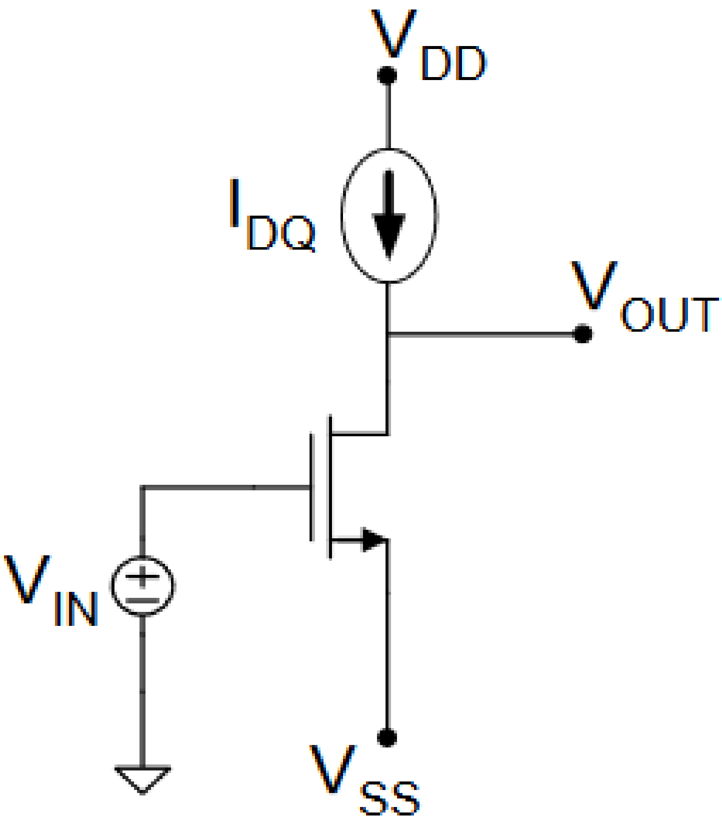
$$\frac{V_{in}}{R_1} = V_{out} * \frac{R_2 - R_f}{R_2 * R_f} \tag{3}$$

$$\frac{V_{out}}{V_{in}} = \frac{R_2 * R_f}{R_1(R_2 - R_f)} \tag{4}$$

$$lim_{R_f \to \infty} \frac{R_2 * R_f}{R_1(R_2 - R_f)} = \frac{R_2}{R_1} \tag{5}$$

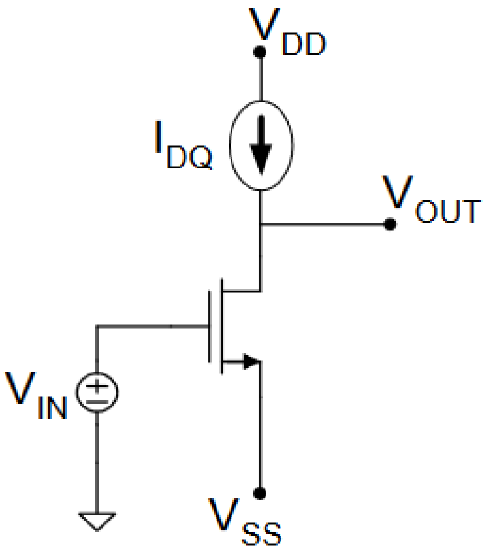
Problem 4

Assume the amplifier shown below is designed in a 0.18μ CMOS process. Assume also that $V_{DD} = 1V$, $V_{SS} = -1V$, and $I_{DQ} = 4mA$.



a)

Analytically determine the W and L needed to establish a quiescent output voltage of 0.5V when $V_{INQ} = 0.5V$.



Since we are using a mosfet in linear (non saturation region. We can use the following equations:

$$i_d = K * (2(V_{SG} - V_{TP})V_{SD} - V_{SD}^2) \tag{1}$$

Where

$$K = \frac{1}{2}(\mu_n C_{ox})\frac{W}{L} \tag{2}$$

$$V_{out} = V_{DS} = 0.5V \tag{3}$$

$$V_{in} = V_{GS} = 0.5V \tag{4}$$

$$i_d = K * (2(-0.5 - V_{TP})(-0.5V) - (0.5V)^2) \tag{5}$$

b)

Verify the transfer characteristics by Spice simulation.

c)

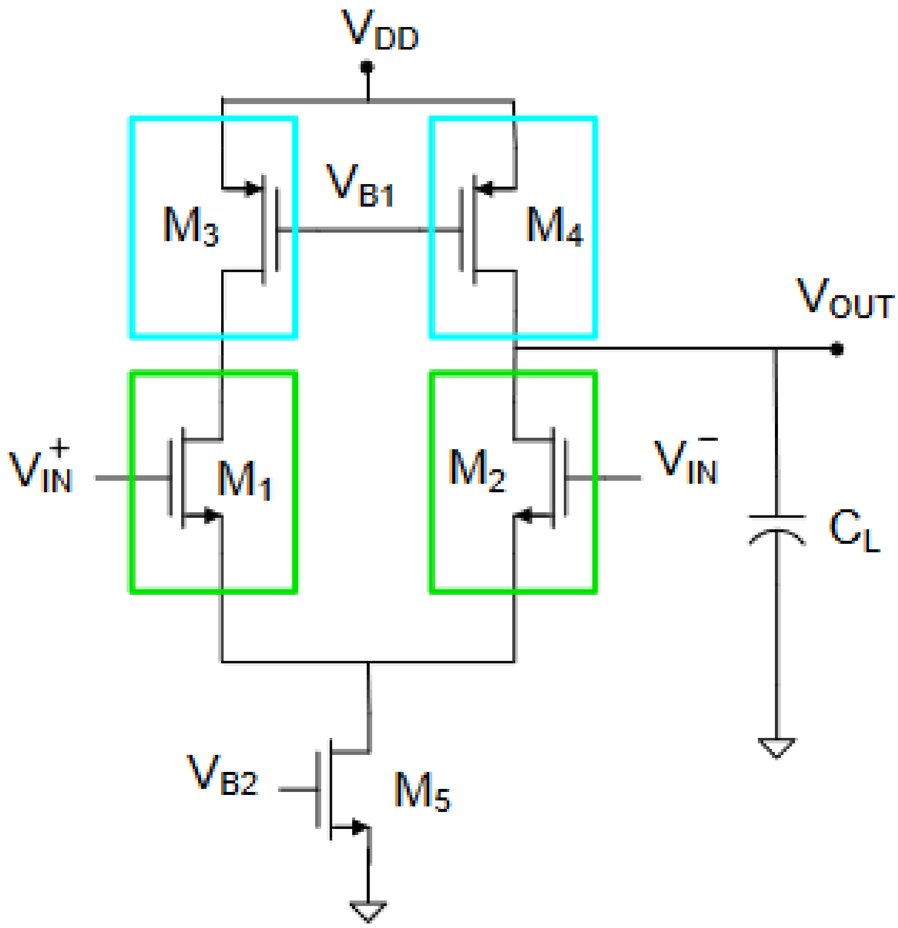
Analytically determine the dc voltage gain at the Q-point established in a).

d)

Using SPICE, obtain a plot of the small signal voltage gain versus the quiescent output voltage.

Problem 5 and 6

Design a 5T op amp to have a dc gain of 50dB and a GB of 2MHz in the ON 0.5μm CMOS process. Assume $V_{DD} = 3.5V$ and $C_L = 1pF$. Assume also that the bias voltages V_{B1} and V_{B2} can be precisely set so that a CMFB circuit is not needed. Verify the gain and the GB of your design with a SPICE simulation.

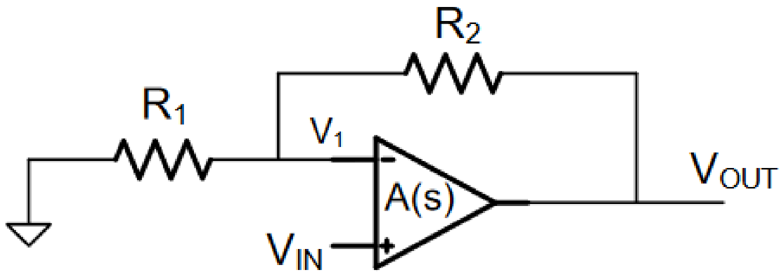


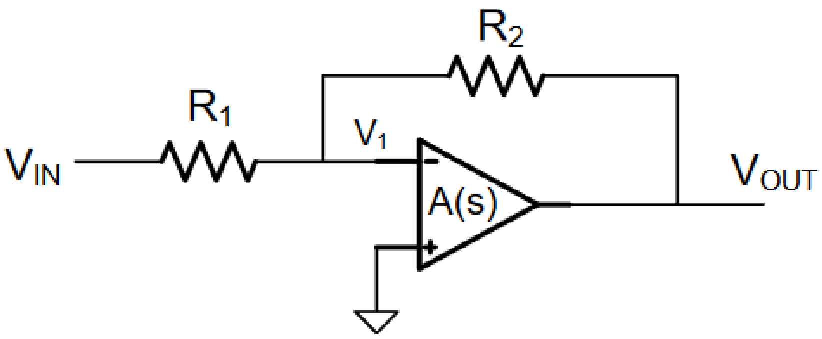
Problem 7

Determine the common-mode input range and the output signal swing of the amplifier you designed in the previous problem.

Problem 8

Assume that the op amp is a single-pole amplifier with gain given by the expression $A(s) = \frac{A_{GB}}{1+\frac{s}{\omega_A}}$, where the gain-bandwidth product of the op amp is $\omega_A A_{GB} = \omega$. Assuming that the frequency-dependent gain of the op amp can be modeled as $GBA(s) = \frac{A_{GB}}{s}$, determine the transfer function $\frac{V_{OUT}}{V_{IN}}$ for the following two





amplifiers.

a)

$$\frac{V_{OUT}}{V_{IN}} = \frac{A_{GB}}{s} \times \frac{1}{1+\frac{1}{2}s}$$

b)

$$\frac{V_{OUT}}{V_{IN}} = \frac{A_{GB}}{s} \times \frac{1}{1+\frac{1}{10}s}$$

c)

With the same op amp model used in part a), analytically determine the 3dB bandwidth of the following two amplifiers.

d)

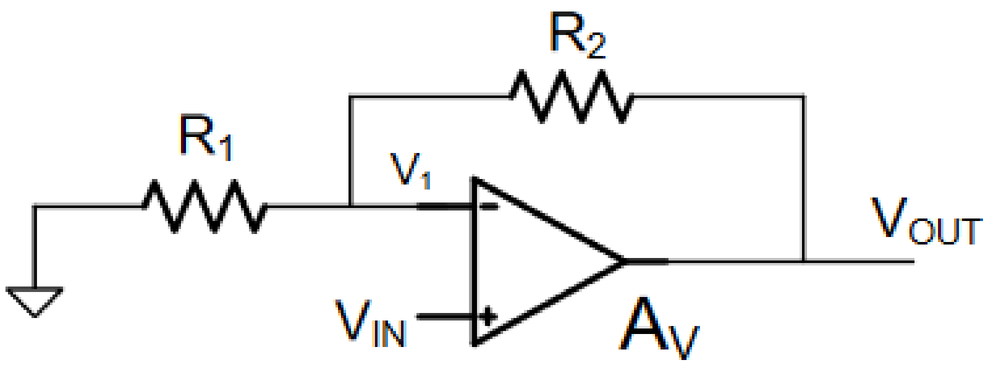
Derive the gain of the inverting feedback amplifier in terms of A_V and β and comment on why it does not look like the standard feedback equation.

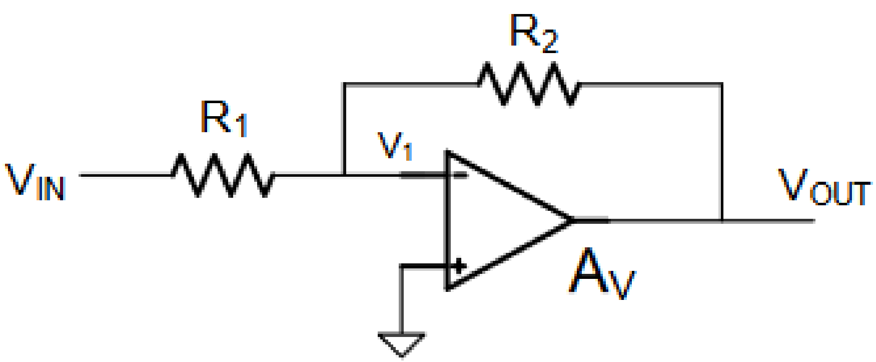
Problem 9

It was stated in class that all even-ordered distortion terms introduced by the amplifier vanish in symmetric fully differential amplifiers. Prove this fact.

Problem 10 (Extra Credit)

The "dead network" of a circuit is obtained by setting all small-signal inputs to 0. That is, by replacing all ac voltage sources with short circuits and all ac current sources with open circuits. The β of a feedback amplifier is a characteristic of the "dead network". Consider the basic inverting and noninverting feedback amplifiers shown below. These are widely used as small-signal voltage amplifiers.





a)

Show that they both have the same "dead network".

b)

The β of the two amplifiers shown is $\frac{1}{1+\frac{R_1}{R_2}}$. Show that the gain of the noninverting feedback amplifier can be expressed by the standard feedback equation

$$A = \frac{V_{OUT}}{V_{IN}} = \frac{1+\frac{R_2}{R_1}}{\frac{R_2}{R_1}}$$

c)

Take the limit as A_V goes to ∞ for the gain derived in part b) and compare with that derived in EE 230 for the gain of the noninverting feedback amplifier.

d)

Derive the gain of the inverting feedback amplifier in terms of A_V and β and comment on why it does not look like the standard feedback equation.