EE 435 Homework 4 Spring 2024

Jonathan Hess GitHub Page

Problem 1 and 2

The model parameters μ , C OX, VTH and λ are widely used in analytical formulations of the performance of analog circuits. These parameters are used to characterize how MOS transistors operate in the square-law model of the transistor. When operating in the saturation region, the square-law model for the drain current of transistors can be expressed as $I_D=$

Though this square-law formulation is a simplification, it gives reasonable results that can be used for much of the design process. Better and more comprehensive models, such as the BSIM models, are then used in computer simulations to more accurately predict performance and for refining the design to meet target specifications. The BSIM models, however, are not analytically tractable and thus unsuitable for analytical formulations. Though intermediate models that are more accurate than the square-law models and less comprehensive than the BSIM models exist, there is little evidence of analytical tractability for models with complexity beyond that of the square-law model. Many analog circuits are quite sensitive to the parameter λ in the square-law model. Unfortunately, the parameter λ is quite sensitive to device dimensions and operating point. As such, a table or plot of λ parameters is useful when using the square- law model for predicting the performance of many analog circuits based upon the square- law model. Generate plots of λ for both n-channel and p-channel transistors in the 0.18 μ m CMOS for different lengths and for three different values of W and for three different values of VDS as shown below. Comment on how λ varies with device dimensions and operating points. When determining λ , assume that the devices are modeled by the BSIM model which is embedded in the PDK file used in SPECTRE. Extract λ at a given operating point by taking two measurements (simulation results) of the drain current at values of VDS slightly above and slightly below the target VDS value on a constant VGS locus as shown in the plot below. This is likely how you extracted the parameter λ in EE 330. The length should vary between LMIN and 20LMIN and the VDS values should be from slightly above VEB to 2.5V.

http://class.ece.iastate.edu/ee435/miscHandouts/TSMC

http://class.ece.iastate.edu/ee330/lectures/EE