

# Implementation of CMOS based 1-bit Magnitude Comparator

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## Abstract

This paper presents the Implementation of CMOS based 1-bit Magnitude Comparator in 28nm technology. Design and Implementation will be done using Synopsys Custom Design Platform. Data comparison is needed in digital systems while performing arithmetic or logical operations. A single bit magnitude digital comparator is a combinational circuit that compares two digital or binary numbers in order to find out whether one binary number is equal, less than or greater than the other binary number. The complete design implementation is done using CMOS technology which has the features such as low static power consumption, temperature stability and stronger anti-noise ability.

## 1. Reference Circuit Details

A magnitude comparator is a hardware electronic device that takes two numbers as input in binary form and determines whether one number is greater than, less than or equal to the other number. The logical design of a circuit will have two inputs one for A and other for B and have three output terminals, one for  $A > B$  condition, one for  $A = B$  condition and one for  $A < B$  condition.

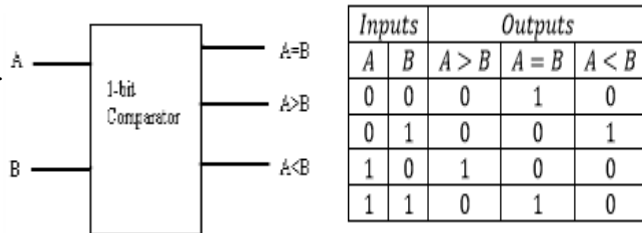


Fig 1. Truth Table

From the above truth table logical expressions for each output can be expressed as:

$$(A=B) = A'B' + AB = (AB' + A'B)'$$

$$(A>B) = AB' = (A'+B)'$$

$$(A<B) = A'B = (A+B')'$$

By using these Boolean expressions, we can implement a logic circuit for this comparator as:

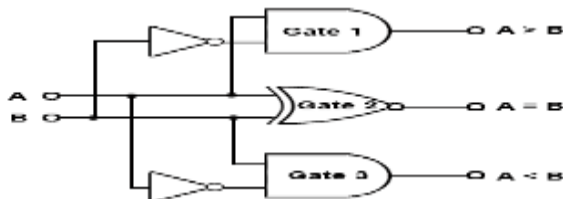


Fig 2. Combinational Circuit

## 2. Reference Circuit design

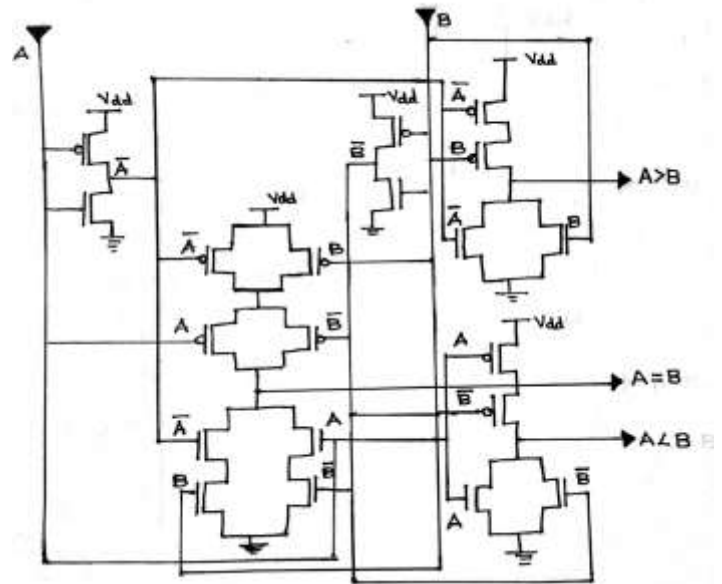


Fig 3. Reference Circuit Diagram (CMOS based 1-bit magnitude comparator)

## 3. Reference Circuit Waveforms

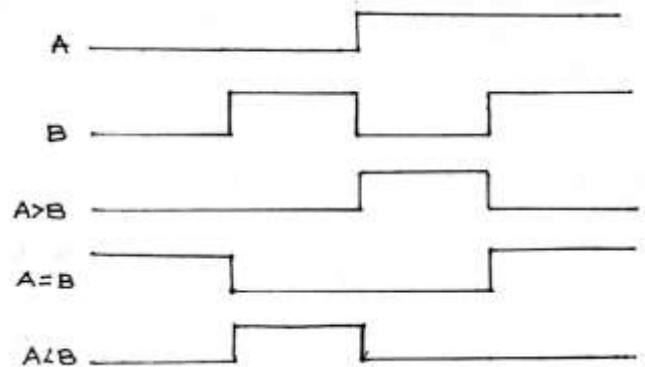


Fig 4. Reference Circuit Output Waveforms

## References

- 1.Improved Design of CMOS 1-Bit Comparator with Stacking Technique. Arvindkumar, Manoj Kumar. 2017 2nd International Conference on Telecommunication network.
- 2.[https://www.researchgate.net/publication/303805925\\_1\\_Bit\\_Comparator\\_CMOS\\_90nm\\_Layout\\_Design#:~:text=The%20comparator%20is%20a%20circuit,analog%2Dto%2Ddigital%20converter.](https://www.researchgate.net/publication/303805925_1_Bit_Comparator_CMOS_90nm_Layout_Design#:~:text=The%20comparator%20is%20a%20circuit,analog%2Dto%2Ddigital%20converter.)