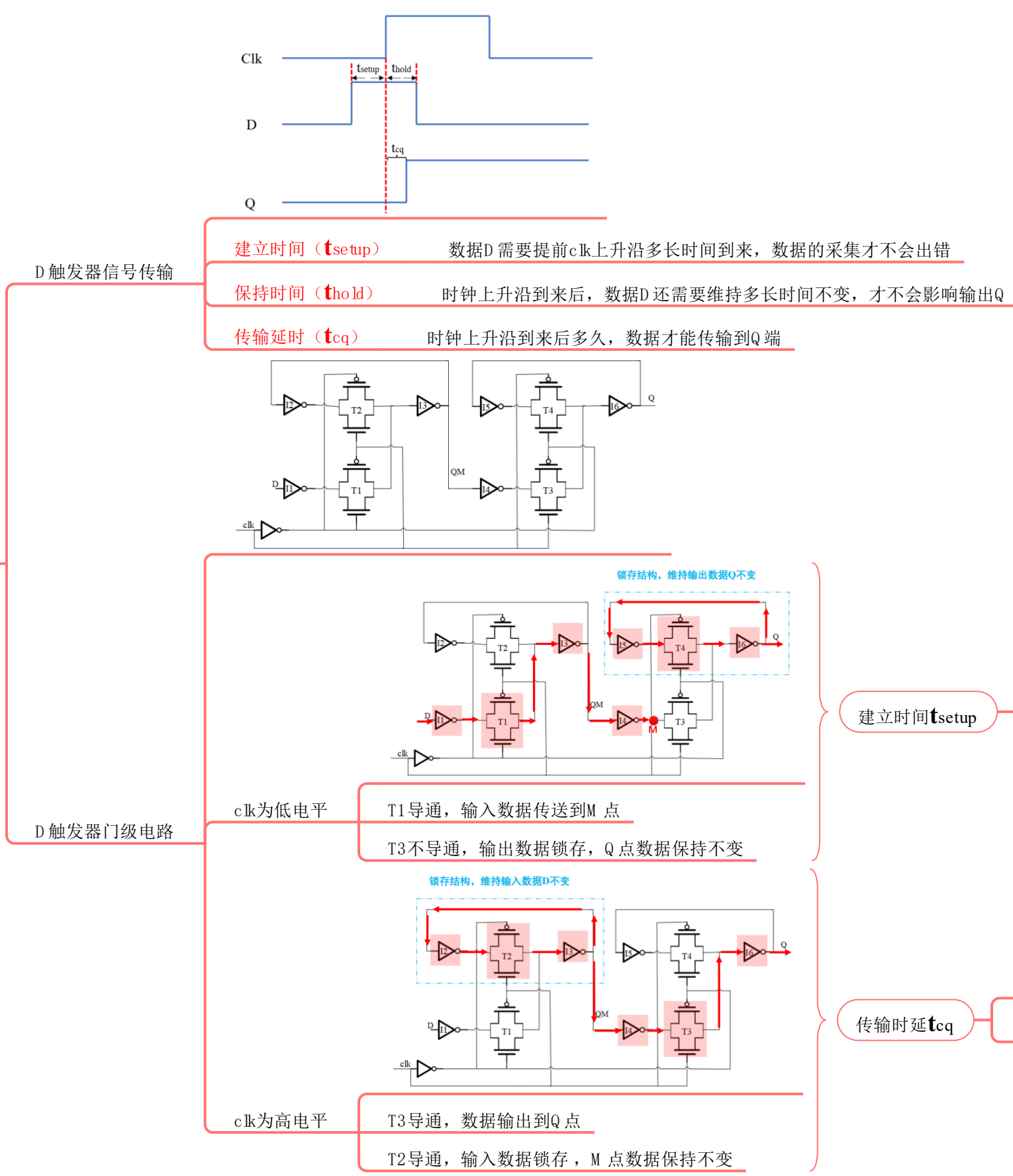
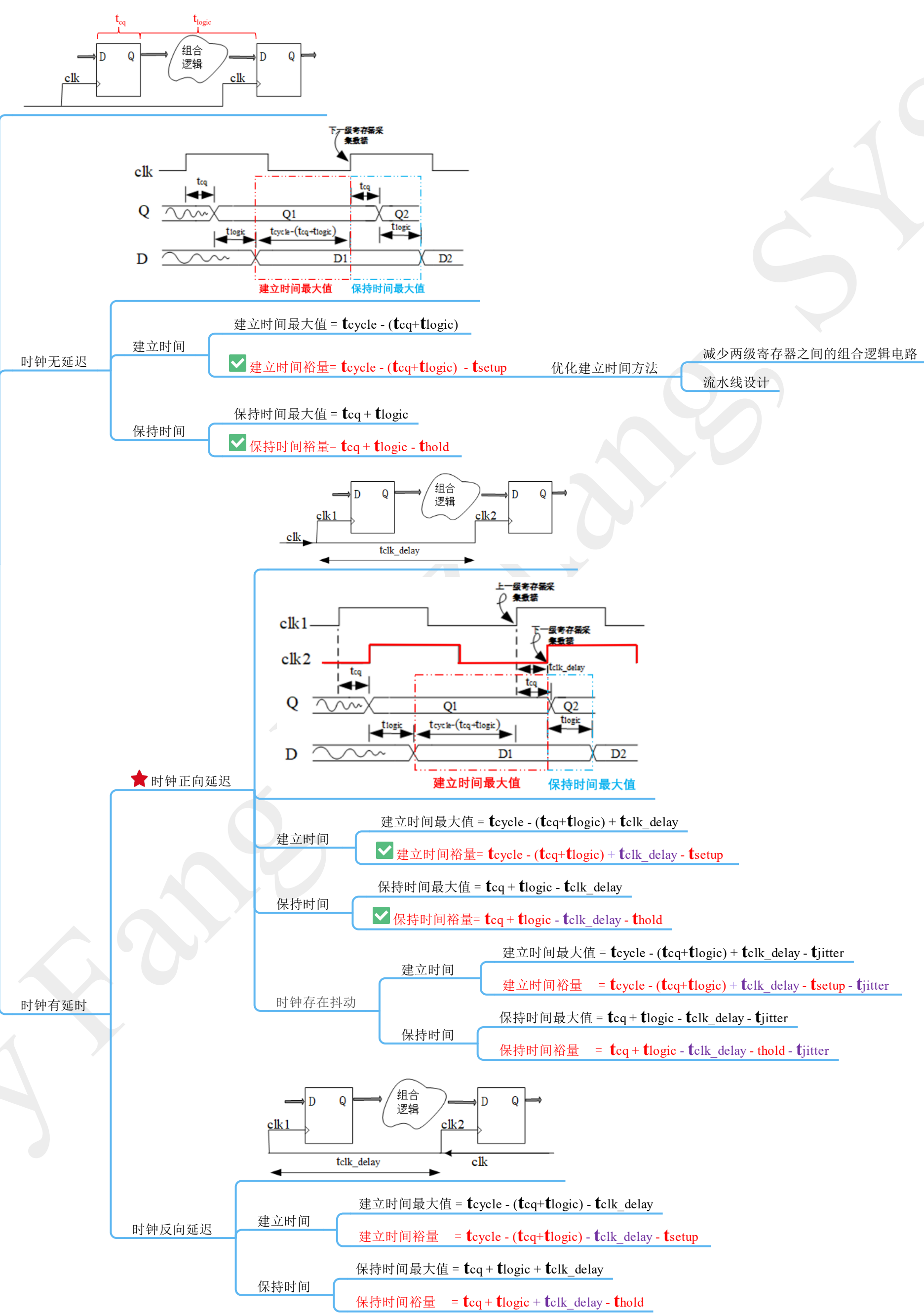


静态时序分析

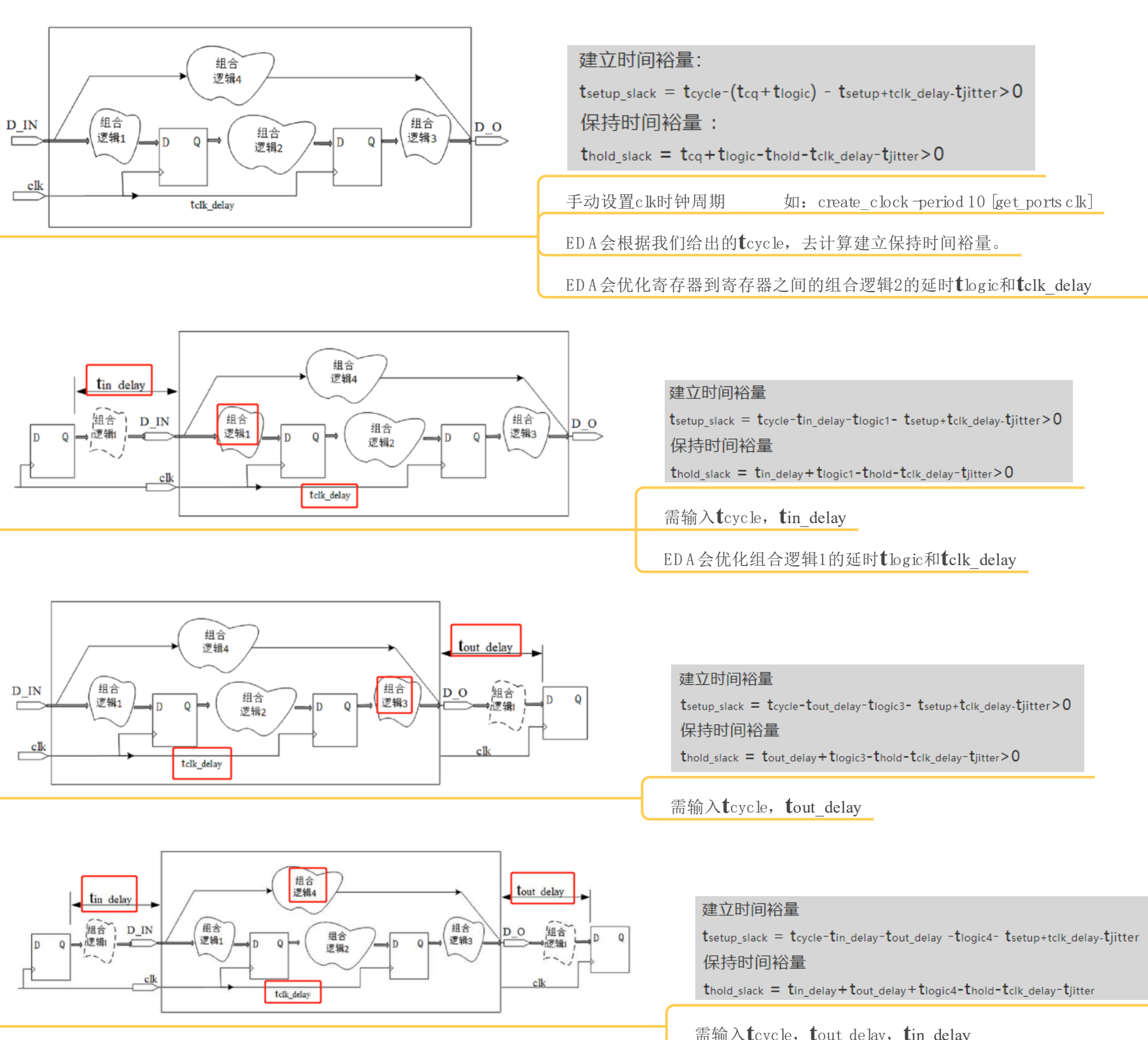
1. 寄存器延迟



2. 建立、保持时间裕量分析



3. 基本同步时钟约束路径



4. 时序违例的优化方法概述

