# Jiajun Tang

Email: jt1488@princeton.edu Website: https://jiajuntang2002.github.io

#### EDUCATION

Princeton University Princeton, NJ Ph.D. in Electrical and Computer Engineering Aug 2024 - Current

Peking University Beijing, China B. S. in Applied Physics, Department of EECS; CGPA: 89/100 (WES-calculated: 3.81/4.00) Sep 2020 - Jul 2024

University of Michigan Ann Arbor, MI Visiting Scholar to Michigan Integrated Circuits Laboratory, Advisor: Prof. David Blaauw Jun 2023 - Dec 2023

#### Research Experience

## Low Power, Energy Efficient Temperature Sensor

Peking University Feb 2024 - Jun 2024

Research Assistant to: Prof. Xiyuan Tang

- o Completed CMOS temperature sensor design in TSMC 28nm Process
- Proposed a novel temperature-voltage transducer, designed a 12-bit SAR ADC for quantization
- $\circ$  Achieved 0.15K resolution, with 12.6pJ/conversion and 0.29pJ $^{\circ}C^{2}$  FoM in post-layout simulation

## High Efficiency CMOS Digital Transmitter in Localization System

University of Michigan Jun 2023 - Dec 2023

Research Assistant to: Prof. David Blaauw

- o Completed CMOS Class-D power amplifier design and TX system integration in TSMC 28nm Process, which is ready for tape-out
- Proposed the idea of bonding-wire based harmonic tuning for class-D power amplifier, adopted eight-shaped inductor structure for inter-stage matching, minimizing EM coupling
- Participated in satellite flyover tests, power amplifier chip testing and debugging
- o Co-designed the TX with compact battery and antenna. Achieved 40% peak PAE and 17dBm output power in hybrid post-layout & EM simulation

## High Precision, Low Latency Capacitance-to-Digital Converter

Peking University

Research Assistant to: Prof. Xiyuan Tang

Dec 2022 - Nov 2023

- o Completed CMOS capacitive sensor design and measurement in TSMC 28nm Process, in cooperation with PhD student Zilong in PRIME lab
- o Proposed hybrid DT-CT architecture for CDC, adopted dead-band switches to eliminate ELD issues
- $\circ$  Modeled the incremental  $\Delta\Sigma$  loop in MATLAB independently, achieved over 100dB SQNR in the entire measurement range
- Designed the FIA amplifier and loop integrator with feed-forward path
- Achieved 100aF resolution, 14-bit ENOB and 181.8dB Schreier FoM in measurement. Published in IEEE CICC 2024 (second author)

## Power Management Unit in Advanced Process Node

Peking University

Research Assistant to: Prof. Weixin Gai

Mar 2022 - Nov 2022

- o Completed CMOS power management circuit design applied in high speed wireline transceiver in TSMC 12nm FinFET process, which is ready for tape-out
- ∘ Set up design targets independently, achieved >80dB PSRR in low frequency and ~10ppm temperature coefficient in post-layout simulation

#### Publications

o Zilong Shen, **Jiajun Tang**, Haoyang Luo, Zhongyi Wu, Zongnan Wang, Xing Zhang, Xiyuan Tang, and Yuan Wang, "A 181.8dB FoMs Zoom Capacitance-to-Digital Converter with kT/C Noise Cancellation and Dead Band Operation," in 2024 IEEE Custom Integrated Circuits Conference (CICC).

## Relevant Courses

- Circuit Design: Radio Frequency Integrated Circuit; Advanced Analog Integrated Circuits Design; Principle and Design of Integrated Circuit; Fundamentals of Electronic Circuits and Experiments
- o **Device & Physics**: Physics of Semiconductor; Integrated Circuit Devices; Integrated Circuit Manufacturing Technology; Nanoionics; Electrodynamics; Theoretical Mechanics; Quantum Mechanics; Solid State Physics
- Signal Processing: Digital Signal Processing; Signals and Systems (Honor Track)
- Computing: Introduction to Computation; Data Structure and Algorithm; Computer Architecture and Intelligent Chip Design; Future Computing with Novel Information Device

## SKILLS SUMMARY

0	Languages:	Mandarin	(Native);	English
---	------------	----------	-----------	---------

• Programming: C++, Python, MATLAB

o Award for Academic Excellents at Peking University

o Circuit Design and Simulation: Cadence Virtuoso, HSpice, Verilog, Chisel, HFSS

# EXTRA-CURRICULUM OUTREACH

EXTRA-CURRICULUM OUTREACH				
<ul> <li>Teaching assistant for course "Undergraduate Research Practice for Electronic Information Science" at Peking University</li> </ul>	Feb, 2024			
$\circ$ Vice President for the Students' Association for Science and Technology at Peking University	Sep, $2022$			
$\circ$ Member of department's basketball team at Peking University	Sep, 2020			
Honors and Awards				
• Merit Student at Peking University	Sep, $2023$			
o Third Prize Scholarship at Peking University	Sep, $2023$			
$\circ$ Third Award in the Final Competition of Integrated Circuit EDA Elite Challenge in China	$\mathrm{Dec},2022$			
$\circ$ Award for Scientific Research Excellents at Peking University	Sep, $2022$			

Sep, 2021