xv6 is a re-implementation of Dennis Ritchie's and Ken Thompson's Unix Version 6 (v6). xv6 loosely follows the structure and style of v6, but is implemented for a modern x86-based multiprocessor using ANSI C.

ACKNOWLEDGMENTS

xv6 is inspired by John Lions's Commentary on UNIX 6th Edition (Peer to Peer Communications; ISBN: 1-57398-013-7; 1st edition (June 14, 2000)). See also http://pdos.csail.mit.edu/6.828/2016/xv6.html, which provides pointers to on-line resources for v6.

xv6 borrows code from the following sources:
 JOS (asm.h, elf.h, mmu.h, bootasm.S, ide.c, console.c, and others)
 Plan 9 (entryother.S, mp.h, mp.c, lapic.c)
 FreeBSD (ioapic.c)
 NetBSD (console.c)

The following people have made contributions: Russ Cox (context switching, locking), Cliff Frey (MP), Xiao Yu (MP), Nickolai Zeldovich, and Austin Clements.

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The code in the files that constitute xv6 is Copyright 2006–2016 Frans Kaashoek, Robert Morris, and Russ Cox.

ERROR REPORTS

Please send errors and suggestions to Frans Kaashoek and Robert Morris (kaashoek,rtm@mit.edu). The main purpose of xv6 is as a teaching operating system for MIT's 6.828, so we are more interested in simplifications and clarifications than new features.

BUILDING AND RUNNING XV6

To build xv6 on an x86 ELF machine (like Linux or FreeBSD), run "make". On non-x86 or non-ELF machines (like OS X, even on x86), you will need to install a cross-compiler gcc suite capable of producing x86 ELF binaries. See http://pdos.csail.mit.edu/6.828/2016/tools.html. Then run "make TOOLPREFIX=i386-jos-elf-". Now install the QEMU PC simulator and run "make gemu".

The numbers to the left of the file names in the table are sheet numbers. The source code has been printed in a double column format with fifty lines per column, giving one hundred lines per sheet (or page). Thus there is a convenient relationship between line numbers and sheet numbers.

# basic headers	# system calls	67 pipe.c
01 types.h	32 traps.h	
01 param.h	32 vectors.pl	# string operations
02 memlayout.h	33 trapasm.S	69 string.c
02 defs.h	33 trap.c	
04 x86.h	35 syscall.h	<pre># low-level hardware</pre>
06 asm.h	35 syscall.c	70 mp.h
07 mmu.h	37 sysproc.c	72 mp.c
09 elf.h	3,1	73 lapic.c
	# file system	76 ioapic.c
# entering xv6	38 buf.h	77 kbd.h
10 entry.S	39 sleeplock.h	78 kbd.c
11 entryother.S	39 fcntl.h	79 console.c
12 main.c	40 stat.h	83 uart.c
	40 fs.h	
# locks	41 file.h	# user-level
15 spinlock.h	42 ide.c	84 initcode.S
15 spinlock.c	44 bio.c	84 usys.S
r	46 sleeplock.c	85 init.c
# processes	47 log.c	85 sh.c
17 vm.c	49 fs.c	
23 proc.h	58 file.c	# bootloader
24 proc.c	60 sysfile.c	91 bootasm.S
30 swtch.S	66 exec.c	92 bootmain.c
31 kalloc.c		
	# pipes	

The source listing is preceded by a cross-reference that lists every defined constant, struct, global variable, and function in xv6. Each entry gives, on the same line as the name, the line number (or, in a few cases, numbers) where the name is defined. Successive lines in an entry list the line numbers where the name is used. For example, this entry:

swtch 2658 0374 2428 2466 2657 2658

indicates that swtch is defined on line 2658 and is mentioned on five lines on sheets 03. 24. and 26.

acquire 1574	0263 1230 4438	CAPSLOCK 7712	0271 7898 8177 8375
0380 1574 1578 2478 2548 2614	bmap 5410	7712 7745 7886	consoleread 8221
2649 2677 2769 2830 2891 2906	5154 5410 5436 5520 5570	cgaputc 8105	8221 8279
2966 2979 3175 3192 3416 3822	bootmain 9217	8105 8163	consolewrite 8259
3842 4309 4365 4470 4533 4624	9168 9217	clearpteu 2022 0439 2022 2028 6667 cli 0557	8259 8278
4636 4655 4830 4857 4876 4931	BPB 4107	0439 2022 2028 6667	consputc 8151
5258 5291 5362 5375 5880 5904	4107 4110 5022 5024 5059	cli 0557	7917 7948 8018 8036 8039 8043
5918 6813 6834 6855 8010 8181	bread 4502	0557 0559 1124 1660 8060 8154	8044 8151 8192 8198 8205 8266
8228 8264	0264 4502 4777 4778 4790 4806	9112	context 2326
acquiresleep 4622	4890 4891 4985 5006 5023 5058	cmd 8565	0251 0377 2303 2326 2345 2509
0389 4477 4492 4622 5311 5360	5211 5235 5314 5426 5470 5520	8565 8577 8586 8587 8592 8593	2510 2511 2512 2781 2822 3028
allocproc 2473	5570	8598 8602 8606 8615 8618 8623	CONV 7572
2473 2525 2587	brelse 4526	8631 8637 8641 8651 8675 8677	7572 7573 7574 7575 7576 7577
allocuvm 1927	0265 4526 4529 4781 4782 4797	8752 8755 8757 8758 8759 8760	7578 7579
0430 1927 1941 1947 2565 6651	4814 4894 4895 4987 5009 5029	8763 8764 8766 8768 8769 8770	copyout 2118
6665	5034 5065 5217 5220 5244 5322	8771 8772 8773 8774 8775 8776	0438 2118 6675 6686
alltraps 3304	5432 5476 5523 5574	8779 8780 8782 8784 8785 8786	copyuvm 2035
3259 3267 3280 3285 3303 3304		8787 8788 8789 8800 8801 8803	0435 2035 2046 2048 2592
ALT 7710	3859 4055 4074 4101 4107 4280	8805 8806 8807 8808 8809 8810	cprintf 8002
7710 7738 7740	4296 4319 4758 4779 4892 5007	8813 8814 8816 8818 8819 8820	0270 1254 1941 1947 3026 3030
argfd 6071	5520 5521 5522 5566 5570 5571	8821 8822 8912 8913 8914 8915	3032 3440 3453 3458 3710 5153
6071 6123 6138 6157 6168 6181	5572	8917 8921 8924 8930 8931 8934	6625 7659 8002 8063 8064 8065
argint 3602	buf 3850	8937 8939 8942 8946 8948 8950	8068
0404 3602 3616 3632 3783 3806	0250 0264 0265 0266 0308 0335	8953 8955 8958 8960 8963 8964	cpu 2301
3820 6076 6138 6157 6408 6475	2120 2123 2132 2134 3850 3856	8975 8978 8981 8985 9000 9003	0311 0363 1254 1268 1506 1566
6476 6532	3857 3858 4213 4231 4234 4274	9008 9012 9013 9016 9021 9022	1590 1608 1647 1717 2301 2312
argptr 3611	4306 4354 4356 4359 4426 4430	9028 9037 9038 9044 9045 9051	2436 2458 2761 3440 3453 3458
0405 3611 6138 6157 6181 6557	4434 4440 4453 4465 4468 4501	9052 9061 9064 9066 9072 9073	7213
argstr 3629	4504 4515 4526 4706 4777 4778	9078 9084 9090 9091 9094	cpuid 2430
0406 3629 6207 6308 6408 6457	4790 4791 4797 4806 4807 4813	CMOS_PORT 7477	0358 1254 1723 2430 3415 3441
6474 6508 6532	4814 4890 4891 4922 4970 4983	7477 7491 7492 7533	3454 3461
BACK 8561	5004 5019 5054 5207 5232 5305	CMOS_RETURN 7478	CRO_PE 0727
8561 8674 8820 9089	5413 5459 5506 5556 7930 7941	7478 7536	0727 1137 1170 9143
backcmd 8596 8814	7945 7948 8168 8190 8204 8238	CMOS_STATA 7520	CRO_PG 0737
8596 8609 8675 8814 8816 8942	8259 8266 8684 8687 8688 8689	7520 7563	0737 1054 1170
9055 9090	8703 8715 8716 8718 8719 8720	CMOS_STATB 7521	CRO_WP 0733
BACKSPACE 8100	8724	7521 7556	0733 1054 1170
8100 8117 8159 8192 8198	bwrite 4515	CMOS_UIP 7522	CR4_PSE 0739
balloc 5016	0266 4515 4518 4780 4813 4893	7522 7563	0739 1047 1163
5016 5036 5417 5425 5429	bzero 5002	COM1 8314	create 6357
BBLOCK 4110	5002 5030	8314 8324 8327 8328 8329 8330	6357 6377 6390 6394 6414 6457
4110 5023 5058	B_DIRTY 3862	8331 8332 8335 8341 8342 8357	6477
begin_op 4828	3862 4294 4318 4323 4360 4378	8359 8367 8369	CRTPORT 8101
0336 2644 4828 5933 6024 6210	4486 4519 4939	commit 4901	8101 8110 8111 8112 8113 8131
	B_VALID 3861	4753 4875 4901	8132 8133 8134
bfree 5052	3861 4322 4360 4378 4507	CONSOLE 4186	8132 8133 8134 CTL 7709
	C 7731 8174	4186 8278 8279	
5052 5464 5474 5477	7731 8174 7731 7779 7804 7805 7806 7807	consoleinit 8274	7709 7735 7739 7885 DAY 7527
bget 4466			
4466 4496 4506 binit 4438	7808 7810 8174 8184 8188 8195 8206 8239	0269 1226 8274 consoleintr 8177	7527 7544 deallocuvm 1961
DIIIL 4430	0200 0239	COURTELLIFE, 91/7	uea i i i i Cuviii 1901

0421 1042 1049 1061 2000 2569	0520 9626 9627	2417 2512 2052	4220 4257 4207 4210
DEVSDACE 0204	0330 0020 0027	froorango 3151	4230 4237 4207 4310 TDE RSV 4216
0204 1012 1026	9560 9610 9622 9752 9755 0021	2111 2125 2141 2151	10E_B31 4210 4216 4242
dovew 4170	0027 0029 0056 0066	fnoorm 2002	4210 4242 TDE CMD DDMIII 4222
4179 4184 5509 5511 5559 5561	avit 2627	0432 1831 2003 2008 2060 2690	4223 4223 4282
5862 8278 8270	0350 2627 2665 3405 3400 3460	6702 6707	TDE CMD PEAD 4221
dinode 4078	3479 3768 8417 8420 8461 8526	6702 0707 ESST7F 0162	4221 4221 4282
4078 4101 5208 5212 5233 5236	8531 8616 8625 8635 8680 8727	0162 4278	TDE CMD WRITE 4222
5306 5315	8734	natedesc 0901	4222 4283
dirent 4115	FXTMEM 0202	0523 0526 0901 3361	TDF CMD WRMIII 4224
4115 5614 5655 6255 6304	0202 0208 1810	getcallerncs 1627	4224 4283
dirlink 5652	fdalloc 6103	0381 1591 1627 3028 8066	TDF DF 4218
0288 5652 5667 5675 6230 6389	6103 6125 6432 6562	getcmd 8684	4218 4244
6393 6394	fetchint 3567	8684 8715	TDF DRDY 4217
dirlookun 5611	0407 3567 3604 6539	gettoken 8856	4217 4242
0289 5611 5617 5621 5659 5775	fetchstr 3581	8856 8941 8945 8957 8970 8971	TDF FRR 4219
6323 6367	0408 3581 3634 6545	9007 9011 9033	4219 4244
DIRSIZ 4113	file 4150	growproc 2558	idtinit 3379
4113 4117 5605 5672 5728 5729	0252 0278 0279 0280 0282 0283	0361 2558 3809	0415 1255 3379
5792 6204 6305 6361	0284 0351 2348 4150 4971 5860	havedisk1 4233	idup 5289
DPL USER 0779	5865 5875 5878 5881 5901 5902	4233 4263 4362	0291 2608 5289 5762
0779 1726 1727 2533 2534 3373	5914 5916 5952 5965 6002 6065	holding 1645	iget 5254
3468 3478	6071 6074 6103 6120 6134 6153	0382 1577 1604 1645 2813	5159 5218 5254 5274 5629 5760
E0ESC 7716	6166 6178 6405 6554 6758 6772	holdingsleep 4651	iinit 5143
7716 7870 7874 7875 7877 7880	7911 8309 8578 8633 8634 8764	0391 4358 4517 4528 4651 5333	0292 2864 5143
elfhdr 0955	8772 8972	HOURS 7526	ilock 5303
0955 6615 9219 9224	filealloc 5876	7526 7543	0293 5303 5309 5325 5765 5955
ELF_MAGIC 0952	0278 5876 6432 6778	ialloc 5204	5974 6025 6216 6229 6242 6317
0952 6634 9230	fileclose 5914	0290 5204 5222 6376 6377	6325 6365 6369 6379 6424 6512
ELF_PROG_LOAD 0986	0279 2639 5914 5920 6171 6434	IBLOCK 4104	6628 8233 8253 8268
0986 6645	6565 6566 6804 6806	4104 5211 5235 5314	inb 0453
end_op 4853	filedup 5902	ICRHI 7380	0453 4242 4262 7346 7536 7864
0337 2646 4853 5935 6029 6212	0280 2607 5902 5906 6127	7380 7444 7502 7514	7867 8111 8113 8335 8341 8342
6219 6237 6246 6313 6347 6352	fileinit 5869	ICRLO 7370	8357 8367 8369 9123 9131 9254
6416 6421 6427 6436 6440 6458	0281 1231 5869	7370 7445 7446 7503 7505 7515	initlock 1562
6462 6478 6482 6509 6515 6520	fileread 5965	ID 7363	0383 1562 2425 3133 3375 4255
6624 6659 6710	0282 5965 5980 6140	7363 7404 7459	4442 4615 4762 5147 5871 6786
entry 1044	filestat 5952	ideinit 4251	8276
0961 1040 1043 1044 3252 3253	0283 5952 6183	0306 1232 4251	initlog 4756
6699 7071 9221 9245 9246	filewrite 6002	ideintr 4304	0334 2865 4756 4759
EOI 7366	0284 6002 6034 6039 6159	0307 3424 4304	initsleeplock 4613
7366 7441 7467	FL_IF 0710	idelock 4230	0392 4456 4613 5149
ERROR 7387	0710 1662 1669 2441 2537 2819	4230 4255 4309 4312 4330 4365	inituvm 1886
7387 7434	fork 2580	4379 4383	0433 1886 1891 2530
ESR 7369	0360 2580 3762 8460 8523 8525	iderw 4354	inode 4162
7369 7437 7438	8742 8744	0308 4354 4359 4361 4363 4508	0253 0288 0289 0290 0291 0293
EXEC 8557	fork1 8738	4520	0294 0295 0296 0297 0299 0300
8557 8622 8759 9065	8600 8642 8654 8661 8676 8723	idestart 4274	0301 0302 0303 0434 1903 2349
exec 6610	8738	4234 4274 4277 4285 4328 4375	4156 4162 4180 4181 4974 5139
0275 6548 6610 6625 8468 8529	forkret 2853	freerange 3151 3111 3135 3141 3151 freevm 2003 0432 1831 2003 2008 2060 2690 6702 6707 FSSIZE 0162 0162 4278 gatedesc 0901 0523 0526 0901 3361 getcallerpcs 1627 0381 1591 1627 3028 8066 getmd 8684 8684 8715 gettoken 8856 8856 8941 8945 8957 8970 8971 9007 9011 9033 growproc 2558 0361 2558 3809 havedisk1 4233 4233 4263 4362 holding 1645 0382 1577 1604 1645 2813 holdingsleep 4651 0391 4358 4517 4528 4651 5333 HOURS 7526 7526 7543 ialloc 5204 0290 5204 5222 6376 6377 IBLOCK 4104 4104 5211 5235 5314 ICRHI 7380 7380 7444 7502 7514 ICRLO 7370 7370 7445 7446 7503 7505 7515 ID 7363 7363 7404 7459 ideinit 4251 0306 1232 4251 ideintr 4304 0307 3424 4304 idelock 4230 4230 4255 4309 4312 4330 4365 4379 4383 iderw 4354 0308 4354 4359 4361 4363 4508 4520 idestart 4274 4234 4274 4277 4285 4328 4375 idewait 4238	5149 5159 5203 5230 5253 5256

5363 5300 5300 5303 5331 5350	4074 5267 5456	7724 7765 7707 7044	1020 1022 1022 1021 1026 1020
5262 5288 5289 5303 5331 5358	4974 5367 5456	7721 7765 7787 7811	4830 4832 4833 4834 4836 4838
5382 5410 5456 5488 5503 5553	iunlock 5331	kfree 3164	4839 4857 4858 4859 4860 4861
5610 5611 5652 5656 5754 5757	0295 5331 5334 5384 5772 5957	0317 1949 1977 1979 2013 2016	4863 4868 4870 4876 4877 4878
5789 5800 6205 6252 6303 6356	5977 6028 6225 6439 6518 8226	2593 2688 3156 3164 3169 6802	4879 4889 4890 4891 4903 4907
6360 6406 6454 6469 6504 6616	8263	6823	4926 4928 4931 4932 4933 4936
8221 8259	iunlockput 5382	kill 2975	4937 4938 4940
INPUT_BUF 8166	0296 5382 5767 5776 5779 6218	0362 2975 3459 3785 8467	-
8166 8168 8190 8202 8204 8206	6231 6234 6245 6330 6341 6345	kinit1 3131 0318 1219 3131 kinit2 3139	4733 4745 4758 4759 4791 4807
8238	6351 6368 6372 6396 6426 6435	0318 1219 3131	LOGSIZE 0160
insl 0462	6461 6481 6514 6658 6709	kinit2 3139	0160 4735 4834 4926 6017
0462 0464 4319 9273	iupdate 5230	0319 1234 3139 KSTACKSIZE 0151	log_write 4922
0462 0464 4319 9273 install_trans 4772 4772 4821 4906 INT_DISABLED 7619 7619 7664 IOAPIC 7608 7608 7655 ioapic 7627	0297 5230 5369 5482 5579 6224		
4772 4821 4906	6244 6339 6344 6383 6387	0151 1058 1067 1285 1874 2498	
INT_DISABLED 7619	kalloc 3187	kvmalloc 1840	ltr 0538
7619 7664	0316 1284 1744 1823 1892 1939 2051 2494 3187 6780	0427 1220 1840	0538 0540 1878
IOAPIC 7608	2051 2494 3187 6780	lapiceoi 7464	mappages 1760
7608 7655	KBDATAP 7704	0328 3421 3425 3432 3436 3442	1760 1829 1894 1946 2054
ioapic 7627	7704 7867	7464	MAXARG 0158
7308 7325 7326 7624 7627 7636	kbdgetc 7856	lapicid 7455	0158 6528 6614 6672
7637 7643 7644 7655	7856 7898	0326 2444 7455 8063 lapicinit 7408	MAXARGS 8563
ioapicenable 7670	kbdintr 7896	lapicinit 7408	8563 8571 8572 9040
ioapicenable 7670 0311 4256 7670 8282 8343	0322 3431 7896	0329 1222 1245 7408 lapicstartap 7483	MAXFILE 4075
ioapicid 7215	KBSTATP 7702		4075 5566
0312 7215 7326 7658 7659	7702 7864	0330 1289 7483	MAXOPBLOCKS 0159
ioapicinit 7651	KBS_DIB 7703	lapicw 7401	0159 0160 0161 4834
0313 1225 7651 7659 ioapicread 7634	7703 7865	7401 7414 7420 7421 7422 7425	memcmp 6915
ioapicread 7634	KERNBASE 0207	7426 7431 7434 7437 7438 7441	0395 6915 7237 7288 7566
7634 7656 7657	0207 0208 0210 0211 0213 0214	7444 7445 7451 7467 7502 7503	memmove 6931
ioapicwrite 7641	1310 1634 1810 1932 2009	7505 7514 7515	0396 1275 1895 2053 2132 4779
7641 7664 7665 7675 7676	0207 0208 0210 0211 0213 0214 1310 1634 1810 1932 2009 KERNLINK 0208	lcr3 0590	4892 4986 5242 5321 5522 5572
1FB 4101	0208 1811	0590 1855 1879	5729 5731 6931 6954 8126
4101 4104 5212 5236 5315	KEY_DEL 7728	lgdt 0512	memset 6904
iput 5358	7728 7769 7791 7815	0512 0520 1135 1728 9141	0397 1747 1825 1893 1945 2511
0294 2645 5358 5385 5660 5783	KEY_DN 7722	lidt 0526	2532 3172 5007 5214 6334 6535
5934 6235 6519	7722 7765 7787 7811	0526 0534 3381	6904 8128 8687 8758 8769 8785
IRQ_COM1 3233	KEY_END 7720		8806 8819
3233 3434 8343	7720 7768 7790 7814	7385 7425 LINT1 7386 7386 7426 LIST 8560	microdelay 7473
IRQ_ERROR 3235	KEY_HOME 7719	LINT1 7386	0331 7473 7504 7506 7516 7534
3235 7434	7719 7768 7790 7814	7386 7426	8358
IRQ_IDE 3234	KEY_INS 7727	LIST 8560	min 4973
3234 3423 3427 4256	7727 7769 7791 7815	8560 8640 8807 9083	4973 5521 5571
IRQ_KBD 3232	KEY_LF 7723	listcmd 8590 8801	MINS 7525
3232 3430 8282	7723 7767 7789 7813	8590 8611 8641 8801 8803 8946	7525 7542
IRQ_SPURIOUS 3236	KEY_PGDN 7726	9057 9084	MONTH 7528
3236 3439 7414	7726 7766 7788 7812	loaduvm 1903	7528 7545
IRQ_TIMER 3231	KEY_PGUP 7725	0434 1903 1909 1912 6655	mp 7052
3231 3414 3474 7421	7725 7766 7788 7812	log 4738 4750	7052 7208 7229 7236 7237 7238
isdirempty 6252	KEY_RT 7724	4738 4750 4762 4764 4765 4766	7255 7260 7264 7265 7268 7269
IRQ_SPURIOUS 3236 3236 3439 7414 IRQ_TIMER 3231 3231 3414 3474 7421 isdirempty 6252 6252 6259 6329 itrunc 5456	7724 7767 7789 7813	4776 4777 4778 4790 4793 4794	7280 7283 7285 7287 7294 7305
itrunc 5456	KEY_UP 7721	4795 4806 4809 4810 4811 4822	7310 7342

MPBUS 7102	0300 5755 5770 5782 5801 6227	8131 8132 8133 8134 8324 8327	7384 7431
7102 7329	6312 6363	8328 8329 8330 8331 8332 8359	
mpconf 7063	namex 5755	9128 9136 9264 9265 9266 9267	0103 0428 0429 0430 0431 0432
7063 7279 7282 7287 7306	5755 5793 5803	9268 9269	0433 0434 0435 0438 0439 1210
mpconfig 7280	NBUF 0161	outs1 0483	1260 1306 1710 1735 1737 1760
7280 7310	0161 4430 4453	0483 0485 4296	1817 1820 1823 1886 1903 1927
mpenter 1241	NCPU 0152	outw 0477	1961 2003 2022 2034 2035 2037
1241 1286	0152 2312 7213 7318	0477 1180 1182 9174 9176	2102 2118 2339 6618
mpinit 7301	ncpu 7214	O CREATE 3953	PDX 0812
0341 1221 7301	1277 2313 2447 4256 7214 7318	3953 6413 8978 8981	0812 1740 1973
MPIOAPIC 7103	7319 7320	O_RDONLY 3950	PDXSHIFT 0827
7103 7324	NDEV 0156	3950 6425 8975	0812 0818 0827 1310
mpioapic 7089	0156 5509 5559 5862	O_RDWR 3952	peek 8901
7089 7308 7325 7327	NDIRECT 4073	3952 6446 8514 8516 8707	8901 8925 8940 8944 8956 8969
MPIOINTR 7104	4073 4075 4084 4174 5415 5420	O_WRONLY 3951	9005 9009 9024 9032
7104 7330	5424 5425 5462 5469 5470 5477	3951 6445 6446 8978 8981	PGADDR 0818
MPLINTR 7105	5478	P2V 0211	0818 1973
7105 7331	NELEM 0442	0211 1219 1234 1274 1742 1826	PGROUNDDOWN 0830
mpmain 1252	0442 1828 3022 3707 6537	1918 1978 2012 2053 2111 7234	0830 1765 1766 2125
1209 1236 1246 1252	nextpid 2416	7262 7287 7493 8102	PGROUNDUP 0829
MPPROC 7101	2416 2489	panic 8055 8731	0829 1937 1969 3154 6664
7101 7316	NFILE 0154	0272 1578 1605 1670 1672 1771	
mpproc 7078	0154 5865 5881	1827 1863 1865 1867 1891 1909	0823 0829 0830 1305 1747 1775
7078 7307 7317 7322	NINDIRECT 4074	1912 1977 2008 2028 2046 2048	1776 1825 1890 1893 1894 1908
mpsearch 7256	4074 4075 5422 5472	2442 2451 2529 2634 2665 2814	1910 1914 1917 1938 1945 1946
7256 7285	NINODE 0155	2816 2818 2820 2879 2882 3169	1970 1973 2044 2053 2054 2129
mpsearch1 7230	0155 5139 5148 5262	3455 4277 4279 4285 4359 4361	2135 2531 2538 3155 3168 3172
7230 7264 7268 7271	NO 7706	4363 4496 4518 4529 4759 4860	6653 6665 6667
multiboot_header 1029	7706 7752 7755 7757 7758 7759	4927 4929 5036 5062 5222 5274	PHYSTOP 0203
1028 1029	7760 7762 7774 7777 7779 7780	5309 5325 5334 5436 5617 5621	0203 1234 1812 1826 1827 3168
mycpu 2437	7781 7782 7784 7802 7803 7805	5667 5675 5906 5920 5980 6034	pinit 2423
0363 1256 1278 1590 1647 1661	7806 7807 7808	6039 6259 6328 6336 6377 6390	0365 1228 2423
1662 1663 1671 1673 1870 1871	NOFILE 0153	6394 7311 7340 8013 8055 8063	PIPE 8559
1872 1873 1874 1877 2431 2437	0153 2348 2605 2637 6078 6108	8123 8601 8620 8653 8731 8744	8559 8650 8786 9077
2442 2461 2761 2815 2821 2822	NPDENTRIES 0821	8928 8972 9006 9010 9036 9041	pipe 6762
2823	0821 1306 2010	panicked 7919	0254 0352 0353 0354 4155 5931
myproc 2457	NPROC 0150	7919 8069 8153	5972 6009 6762 6774 6780 6786
0364 2457 2561 2584 2629 2675	0150 2411 2480 2654 2681 2770	parseblock 9001	6790 6794 6811 6830 6851 8463
2811 2831 2876 3404 3406 3408	2957 2980 3019	9001 9006 9025	8652 8653
3451 3460 3462 3468 3473 3478	NSEGS 0749	parsecmd 8918	pipealloc 6772
3569 3584 3604 3614 3704 3791	0749 2305	8602 8724 8918	0351 6559 6772
3808 3825 4629 5762 6078 6106	nulterminate 9052	parseexec 9017	pipeclose 6811
6170 6505 6564 6619 6837 6857	8915 8930 9052 9073 9079 9080	8914 8955 9017	0352 5931 6811
8231	9085 9086 9091	parseline 8935	pipecmd 8584 8780
namecmp 5603	NUMLOCK 7713	8912 8924 8935 8946 9008	8584 8612 8651 8780 8782 8958
0298 5603 5624 6320	7713 7746	parsepipe 8951	9058 9078
namei 5790	outb 0471	8913 8939 8951 8958	piperead 6851
0299 2542 5790 6211 6420 6508	0471 4260 4269 4288 4289 4290	parseredirs 8964	0353 5972 6851
6623	4291 4292 4293 4295 4298 7345	8964 9012 9031 9042	PIPESIZE 6760
nameiparent 5801	7346 7491 7492 7533 8110 8112	PCINT 7384	6760 6764 6836 6844 6866

pipewrite 6830	0544 1659 1669 2441 2819	safestrcpy 6982	5715 5764
0354 6009 6830	readi 5503	0398 2541 2610 6693 6982	
poperi 1007	0301 1310 3303 3020 3000 3373		0370 2707 2874 2879 2882 3009
0386 1622 1667 1670 1672 1880	6258 6259 6632 6643	0287 4104 4110 4761 4763 4764	3829 4379 4615 4626 4833 4836
2463	readsb 4981	4765 4977 4981 4986 5022 5023	6842 6861 8236 8479
printint 7927	0287 4763 4981 5057 5152	5024 5057 5058 5152 5153 5154	sleeplock 3901
7927 8026 8030	readsect 9260	5155 5156 5210 5211 5235 5314	0258 0389 0390 0391 0392 3854
proc 2337	9260 9295	7554 7556 7558	3901 4166 4211 4424 4610 4613
0255 0364 0369 0436 1205 1558	readseg 9279	sched 2808	4622 4634 4651 4704 4968 5859
1706 1860 2309 2337 2343 2406	9214 9227 9238 9279	0368 2664 2808 2814 2816 2818	6064 6757 7909 8307
2411 2414 2456 2459 2462 2472	read_head 4788	2820 2832 2898	spinlock 1501
2475 2480 2522 2561 2583 2584	4788 4820	scheduler 2758	0257 0370 0380 0382 0383 0384
2629 2630 2654 2673 2675 2681	recover_from_log 4818	0367 1257 2303 2758 2781 2822	0418 1501 1559 1562 1574 1602
2760 2762 2770 2777 2786 2811	4752 4767 4818	SCROLLLOCK 7714	1645 2407 2410 2874 3109 3120
2876 2955 2957 2977 2980 3015	REDIR 8558	7714 7747	3358 3363 3903 4210 4230 4423
3019 3355 3459 3555 3569 3584	8558 8630 8770 9071	SECS 7524	4429 4609 4703 4739 4967 5138
3614 3704 3757 4207 4608 4966	redircmd 8575 8764	7524 7541	5858 5864 6063 6756 6763 7908
6061 6106 6505 6604 6619 6754	8575 8613 8631 8764 8766 8975	SECTOR_SIZE 4215	7922 8306
7211 7307 7317 7319 7914 8311	8978 8981 9059 9072	4215 4280	start 1123 8409 9111
procdump 3004	REG_ID 7610	SECTSIZE 9212	1122 1123 1166 1174 1176 4740
0366 3004 8216	7610 7657	9212 9273 9286 9289 9294	4764 4777 4790 4806 4890 5154
proghdr 0974	REG_TABLE 7612	SEG 0769	8408 8409 9110 9111 9167
0974 6617 9220 9234	7612 7664 7665 7675 7676	0769 1724 1725 1726 1727	
PTE_ADDR 0844	REG_VER 7611	SEG16 0773	1208 1233 1264
0844 1742 1913 1975 2012 2049	7611 7656	0773 1870	stat 4004
2111	release 1602	segdesc 0752	0259 0283 0302 4004 4964 5488
PTE_FLAGS 0845	0384 1602 1605 2484 2491 2552	0509 0512 0752 0769 0773 2305	
0845 2050	2618 2696 2702 2788 2833 2857	seginit 1715	stati 5488
PTE_P 0833	2892 2905 2968 2986 2990 3180	0426 1223 1244 1715	0302 5488 5956
0833 1308 1310 1741 1751 1770	3197 3419 3826 3831 3844 4312	SEG_ASM 0660	STA_R 0669 0786
1772 1974 2011 2047 2107	4330 4383 4476 4491 4545 4630	0660 1189 1190 9184 9185 SEC KCODE 0742	0669 0786 1189 1724 1726 9184
PTE_PS 0840	4640 4657 4839 4870 4879 4940 5265 5281 5293 5364 5377 5884	SEG_REODE 0742	STA_W 0668 0785
0840 1308 1310	5888 5908 5922 5928 6822 6825	0742 1143 1724 3372 3373 9153 SEG_KDATA 0743	0668 0785 1190 1725 1727 9185 STA_X 0665 0782
pte_t 0848 0848 1734 1738 1742 1744 1763	6838 6847 6858 6869 8051 8214	0743 1153 1725 1873 3313 9158	0665 0782 1189 1724 1726 9184
1906 1963 2024 2038 2104	8232 8252 8267	SEG_NULLASM 0654	sti 0563
PTE_U 0835	releasesleep 4634	0654 1188 9183	0563 0565 1674 2766
0835 1751 1894 1946 2029 2109	0390 4531 4634 5336 5373		stosb 0492
PTE_W 0834	ROOTDEV 0157	0746 1870 1872 1878	0492 0494 6910 9240
0834 1308 1310 1751 1810 1812	0157 2864 2865 5760	SEG_UCODE 0744	stosl 0501
1813 1894 1946	ROOTINO 4054	0744 1726 2533	0501 0503 6908
PTX 0815	4054 5760	SEG_UDATA 0745	strlen 7001
0815 1753	run 3115	0745 1727 2534	0399 6674 6675 7001 8718 8923
PTXSHIFT 0826	3011 3115 3116 3122 3166 3176	SETGATE 0921	strncmp 6958
0815 0818 0826	3189 7311	0921 3372 3373	0400 5605 6958
pushcli 1655	runcmd 8606	setupkvm 1818	strncpy 6968
0385 1576 1655 1869 2460	8606 8620 8637 8643 8645 8659	0428 1818 1842 2042 2528 6637	0401 5672 6968
rcr2 0582	8666 8677 8724	SHIFT 7708	STS_IG32 0800
0582 3454 3461	RUNNING 2334	7708 7736 7737 7885	0800 0927
readeflags 0544	2334 2779 2817 3011 3473	skipelem 5715	STS_T32A 0797
Teader rags OFTT	TOOT LIES LOTE DOTT DALD	SKIPCICII 3713	313_13LN 0131

0797 1870	sys_fstat 6176
STS_TG32 0801	3656 3680 6176
0801 0927	SYS_getpid 3511
sum 7218	3511 3683
7218 7220 7222 7224 7225 7237	sys_getpid 3789
7292	3657 3683 3789
superblock 4063	SYS_kill 3506
0260 0287 4063 4761 4977 4981	3506 3678
SVR 7367	sys_kill 3779
7367 7414	3658 3678 3779
switchkvm 1853	SYS_link 3519
0437 1243 1843 1853 2782	3519 3691
switchuvm 1860	sys_link 6202
0436 1860 1863 1865 1867 2572	3659 3691 6202
2778 6701	SYS_mkdir 3520
swtch 3058	3520 3692
0377 2781 2822 3057 3058	sys_mkdir 6451
SYSCALL 8453 8460 8461 8462 8463 84	3660 3692 6451
8460 8461 8462 8463 8464 8465	SYS_mknod 3517
8466 8467 8468 8469 8470 8471	3517 3689
8472 8473 8474 8475 8476 8477	sys_mknod 6467
8478 8479 8480	3661 3689 6467
syscall 3701	SYS_open 3515
0409 3407 3557 3701	3515 3687
SYS_chdir 3509	sys_open 6401
3509 3681	3662 3687 6401
sys_chdir 6501	SYS_pipe 3504
3650 3681 6501	3504 3676
SYS_close 3521	sys_pipe 6551
3521 3693	3663 3676 6551
sys_close 6163	SYS_read 3505
3651 3693 6163	3505 3677
SYS_dup 3510	sys_read 6132
3510 3682	3664 3677 6132
sys_dup 6118	SYS_sbrk 3512
3652 3682 6118	3512 3684
SYS_exec 3507	sys_sbrk 3801
3507 3679 8413	3665 3684 3801
sys_exec 6526	SYS_sleep 3513
3653 3679 6526	3513 3685
SYS_exit 3502	sys_sleep 3815
3502 3674 8418	3666 3685 3815
sys_exit 3766	SYS_unlink 3518
3654 3674 3766	3518 3690
SYS_fork 3501	sys_unlink 6301
3501 3673	3667 3690 6301
sys_fork 3760	SYS_uptime 3514
3655 3673 3760 6¥6 5 1 1 2500	3514 3686
SYS_fstat 3508	sys_uptime 3838
3508 3680	3670 3686 3838

SYS_wait 3503	uartgetc 8363
3503 3675	8363 8375
sys_wait 3773	uartinit 8319
3668 3675 3773	0421 1227 8319
SYS_write 3516	uartintr 8373
3516 3688	0422 3435 8373
sys_write 6151	uartputc 8351
3669 3688 6151	0423 8160 8162 8347 8351
taskstate 0851	userinit 2520
0851 2304	0371 1235 2520 2529
TDCR 7391	uva2ka 2102
7391 7420	0429 2102 2126
ticks 3364	V2P 0210
0416 3364 3417 3418 3823 3824	0210 1287 1289 1751 1811 1812
3829 3843	1855 1879 1894 1946 2054 3168
tickslock 3363	V2P_W0 0213
0418 3363 3375 3416 3419 3822	0213 1040 1050
3826 3829 3831 3842 3844	VER 7364
TICR 7389	7364 7430
7389 7422	wait 2671
TIMER 7381	0372 2671 3775 8462 8533 8644
7381 7421	8670 8671 8725
TPR 7365	waitdisk 9251
7365 7451	9251 9263 9272
trap 3401	wakeup 2964
3252 3254 3319 3401 3453 3455	
3458	4878 6816 6819 6841 6846 6868
trapframe 0602	8208
0602 2344 2502 3401	wakeup1 2953
trapret 3324	2420 2651 2658 2953 2967
2418 2507 3323 3324	walkpgdir 1735
tvinit 3367	1735 1768 1911 1971 2026 2045
0417 1229 3367	2106
T_DEV 4002	writei 5553
4002 5508 5558 6477	0303 5553 5674 6026 6335 6336
T_DIR 4000	write_head 4804
4000 5616 5766 6217 6329 6337	
6385 6425 6457 6513	write_log 4885
T_FILE 4001	4885 4904
4001 6370 6414	xchg 0569
T_IRQ0 3229	0569 1256 1581
3229 3414 3423 3427 3430 3434 3438 3439 3474 7414 7421 7434	
7664 7675	yield 2828
T_SYSCALL 3226 3226 3373 3403 8414 8419 8457	0374 2828 3475
uart 8316 8316 8337 8355 8365	0272 0367 1209 1305
ODTO 0001 0000 0000	

0100	typedef	unsigned	int	uint;
0101	typedef	unsigned	short	ushort;
0102	typedef	unsigned		uchar;
0103	typedef	uint pde_		acmar,
0103	суречет	unic pue_	,	
0105				
0106				
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0123				
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0125				
0126				
0127				
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```
0150 #define NPROC
                         64 // maximum number of processes
0151 #define KSTACKSIZE 4096 // size of per-process kernel stack
0152 #define NCPU
                          8 // maximum number of CPUs
0153 #define NOFILE
                         16 // open files per process
0154 #define NFILE
                        100 // open files per system
0155 #define NINODE
                         50 // maximum number of active i-nodes
0156 #define NDEV
                         10 // maximum major device number
0157 #define ROOTDEV
                          1 // device number of file system root disk
0158 #define MAXARG
                         32 // max exec arguments
0159 #define MAXOPBLOCKS 10 // max # of blocks any FS op writes
0160 #define LOGSIZE
                         (MAXOPBLOCKS*3) // max data blocks in on-disk log
0161 #define NBUF
                         (MAXOPBLOCKS*3) // size of disk block cache
0162 #define FSSIZE
                         1000 // size of file system in blocks
0163
0164
0165
0166
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```

Sheet 01 Sheet 01

```
0200 // Memory layout
                                                                                   0250 struct buf:
0201
                                                                                   0251 struct context;
0202 #define EXTMEM 0x100000
                                         // Start of extended memory
                                                                                   0252 struct file;
0203 #define PHYSTOP 0xE000000
                                         // Top physical memory
                                                                                   0253 struct inode;
0204 #define DEVSPACE 0xFE000000
                                         // Other devices are at high addresses
                                                                                   0254 struct pipe;
0205
                                                                                   0255 struct proc;
0206 // Key addresses for address space layout (see kmap in vm.c for layout)
                                                                                   0256 struct rtcdate;
0207 #define KERNBASE 0x80000000
                                         // First kernel virtual address
                                                                                   0257 struct spinlock;
0208 #define KERNLINK (KERNBASE+EXTMEM) // Address where kernel is linked
                                                                                   0258 struct sleeplock;
0209
                                                                                   0259 struct stat;
0210 #define V2P(a) (((uint) (a)) - KERNBASE)
                                                                                   0260 struct superblock;
0211 #define P2V(a) (((void *) (a)) + KERNBASE)
                                                                                   0261
0212
                                                                                   0262 // bio.c
0213 #define V2P_W0(x) ((x) - KERNBASE)
                                           // same as V2P, but without casts
                                                                                   0263 void
                                                                                                        binit(void);
0214 #define P2V W0(x) ((x) + KERNBASE)
                                                                                   0264 struct buf*
                                           // same as P2V. but without casts
                                                                                                        bread(uint, uint);
0215
                                                                                   0265 void
                                                                                                        brelse(struct buf*);
0216
                                                                                   0266 void
                                                                                                        bwrite(struct buf*);
0217
                                                                                   0267
0218
                                                                                   0268 // console.c
0219
                                                                                   0269 void
                                                                                                        consoleinit(void);
0220
                                                                                   0270 void
                                                                                                        cprintf(char*, ...);
0221
                                                                                   0271 void
                                                                                                        consoleintr(int(*)(void));
0222
                                                                                   0272 void
                                                                                                        panic(char*) __attribute__((noreturn));
0223
                                                                                   0273
                                                                                   0274 // exec.c
0224
0225
                                                                                   0275 int
                                                                                                        exec(char*, char**);
0226
                                                                                   0276
0227
                                                                                   0277 // file.c
0228
                                                                                   0278 struct file*
                                                                                                        filealloc(void);
0229
                                                                                   0279 void
                                                                                                        fileclose(struct file*);
0230
                                                                                   0280 struct file*
                                                                                                        filedup(struct file*);
0231
                                                                                   0281 void
                                                                                                        fileinit(void);
                                                                                   0282 int
0232
                                                                                                        fileread(struct file*, char*, int n);
0233
                                                                                   0283 int
                                                                                                        filestat(struct file*, struct stat*);
0234
                                                                                   0284 int
                                                                                                        filewrite(struct file*, char*, int n);
0235
                                                                                   0285
0236
                                                                                   0286 // fs.c
0237
                                                                                   0287 void
                                                                                                        readsb(int dev, struct superblock *sb);
0238
                                                                                   0288 int
                                                                                                        dirlink(struct inode*, char*, uint);
0239
                                                                                   0289 struct inode*
                                                                                                        dirlookup(struct inode*, char*, uint*);
0240
                                                                                   0290 struct inode*
                                                                                                        ialloc(uint, short);
0241
                                                                                   0291 struct inode*
                                                                                                        idup(struct inode*);
0242
                                                                                   0292 void
                                                                                                        iinit(int dev);
0243
                                                                                   0293 void
                                                                                                        ilock(struct inode*);
0244
                                                                                   0294 void
                                                                                                        iput(struct inode*);
                                                                                   0295 void
0245
                                                                                                        iunlock(struct inode*);
0246
                                                                                   0296 void
                                                                                                        iunlockput(struct inode*);
0247
                                                                                   0297 void
                                                                                                        iupdate(struct inode*);
0248
                                                                                   0298 int
                                                                                                        namecmp(const char*, const char*);
0249
                                                                                   0299 struct inode*
                                                                                                        namei(char*);
```

Sheet 02 Sheet 02

0300 struct inode*	<pre>nameiparent(char*, char*);</pre>	0350 // pipe.c	
0301 int	readi(struct inode*, char*, uint, uint);	0351 int	<pre>pipealloc(struct file**, struct file**);</pre>
0302 void	<pre>stati(struct inode*, struct stat*);</pre>	0352 void	<pre>pipeclose(struct pipe*, int);</pre>
0303 int	<pre>writei(struct inode*, char*, uint, uint);</pre>	0353 int	<pre>piperead(struct pipe*, char*, int);</pre>
0304		0354 int	<pre>pipewrite(struct pipe*, char*, int);</pre>
0305 // ide.c		0355	
0306 void	<pre>ideinit(void);</pre>	0356	
0307 void	ideintr(void);	0357 // proc.c	
0308 void	iderw(struct buf*);	0358 int	<pre>cpuid(void);</pre>
0309		0359 void	<pre>exit(void);</pre>
0310 // ioapic.c		0360 int	<pre>fork(void);</pre>
0311 void	ioapicenable(int irq, int cpu);	0361 int	<pre>growproc(int);</pre>
0312 extern uchar	ioapicid;	0362 int	<pre>kill(int);</pre>
0313 void	<pre>ioapicinit(void);</pre>	0363 struct cpu*	<pre>mycpu(void);</pre>
0314		0364 struct proc*	<pre>myproc();</pre>
0315 // kalloc.c		0365 void	<pre>pinit(void);</pre>
0316 char*	kalloc(void);	0366 void	<pre>procdump(void);</pre>
0317 void	kfree(char*);	0367 void	<pre>scheduler(void)attribute((noreturn));</pre>
0318 void	kinit1(void*, void*);	0368 void	<pre>sched(void);</pre>
0319 void	kinit2(void*, void*);	0369 void	<pre>setproc(struct proc*);</pre>
0320		0370 void	<pre>sleep(void*, struct spinlock*);</pre>
0321 // kbd.c		0371 void	<pre>userinit(void);</pre>
0322 void	<pre>kbdintr(void);</pre>	0372 int	<pre>wait(void);</pre>
0323	• • • • • • • • • • • • • • • • • • • •	0373 void	<pre>wakeup(void*);</pre>
0324 // lapic.c		0374 void	yield(void);
0325 void	<pre>cmostime(struct rtcdate *r);</pre>	0375	
0326 int	<pre>lapicid(void);</pre>	0376 // swtch.S	
0327 extern volatile	1 3 77	0377 void	<pre>swtch(struct context**, struct context*);</pre>
0328 void	lapiceoi(void);	0378	,
0329 void	lapicinit(void);	0379 // spinlock.c	
0330 void	lapicstartap(uchar, uint);	0380 void	<pre>acquire(struct spinlock*);</pre>
0331 void	<pre>microdelay(int);</pre>	0381 void	<pre>getcallerpcs(void*, uint*);</pre>
0332	, (,	0382 int	holding(struct spinlock*);
0333 // log.c		0383 void	<pre>initlock(struct spinlock*, char*);</pre>
0334 void	<pre>initlog(int dev);</pre>	0384 void	release(struct spinlock*);
0335 void	log_write(struct buf*);	0385 void	pushcli(void);
0336 void	begin_op();	0386 void	popcli(void);
0337 void	end_op();	0387	poperi(void);
0338	επα_ορ();	0388 // sleeplock.c	
0339 // mp.c		0389 void	<pre>acquiresleep(struct sleeplock*);</pre>
0340 extern int	ismp;	0390 void	releasesleep(struct sleeplock*);
0341 void	mpinit(void);	0391 int	holdingsleep(struct sleeplock*);
0342	mprire (vora);	0392 void	<pre>initsleeplock(struct sleeplock*, char*);</pre>
0343 // picirq.c		0393	intesteephoek(seruet steephoek, char),
0344 void	<pre>picenable(int);</pre>	0393 0394 // string.c	
0345 void	picinit(void);	0394 // String.c 0395 int	<pre>memcmp(const void*, const void*, uint);</pre>
0345 VOTU	picinic(voiu),	0395 THE 0396 void*	
0347		0396 Void*	<pre>memmove(void*, const void*, uint); memset(void*, int, uint);</pre>
0348		0397 V010^ 0398 char*	
0348		0398 Char* 0399 int	<pre>safestrcpy(char*, const char*, int); strlen(const char*);</pre>
0343		OSS IIIL	Sti len(tuist that "),

```
0400 int
                     strncmp(const char*, const char*, uint);
                                                                                  0450 // Routines to let C code use special x86 instructions.
0401 char*
                     strncpy(char*, const char*, int);
                                                                                  0451
0402
                                                                                  0452 static inline uchar
0403 // syscall.c
                                                                                  0453 inb(ushort port)
0404 int
                     argint(int, int*);
                                                                                  0454 {
                     argptr(int, char**, int);
0405 int
                                                                                  0455 uchar data;
0406 int
                     argstr(int, char**);
                                                                                  0456
0407 int
                     fetchint(uint, int*);
                                                                                  0457
                                                                                        asm volatile("in %1,%0" : "=a" (data) : "d" (port));
0408 int
                     fetchstr(uint, char**);
                                                                                  0458
                                                                                         return data:
                                                                                  0459 }
0409 void
                     syscall(void);
0410
                                                                                  0460
0411 // timer.c
                                                                                  0461 static inline void
0412 void
                                                                                  0462 insl(int port, void *addr, int cnt)
                     timerinit(void);
0413
                                                                                  0463 {
0414 // trap.c
                                                                                  0464
                                                                                        asm volatile("cld: rep insl" :
0415 void
                                                                                  0465
                                                                                                       "=D" (addr), "=c" (cnt) :
                     idtinit(void);
0416 extern uint
                     ticks:
                                                                                  0466
                                                                                                       "d" (port), "0" (addr), "1" (cnt) :
0417 void
                     tvinit(void):
                                                                                  0467
                                                                                                       "memory", "cc");
0418 extern struct spinlock tickslock;
                                                                                  0468 }
0419
                                                                                  0469
0420 // uart.c
                                                                                  0470 static inline void
0421 void
                     uartinit(void);
                                                                                  0471 outb(ushort port, uchar data)
0422 void
                     uartintr(void);
                                                                                  0472 {
0423 void
                     uartputc(int);
                                                                                  0473 asm volatile("out %0,%1" : : "a" (data), "d" (port));
0424
                                                                                  0474 }
0425 // vm.c
                                                                                  0475
0426 void
                     seginit(void);
                                                                                  0476 static inline void
0427 void
                     kvmalloc(void);
                                                                                  0477 outw(ushort port, ushort data)
                     setupkvm(void);
0428 pde_t*
                                                                                  0479 asm volatile("out %0,%1" : : "a" (data), "d" (port));
0429 char*
                     uva2ka(pde_t*, char*);
0430 int
                     allocuvm(pde_t*, uint, uint);
                                                                                  0480 }
0431 int
                     deallocuvm(pde_t*, uint, uint);
                                                                                  0481
0432 void
                     freevm(pde_t*);
                                                                                  0482 static inline void
0433 void
                     inituvm(pde_t*, char*, uint);
                                                                                  0483 outsl(int port, const void *addr, int cnt)
0434 int
                     loaduvm(pde_t*, char*, struct inode*, uint, uint);
                                                                                  0484 {
                                                                                        asm volatile("cld; rep outsl" :
0435 pde_t*
                     copyuvm(pde_t*, uint);
                                                                                  0485
0436 void
                     switchuvm(struct proc*);
                                                                                  0486
                                                                                                       "=S" (addr), "=c" (cnt):
0437 void
                     switchkvm(void);
                                                                                  0487
                                                                                                       "d" (port), "0" (addr), "1" (cnt) :
0438 int
                     copyout(pde_t*, uint, void*, uint);
                                                                                  0488
                                                                                                       "cc"):
0439 void
                     clearpteu(pde_t *pgdir, char *uva);
                                                                                  0489 }
0440
                                                                                  0490
                                                                                  0491 static inline void
0441 // number of elements in fixed-size array
0442 #define NELEM(x) (sizeof(x)/sizeof((x)[0]))
                                                                                  0492 stosb(void *addr, int data, int cnt)
0443
                                                                                  0493 {
0444
                                                                                  0494
                                                                                        asm volatile("cld; rep stosb" :
0445
                                                                                                       "=D" (addr), "=c" (cnt):
                                                                                  0495
                                                                                                       "0" (addr), "1" (cnt), "a" (data) :
0446
                                                                                  0496
0447
                                                                                  0497
                                                                                                       "memory", "cc");
0448
                                                                                  0498 }
0449
                                                                                  0499
```

```
0500 static inline void
0501 stosl(void *addr, int data, int cnt)
0502 {
0503 asm volatile("cld; rep stosl" :
                   "=D" (addr), "=c" (cnt):
0504
0505
                   "0" (addr), "1" (cnt), "a" (data) :
                   "memory", "cc");
0506
0507 }
0508
0509 struct segdesc;
0510
0511 static inline void
0512 lqdt(struct segdesc *p, int size)
0513 {
0514 volatile ushort pd[3];
0515
0516 	 pd[0] = size-1;
0517 pd[1] = (uint)p;
0518 	 pd[2] = (uint)p >> 16;
0519
0520 asm volatile("lqdt (%0)" : : "r" (pd));
0521 }
0522
0523 struct gatedesc;
0524
0525 static inline void
0526 lidt(struct gatedesc *p, int size)
0527 {
0528 volatile ushort pd[3];
0529
0530 pd[0] = size-1;
0531 pd[1] = (uint)p;
0532 pd[2] = (uint)p >> 16;
0533
0534 asm volatile("lidt (%0)" : : "r" (pd));
0535 }
0536
0537 static inline void
0538 ltr(ushort sel)
0539 {
0540 asm volatile("ltr %0" : : "r" (sel));
0541 }
0542
0543 static inline uint
0544 readeflags(void)
0545 {
0546 uint eflags;
0547 asm volatile("pushfl; popl %0" : "=r" (eflags));
0548 return eflags;
0549 }
```

```
0550 static inline void
0551 loadgs(ushort v)
0552 {
0553 asm volatile("movw %0, %%qs" : : "r" (v));
0554 }
0555
0556 static inline void
0557 cli(void)
0558 {
0559 asm volatile("cli");
0560 }
0561
0562 static inline void
0563 sti(void)
0564 {
0565 asm volatile("sti");
0566 }
0567
0568 static inline uint
0569 xchg(volatile uint *addr, uint newval)
0570 {
0571 uint result;
0572
0573 // The + in "+m" denotes a read-modify-write operand.
0574 asm volatile("lock; xchql %0, %1":
0575
                   "+m" (*addr), "=a" (result):
0576
                   "1" (newval) :
0577
                   "cc");
0578 return result;
0579 }
0580
0581 static inline uint
0582 rcr2(void)
0583 {
0584 uint val;
0585 asm volatile("movl %%cr2,%0" : "=r" (val));
0586 return val;
0587 }
0588
0589 static inline void
0590 lcr3(uint val)
0591 {
0592 asm volatile("movl %0,%%cr3" : : "r" (val));
0593 }
0594
0595
0596
0597
0598
0599
```

```
0600 // Layout of the trap frame built on the stack by the
                                                                                  0650 //
0601 // hardware and by trapasm.S, and passed to trap().
                                                                                  0651 // assembler macros to create x86 segments
0602 struct trapframe {
                                                                                  0652 //
0603 // registers as pushed by pusha
                                                                                  0653
                                                                                  0654 #define SEG_NULLASM
0604 uint edi;
0605
      uint esi;
                                                                                  0655
                                                                                               .word 0, 0;
0606
      uint ebp;
                                                                                  0656
                                                                                               .byte 0, 0, 0, 0
0607
                      // useless & ignored
                                                                                  0657
      uint oesp;
0608
      uint ebx;
                                                                                  0658 // The 0xC0 means the limit is in 4096-byte units
                                                                                  0659 // and (for executable segments) 32-bit mode.
0609
      uint edx;
0610
      uint ecx;
                                                                                  0660 #define SEG_ASM(type,base,lim)
                                                                                               .word (((lim) >> 12) & 0xffff), ((base) & 0xffff);
0611
      uint eax;
                                                                                  0661
0612
                                                                                  0662
                                                                                               .byte (((base) >> 16) & 0xff), (0x90 | (type)),
0613
      // rest of trap frame
                                                                                  0663
                                                                                                       (0xC0 \mid (((1im) >> 28) \& 0xf)), (((base) >> 24) \& 0xff)
0614
                                                                                  0664
      ushort as:
0615
      ushort padding1;
                                                                                  0665 #define STA X
                                                                                                                   // Executable segment
                                                                                                         0x8
0616
      ushort fs;
                                                                                  0666 #define STA E
                                                                                                         0x4
                                                                                                                   // Expand down (non-executable segments)
0617
      ushort padding2;
                                                                                  0667 #define STA C
                                                                                                         0x4
                                                                                                                   // Conforming code segment (executable only)
0618
      ushort es;
                                                                                  0668 #define STA W
                                                                                                         0x2
                                                                                                                   // Writeable (non-executable segments)
0619
      ushort padding3;
                                                                                  0669 #define STA_R
                                                                                                         0x2
                                                                                                                   // Readable (executable segments)
0620
      ushort ds:
                                                                                  0670 #define STA A
                                                                                                         0x1
                                                                                                                   // Accessed
0621
      ushort padding4;
                                                                                  0671
0622
      uint trapno;
                                                                                  0672
0623
                                                                                  0673
0624
                                                                                  0674
      // below here defined by x86 hardware
0625
      uint err;
                                                                                  0675
0626
      uint eip;
                                                                                  0676
0627
      ushort cs;
                                                                                  0677
0628
      ushort padding5;
                                                                                  0678
0629
      uint eflags;
                                                                                  0679
0630
                                                                                  0680
0631
      // below here only when crossing rings, such as from user to kernel
                                                                                  0681
0632
      uint esp;
                                                                                  0682
0633
      ushort ss;
                                                                                  0683
0634
      ushort padding6;
                                                                                  0684
0635 };
                                                                                  0685
0636
                                                                                  0686
0637
                                                                                  0687
0638
                                                                                  0688
0639
                                                                                  0689
0640
                                                                                  0690
0641
                                                                                  0691
0642
                                                                                  0692
0643
                                                                                  0693
0644
                                                                                  0694
0645
                                                                                  0695
0646
                                                                                  0696
0647
                                                                                  0697
0648
                                                                                  0698
0649
                                                                                  0699
```

Sheet 06 Sheet 06

```
0750 #ifndef __ASSEMBLER__
0700 // This file contains definitions for the
0701 // x86 memory management unit (MMU).
                                                                                  0751 // Segment Descriptor
0702
                                                                                  0752 struct segdesc {
0703 // Eflags register
                                                                                  0753
                                                                                         uint lim_15_0 : 16; // Low bits of segment limit
0704 #define FL_CF
                             0x0000001
                                             // Carry Flag
                                                                                  0754
                                                                                         uint base_15_0 : 16; // Low bits of segment base address
                                                                                        uint base_23_16 : 8; // Middle bits of segment base address
0705 #define FL PF
                             0x00000004
                                             // Parity Flag
                                                                                  0755
0706 #define FL AF
                             0x00000010
                                             // Auxiliary carry Flag
                                                                                  0756
                                                                                         uint type : 4;
                                                                                                              // Segment type (see STS_ constants)
0707 #define FL_ZF
                             0x00000040
                                                                                  0757
                                                                                         uint s : 1;
                                                                                                              // 0 = system, 1 = application
                                             // Zero Flag
0708 #define FL_SF
                             0x00000080
                                             // Sign Flag
                                                                                  0758
                                                                                         uint dpl : 2;
                                                                                                              // Descriptor Privilege Level
                                             // Trap Flag
0709 #define FL_TF
                             0x00000100
                                                                                  0759
                                                                                         uint p : 1;
                                                                                                              // Present
0710 #define FL_IF
                             0x00000200
                                             // Interrupt Enable
                                                                                  0760
                                                                                         uint lim_19_16 : 4; // High bits of segment limit
0711 #define FL DF
                             0x00000400
                                             // Direction Flag
                                                                                  0761
                                                                                         uint avl : 1:
                                                                                                              // Unused (available for software use)
                                                                                  0762
                                                                                                              // Reserved
0712 #define FL OF
                             0x00000800
                                             // Overflow Flag
                                                                                         uint rsv1 : 1:
0713 #define FL_IOPL_MASK
                             0x00003000
                                             // I/O Privilege Level bitmask
                                                                                  0763
                                                                                         uint db : 1;
                                                                                                              // 0 = 16-bit segment, 1 = 32-bit segment
                                                                                                              // Granularity: limit scaled by 4K when set
0714 #define FL IOPL 0
                             0x00000000
                                                 IOPL == 0
                                                                                  0764
                                                                                         uint a : 1:
0715 #define FL IOPL 1
                             0x00001000
                                             //
                                                 IOPL == 1
                                                                                         uint base_31_24 : 8; // High bits of segment base address
                                                                                  0765
0716 #define FL_IOPL_2
                             0x00002000
                                             //
                                                 IOPL == 2
                                                                                  0766 };
0717 #define FL IOPL 3
                             0x00003000
                                             // IOPL == 3
                                                                                  0767
0718 #define FL NT
                             0x00004000
                                             // Nested Task
                                                                                  0768 // Normal segment
                                                                                  0769 #define SEG(type, base, lim, dpl) (struct segdesc)
0719 #define FL_RF
                             0x00010000
                                             // Resume Flag
0720 #define FL VM
                             0x00020000
                                             // Virtual 8086 mode
                                                                                  0770 { ((lim) >> 12) & 0xffff, (uint)(base) & 0xffff,
0721 #define FL AC
                             0x00040000
                                             // Alianment Check
                                                                                         ((uint)(base) >> 16) \& 0xff, type, 1, dpl, 1,
                                                                                         (uint)(lim) >> 28, 0, 0, 1, 1, (uint)(base) >> 24 }
0722 #define FL_VIF
                             0x00080000
                                             // Virtual Interrupt Flag
                                                                                  0772
0723 #define FL_VIP
                             0x00100000
                                             // Virtual Interrupt Pending
                                                                                  0773 #define SEG16(type, base, lim, dpl) (struct segdesc)
                                                                                  0774 { (lim) & 0xffff, (uint)(base) & 0xffff,
0724 #define FL ID
                             0x00200000
                                             // ID flag
                                                                                         ((uint)(base) >> 16) & 0xff, type, 1, dpl, 1,
0725
0726 // Control Register flags
                                                                                  0776
                                                                                         (uint)(lim) >> 16, 0, 0, 1, 0, (uint)(base) >> 24
0727 #define CRO_PE
                                                                                  0777 #endif
                             0x0000001
                                             // Protection Enable
                                             // Monitor coProcessor
0728 #define CRO_MP
                             0x00000002
                                                                                  0778
                                             // Emulation
0729 #define CR0 EM
                             0x00000004
                                                                                  0779 #define DPL USER
                                                                                                                   // User DPL
                                                                                                           0x3
0730 #define CRO_TS
                             0x00000008
                                             // Task Switched
                                                                                  0780
                                             // Extension Type
0731 #define CRO ET
                             0x00000010
                                                                                  0781 // Application segment type bits
0732 #define CRO NE
                             0x00000020
                                             // Numeric Errror
                                                                                  0782 #define STA X
                                                                                                           0x8
                                                                                                                   // Executable segment
0733 #define CRO_WP
                             0x00010000
                                             // Write Protect
                                                                                  0783 #define STA_E
                                                                                                                   // Expand down (non-executable segments)
                                                                                                           0x4
                                             // Alignment Mask
0734 #define CRO_AM
                             0x00040000
                                                                                  0784 #define STA C
                                                                                                           0x4
                                                                                                                   // Conforming code segment (executable only)
0735 #define CRO NW
                             0x20000000
                                             // Not Writethrough
                                                                                  0785 #define STA W
                                                                                                           0x2
                                                                                                                   // Writeable (non-executable segments)
0736 #define CR0 CD
                             0x40000000
                                             // Cache Disable
                                                                                  0786 #define STA_R
                                                                                                           0x2
                                                                                                                   // Readable (executable segments)
0737 #define CRO PG
                             0x80000000
                                             // Paging
                                                                                  0787 #define STA A
                                                                                                           0x1
                                                                                                                   // Accessed
0738
                                                                                  0788
0739 #define CR4_PSE
                             0x00000010
                                             // Page size extension
                                                                                  0789 // System segment type bits
0740
                                                                                  0790 #define STS T16A
                                                                                                           0x1
                                                                                                                   // Available 16-bit TSS
0741 // various segment selectors.
                                                                                  0791 #define STS LDT
                                                                                                           0x2
                                                                                                                   // Local Descriptor Table
0742 #define SEG_KCODE 1 // kernel code
                                                                                  0792 #define STS_T16B
                                                                                                           0x3
                                                                                                                   // Busy 16-bit TSS
0743 #define SEG_KDATA 2 // kernel data+stack
                                                                                  0793 #define STS_CG16
                                                                                                           0x4
                                                                                                                   // 16-bit Call Gate
0744 #define SEG UCODE 3 // user code
                                                                                  0794 #define STS TG
                                                                                                           0x5
                                                                                                                   // Task Gate / Coum Transmitions
0745 #define SEG_UDATA 4 // user data+stack
                                                                                  0795 #define STS_IG16
                                                                                                           0x6
                                                                                                                   // 16-bit Interrupt Gate
0746 #define SEG_TSS 5 // this process's task state
                                                                                  0796 #define STS_TG16
                                                                                                           0x7
                                                                                                                   // 16-bit Trap Gate
0747
                                                                                  0797 #define STS_T32A
                                                                                                           0x9
                                                                                                                   // Available 32-bit TSS
0748 // cpu->qdt[NSEGS] holds the above segments.
                                                                                  0798 #define STS_T32B
                                                                                                           0xB
                                                                                                                   // Busy 32-bit TSS
0749 #define NSEGS
                                                                                  0799 #define STS_CG32
                                                                                                           0xC
                                                                                                                   // 32-bit Call Gate
```

Sheet 07 Sheet 07

```
0800 #define STS IG32
                        0xE
                                // 32-bit Interrupt Gate
                                                                                0850 // Task state segment format
0801 #define STS_TG32
                        0xF
                                // 32-bit Trap Gate
                                                                                0851 struct taskstate {
0802
                                                                                0852 uint link;
                                                                                                          // Old ts selector
0803 // A virtual address 'la' has a three-part structure as follows:
                                                                                0853
                                                                                       uint esp0;
                                                                                                          // Stack pointers and segment selectors
                                                                                0854
                                                                                       ushort ss0;
                                                                                                          // after an increase in privilege level
0805 // +-----10-----+------12------+
                                                                                       ushort padding1;
                                                                                0855
0806 // | Page Directory | Page Table | Offset within Page |
                                                                                0856
                                                                                       uint *esp1;
0807 // |
              Index
                                                                                0857
                                                                                       ushort ss1;
0808 // +----
                                                                                0858
                                                                                       ushort padding2;
0809 // \--- PDX(va) --/ \--- PTX(va) --/
                                                                                0859
                                                                                       uint *esp2;
0810
                                                                                0860
                                                                                       ushort ss2;
0811 // page directory index
                                                                                0861
                                                                                       ushort padding3;
0812 #define PDX(va)
                                                                                       void *cr3:
                            (((uint)(va) >> PDXSHIFT) & 0x3FF)
                                                                                0862
                                                                                                          // Page directory base
0813
                                                                                0863
                                                                                       uint *eip;
                                                                                                          // Saved state from last task switch
0814 // page table index
                                                                                0864
                                                                                       uint eflags:
0815 #define PTX(va)
                                                                                       uint eax:
                            (((uint)(va) >> PTXSHIFT) & 0x3FF)
                                                                                0865
                                                                                                          // More saved state (registers)
0816
                                                                                0866
                                                                                       uint ecx;
0817 // construct virtual address from indexes and offset
                                                                                0867
                                                                                       uint edx:
0818 #define PGADDR(d, t, o) ((uint)((d) << PDXSHIFT | (t) << PTXSHIFT | (o)))
                                                                                0868
                                                                                       uint ebx:
0819
                                                                                0869
                                                                                       uint *esp;
0820 // Page directory and page table constants.
                                                                                0870
                                                                                       uint *ebp:
0821 #define NPDENTRIES
                            1024
                                    // # directory entries per page directory
                                                                                0871
                                                                                       uint esi:
0822 #define NPTENTRIES
                            1024
                                    // # PTEs per page table
                                                                                0872
                                                                                       uint edi;
0823 #define PGSIZE
                            4096
                                    // bytes mapped by a page
                                                                                0873
                                                                                       ushort es;
                                                                                                          // Even more saved state (segment selectors)
0824
                                                                                0874
                                                                                       ushort padding4:
0825 #define PGSHIFT
                                    // log2(PGSIZE)
                            12
                                                                                0875
                                                                                       ushort cs:
0826 #define PTXSHIFT
                            12
                                    // offset of PTX in a linear address
                                                                                0876
                                                                                       ushort padding5;
0827 #define PDXSHIFT
                            22
                                    // offset of PDX in a linear address
                                                                                0877
                                                                                       ushort ss;
0828
                                                                                0878
                                                                                       ushort padding6;
0829 #define PGROUNDUP(sz) (((sz)+PGSIZE-1) & ~(PGSIZE-1))
                                                                                       ushort ds;
                                                                                0879
0830 #define PGROUNDDOWN(a) (((a)) & ~(PGSIZE-1))
                                                                                0880
                                                                                       ushort padding7;
                                                                                       ushort fs;
0831
                                                                                0881
0832 // Page table/directory entry flags.
                                                                                0882
                                                                                       ushort padding8;
0833 #define PTE_P
                            0x001 // Present
                                                                                0883
                                                                                       ushort gs;
0834 #define PTE_W
                            0x002 // Writeable
                                                                                0884
                                                                                       ushort padding9;
0835 #define PTE U
                            0x004 // User
                                                                                0885
                                                                                       ushort 1dt;
0836 #define PTE_PWT
                            0x008 // Write-Through
                                                                                0886
                                                                                       ushort padding10;
0837 #define PTE PCD
                            0x010 // Cache-Disable
                                                                                0887
                                                                                       ushort t:
                                                                                                          // Trap on task switch
0838 #define PTE A
                            0x020 // Accessed
                                                                                0888 ushort iomb;
                                                                                                          // I/O map base address
0839 #define PTE_D
                            0x040 // Dirty
                                                                                0889 };
0840 #define PTE PS
                            0x080
                                   // Page Size
                                                                                0890
0841 #define PTE MBZ
                            0x180 // Bits must be zero
                                                                                0891
0842
                                                                                0892
0843 // Address in page table or page directory entry
                                                                                0893
0844 #define PTE ADDR(pte) ((uint)(pte) & ~0xFFF)
                                                                                0894
0845 #define PTE_FLAGS(pte) ((uint)(pte) & 0xFFF)
                                                                                0895
0846
                                                                                0896
0847 #ifndef __ASSEMBLER__
                                                                                0897
0848 typedef uint pte_t;
                                                                                0898
0849
                                                                                0899
```

Sheet 08 Sheet 08

```
0900 // Gate descriptors for interrupts and traps
                                                                                 0950 // Format of an ELF executable file
0901 struct gatedesc {
                                                                                 0951
0902
      uint off_15_0 : 16; // low 16 bits of offset in segment
                                                                                 0952 #define ELF_MAGIC 0x464C457FU // "\x7FELF" in little endian
0903
      uint cs : 16;
                            // code segment selector
                                                                                 0953
0904
      uint args : 5;
                            // # args, 0 for interrupt/trap gates
                                                                                 0954 // File header
      uint rsv1 : 3;
0905
                            // reserved(should be zero I guess)
                                                                                 0955 struct elfhdr {
0906
      uint type : 4;
                            // type(STS_{TG,IG32,TG32})
                                                                                 0956 uint magic; // must equal ELF_MAGIC
0907
      uint s : 1;
                            // must be 0 (system)
                                                                                 0957
                                                                                       uchar elf[12];
0908
      uint dpl : 2;
                            // descriptor(meaning new) privilege level
                                                                                 0958
                                                                                        ushort type;
                            // Present
0909
      uint p : 1;
                                                                                 0959
                                                                                        ushort machine;
0910 uint off_31_16 : 16; // high bits of offset in segment
                                                                                 0960
                                                                                       uint version;
0911 };
                                                                                 0961 uint entry:
0912
                                                                                 0962
                                                                                       uint phoff;
0913 // Set up a normal interrupt/trap gate descriptor.
                                                                                 0963
                                                                                        uint shoff;
                                                                                        uint flags:
0914 // - istrap: 1 for a trap (= exception) gate, 0 for an interrupt gate.
                                                                                 0964
0915 // interrupt gate clears FL_IF, trap gate leaves FL_IF alone
                                                                                 0965
                                                                                        ushort ehsize:
0916 // - sel: Code segment selector for interrupt/trap handler
                                                                                 0966
                                                                                       ushort phentsize;
0917 // - off: Offset in code segment for interrupt/trap handler
                                                                                 0967
                                                                                       ushort phnum:
0918 // - dpl: Descriptor Privilege Level -
                                                                                 0968
                                                                                       ushort shentsize:
0919 //
              the privilege level required for software to invoke
                                                                                 0969
                                                                                        ushort shnum;
0920 //
              this interrupt/trap gate explicitly using an int instruction.
                                                                                 0970
                                                                                       ushort shstrndx:
0921 #define SETGATE(gate, istrap, sel, off, d)
                                                                                 0971 }:
0922 {
                                                                                 0972
0923
      (gate).off_15_0 = (uint)(off) & 0xffff;
                                                                                 0973 // Program section header
0924
      (qate).cs = (sel):
                                                                                 0974 struct proahdr {
0925
                                                                                 0975 uint type;
       (qate).args = 0;
0926
       (gate).rsv1 = 0;
                                                                                 0976
                                                                                       uint off;
0927
       (gate).type = (istrap) ? STS_TG32 : STS_IG32;
                                                                                 0977
                                                                                        uint vaddr;
0928
                                                                                 0978
                                                                                       uint paddr;
       (qate).s = 0;
0929
       (qate).dpl = (d);
                                                                                 0979
                                                                                       uint filesz;
0930
                                                                                 0980
                                                                                      uint memsz;
      (gate).p = 1;
0931
      (gate).off_31_16 = (uint)(off) >> 16;
                                                                                 0981 uint flags;
0932 }
                                                                                 0982 uint align;
0933
                                                                                 0983 };
0934 #endif
                                                                                 0984
0935
                                                                                 0985 // Values for Proghdr type
0936
                                                                                 0986 #define ELF_PROG_LOAD
                                                                                                                     1
0937
                                                                                 0987
0938
                                                                                 0988 // Flag bits for Proghdr flags
0939
                                                                                 0989 #define ELF_PROG_FLAG_EXEC
                                                                                                                     1
0940
                                                                                 0990 #define ELF PROG FLAG WRITE
                                                                                                                     2
0941
                                                                                 0991 #define ELF_PROG_FLAG_READ
                                                                                                                     4
0942
                                                                                 0992
0943
                                                                                 0993
0944
                                                                                 0994
0945
                                                                                 0995
0946
                                                                                 0996
0947
                                                                                 0997
0948
                                                                                 0998
0949
                                                                                 0999
```

Sheet 09 Sheet 09

```
1000 # The xv6 kernel starts executing in this file. This file is linked with
                                                                                  1050
                                                                                         mov1
                                                                                                 $(V2P_W0(entrypgdir)), %eax
1001 # the kernel C code, so it can refer to kernel symbols such as main().
                                                                                  1051
                                                                                         mov1
                                                                                                 %eax, %cr3
1002 # The boot block (bootasm.S and bootmain.c) jumps to entry below.
                                                                                  1052
                                                                                         # Turn on paging.
1003
                                                                                  1053
                                                                                         mov1
                                                                                                 %cr0, %eax
1004 # Multiboot header, for multiboot boot loaders like GNU Grub.
                                                                                  1054
                                                                                         orl
                                                                                                 $(CRO_PG|CRO_WP), %eax
1005 # http://www.gnu.org/software/grub/manual/multiboot/multiboot.html
                                                                                  1055
                                                                                         mov1
                                                                                                 %eax, %cr0
1006 #
                                                                                  1056
1007 # Using GRUB 2, you can boot xv6 from a file stored in a
                                                                                  1057
                                                                                         # Set up the stack pointer.
1008 # Linux file system by copying kernel or kernelmemfs to /boot
                                                                                  1058
                                                                                         movl $(stack + KSTACKSIZE), %esp
1009 # and then adding this menu entry:
                                                                                  1059
1010 #
                                                                                  1060
                                                                                         # Jump to main(), and switch to executing at
1011 # menuentry "xv6" {
                                                                                  1061
                                                                                        # high addresses. The indirect call is needed because
1012 # insmod ext2
                                                                                  1062
                                                                                         # the assembler produces a PC-relative instruction
1013 # set root='(hd0,msdos1)'
                                                                                  1063
                                                                                         # for a direct jump.
1014 # set kernel='/boot/kernel'
                                                                                         mov $main. %eax
                                                                                  1064
1015 # echo "Loading ${kernel}..."
                                                                                  1065
                                                                                         jmp *%eax
1016 # multiboot ${kernel} ${kernel}
                                                                                  1066
1017 # boot
                                                                                  1067 .comm stack. KSTACKSIZE
1018 # }
                                                                                  1068
1019
                                                                                  1069
1020 #include "asm.h"
                                                                                  1070
1021 #include "memlayout.h"
                                                                                  1071
1022 #include "mmu.h"
                                                                                  1072
1023 #include "param.h"
                                                                                  1073
1024
                                                                                  1074
1025 # Multiboot header. Data to direct multiboot loader.
                                                                                  1075
1026 .p2align 2
                                                                                  1076
1027 .text
                                                                                  1077
1028 .globl multiboot_header
                                                                                  1078
1029 multiboot_header:
                                                                                  1079
1030 #define magic 0x1badb002
                                                                                  1080
1031 #define flags 0
                                                                                  1081
1032 .long magic
                                                                                  1082
1033 .long flags
                                                                                  1083
1034
      .long (-magic-flags)
                                                                                  1084
1035
                                                                                  1085
1036 # By convention, the _start symbol specifies the ELF entry point.
                                                                                  1086
1037 # Since we haven't set up virtual memory yet, our entry point is
                                                                                  1087
1038 # the physical address of 'entry'.
                                                                                  1088
1039 .globl _start
                                                                                  1089
1040 = V2P_W0(entry)
                                                                                  1090
1041
                                                                                  1091
1042 # Entering xv6 on boot processor, with paging off.
                                                                                  1092
1043 .globl entry
                                                                                  1093
1044 entry:
                                                                                  1094
1045
      # Turn on page size extension for 4Mbyte pages
                                                                                  1095
1046
      mov1
              %cr4, %eax
                                                                                  1096
1047
      orl
               $(CR4_PSE), %eax
                                                                                  1097
1048
      mov1
              %eax, %cr4
                                                                                  1098
1049
       # Set page directory
                                                                                  1099
```

Sheet 10 Sheet 10

```
1100 #include "asm.h"
                                                                                  1150 .code32 # Tell assembler to generate 32-bit code now.
1101 #include "memlayout.h"
                                                                                  1151 start32:
1102 #include "mmu.h"
                                                                                 1152
                                                                                        # Set up the protected-mode data segment registers
1103
                                                                                 1153
                                                                                        movw
                                                                                                 $(SEG_KDATA<<3), %ax
                                                                                                                        # Our data segment selector
1104 # Each non-boot CPU ("AP") is started up in response to a STARTUP
                                                                                 1154
                                                                                        movw
                                                                                                 %ax, %ds
                                                                                                                        # -> DS: Data Segment
                                                                                                 %ax, %es
1105 # IPI from the boot CPU. Section B.4.2 of the Multi-Processor
                                                                                 1155
                                                                                                                        # -> ES: Extra Segment
                                                                                        movw
1106 # Specification says that the AP will start in real mode with CS:IP
                                                                                 1156
                                                                                                 %ax, %ss
                                                                                                                        # -> SS: Stack Segment
                                                                                        movw
1107 # set to XY00:0000, where XY is an 8-bit value sent with the
                                                                                 1157
                                                                                                 $0, %ax
                                                                                                                        # Zero segments not ready for use
                                                                                         movw
1108 # STARTUP. Thus this code must start at a 4096-byte boundary.
                                                                                  1158
                                                                                                 %ax, %fs
                                                                                                                        # -> FS
                                                                                        movw
1109 #
                                                                                                                        # -> GS
                                                                                 1159
                                                                                                 %ax, %qs
                                                                                        movw
1110 # Because this code sets DS to zero, it must sit
                                                                                  1160
1111 # at an address in the low 2^16 bytes.
                                                                                  1161
                                                                                        # Turn on page size extension for 4Mbyte pages
1112 #
                                                                                 1162
                                                                                        movl
                                                                                                 %cr4. %eax
1113 # Startothers (in main.c) sends the STARTUPs one at a time.
                                                                                  1163
                                                                                        orl
                                                                                                 $(CR4_PSE), %eax
1114 # It copies this code (start) at 0x7000. It puts the address of
                                                                                 1164
                                                                                        mov1
                                                                                                 %eax. %cr4
1115 # a newly allocated per-core stack in start-4, the address of the
                                                                                 1165
                                                                                         # Use entrypgdir as our initial page table
1116 # place to jump to (mpenter) in start-8, and the physical address
                                                                                  1166
                                                                                        mov1
                                                                                                 (start-12), %eax
1117 # of entrypadir in start-12.
                                                                                 1167
                                                                                        mov1
                                                                                                 %eax. %cr3
1118 #
                                                                                 1168
                                                                                        # Turn on paging.
1119 # This code combines elements of bootasm.S and entry.S.
                                                                                  1169
                                                                                        mov1
                                                                                                 %cr0, %eax
1120
                                                                                 1170
                                                                                        orl
                                                                                                 $(CRO_PE|CRO_PG|CRO_WP), %eax
1121 .code16
                                                                                 1171
                                                                                        mov1
                                                                                                 %eax. %cr0
1122 .globl start
                                                                                  1172
1123 start:
                                                                                 1173
                                                                                        # Switch to the stack allocated by startothers()
1124
      c1i
                                                                                 1174
                                                                                        mov1
                                                                                                 (start-4), %esp
1125
                                                                                        # Call mpenter()
                                                                                  1175
1126
      # Zero data segment registers DS, ES, and SS.
                                                                                 1176
                                                                                        call.
                                                                                                  *(start-8)
1127
      xorw
              %ax,%ax
                                                                                 1177
1128
              %ax,%ds
                                                                                  1178
                                                                                                 $0x8a00, %ax
      movw
                                                                                        movw
1129
              %ax,%es
                                                                                 1179
                                                                                                 %ax, %dx
      movw
                                                                                        movw
1130
              %ax,%ss
                                                                                 1180
                                                                                                 %ax, %dx
      movw
                                                                                        outw
1131
                                                                                  1181
                                                                                                 $0x8ae0, %ax
                                                                                        movw
1132
      # Switch from real to protected mode. Use a bootstrap GDT that makes
                                                                                  1182
                                                                                        outw
                                                                                                 %ax, %dx
1133
      # virtual addresses map directly to physical addresses so that the
                                                                                  1183 spin:
1134
      # effective memory map doesn't change during the transition.
                                                                                  1184
                                                                                       qmj
                                                                                                 spin
1135
      ladt
              adtdesc
                                                                                  1185
1136
      mov1
              %cr0, %eax
                                                                                  1186 .p2align 2
1137
      orl
               $CRO_PE, %eax
                                                                                  1187 gdt:
1138
      mov1
              %eax. %cr0
                                                                                  1188 SEG NULLASM
1139
                                                                                  1189
                                                                                        SEG_ASM(STA_X|STA_R, 0, 0xffffffff)
      # Complete the transition to 32-bit protected mode by using a long imp
                                                                                  1190
                                                                                        SEG_ASM(STA_W, 0, 0xffffffff)
      # to reload %cs and %eip. The segment descriptors are set up with no
                                                                                  1191
1142
      # translation, so that the mapping is still the identity mapping.
                                                                                  1192
1143
              $(SEG_KCODE<<3), $(start32)
                                                                                  1193 gdtdesc:
1144
                                                                                 1194
                                                                                         .word
                                                                                                 (qdtdesc - qdt - 1)
1145
                                                                                 1195
                                                                                         .long
                                                                                                 qdt
1146
                                                                                  1196
1147
                                                                                 1197
1148
                                                                                 1198
1149
                                                                                 1199
```

Sheet 11 Sheet 11

```
1200 #include "types.h"
                                                                                 1250 // Common CPU setup code.
1201 #include "defs.h"
                                                                                 1251 static void
1202 #include "param.h"
                                                                                1252 mpmain(void)
1203 #include "memlayout.h"
                                                                                1253 {
1204 #include "mmu.h"
                                                                                 1254 cprintf("cpu%d: starting %d\n", cpuid(), cpuid());
1205 #include "proc.h"
                                                                                 1255 idtinit();
                                                                                                        // load idt register
1206 #include "x86.h"
                                                                                 1256
                                                                                      xchg(&(mycpu()->started), 1); // tell startothers() we're up
1207
                                                                                       scheduler();
                                                                                                        // start running processes
                                                                                 1257
1208 static void startothers(void);
                                                                                 1258 }
1209 static void mpmain(void) __attribute__((noreturn));
                                                                                 1259
1210 extern pde_t *kpgdir;
                                                                                 1260 pde_t entrypgdir[]; // For entry.S
1211 extern char end[]; // first address after kernel loaded from ELF file
                                                                                 1261
1212
                                                                                 1262 // Start the non-boot (AP) processors.
1213 // Bootstrap processor starts running C code here.
                                                                                 1263 static void
1214 // Allocate a real stack and switch to it. first
                                                                                1264 startothers(void)
1215 // doing some setup required for memory allocator to work.
                                                                                1265 {
1216 int
                                                                                 1266 extern uchar _binary_entryother_start[], _binary_entryother_size[];
1217 main(void)
                                                                                 1267
                                                                                       uchar *code:
1218 {
                                                                                1268
                                                                                      struct cpu *c:
1219 kinit1(end, P2V(4*1024*1024)); // phys page allocator
                                                                                 1269
                                                                                       char *stack;
1220 kvmalloc():
                       // kernel page table
                                                                                1270
1221 mpinit():
                       // detect other processors
                                                                                1271
                                                                                       // Write entry code to unused memory at 0x7000.
1222 lapicinit();
                       // interrupt controller
                                                                                 1272
                                                                                       // The linker has placed the image of entryother.S in
1223
      seginit();
                       // segment descriptors
                                                                                1273 // _binary_entryother_start.
1224
      picinit():
                       // disable pic
                                                                                1274
                                                                                      code = P2V(0x7000):
1225
                                                                                 1275
      ioapicinit();
                       // another interrupt controller
                                                                                       memmove(code, _binary_entryother_start, (uint)_binary_entryother_size);
1226
      consoleinit(); // console hardware
                                                                                1276
1227
      uartinit();
                       // serial port
                                                                                1277
                                                                                        for(c = cpus; c < cpus+ncpu; c++){</pre>
1228
                       // process table
                                                                                 1278
      pinit();
                                                                                         if(c == mycpu()) // We've started already.
1229 tvinit();
                       // trap vectors
                                                                                 1279
                                                                                            continue;
1230 binit();
                       // buffer cache
                                                                                 1280
1231 fileinit();
                       // file table
                                                                                 1281
                                                                                         // Tell entryother.S what stack to use, where to enter, and what
1232 ideinit();
                       // disk
                                                                                1282
                                                                                         // pgdir to use. We cannot use kpgdir yet, because the AP processor
1233
      startothers(); // start other processors
                                                                                 1283
                                                                                         // is running in low memory, so we use entrypgdir for the APs too.
1234
      kinit2(P2V(4*1024*1024), P2V(PHYSTOP)); // must come after startothers()
                                                                                1284
                                                                                         stack = kalloc();
                                                                                         *(void**)(code-4) = stack + KSTACKSIZE;
1235 userinit():
                       // first user process
                                                                                 1285
1236 mpmain();
                       // finish this processor's setup
                                                                                 1286
                                                                                         *(void**)(code-8) = mpenter;
1237 }
                                                                                 1287
                                                                                         *(int**)(code-12) = (void *) V2P(entrypgdir);
1238
                                                                                1288
1239 // Other CPUs jump here from entryother.S.
                                                                                 1289
                                                                                         lapicstartap(c->apicid, V2P(code));
1240 static void
                                                                                 1290
1241 mpenter(void)
                                                                                1291
                                                                                         // wait for cpu to finish mpmain()
1242 {
                                                                                 1292
                                                                                         while(c->started == 0)
1243 switchkvm();
                                                                                 1293
                                                                                1294 }
1244 seginit();
                                                                                1295 }
1245 lapicinit();
1246 mpmain();
                                                                                 1296
1247 }
                                                                                1297
1248
                                                                                 1298
1249
                                                                                1299
```

Sheet 12 Sheet 12

1300 // The boot page table used in entry.S and entryother.S.	1350 // Blank page.
1301 // Page directories (and page tables) must start on page boundaries,	1351
1302 // hence thealigned attribute.	1352
1303 // PTE_PS in a page directory entry enables 4Mbyte pages.	1353
1304	1354
1305attribute((aligned(PGSIZE)))	1355
1306 pde_t entrypgdir[NPDENTRIES] = {	1356
1307 // Map VA's [0, 4MB) to PA's [0, 4MB)	1357
1308 $[0] = (0) \mid PTE_P \mid PTE_W \mid PTE_PS,$	1358
1309 // Map VA's [KERNBASE, KERNBASE+4MB) to PA's [0, 4MB)	1359
1310 [KERNBASE>>PDXSHIFT] = (0) PTE_P PTE_W PTE_PS,	1360
1311 };	1361
1312	1362
1313	1363
1314	1364
1315	1365
1316	1366
1317	1367
1318	1368
1319	1369
1320	1370
1321	1371
1322	1372
1323	1373
1324	1374
1325	1375
1326	1376
1327	1377
1328	1378
1329	1379
1330	1380
1331	1381
1332	1382
1333	1383
1334	1384
1335	1385
1336	1386
1337	1387
1338	1388
1339	1389
1340	1390
1341	1391
1342	1392
1343	1393
1344	1394
1345	1395
1346	1396
1347	1397
1348	1398
1349	1399

1400 // Blank page.	1450 // Blank page.
1401	1451
1402	1452
1403	1453
1404	1454
1405	1455
1406	1456
1407	1457
1408	1458
1409	1459
1410	1460
1411	1461
1412	1462
1413	1463
1414	1464
1415	1465
1416	1466
1417	1467
1418	1468
1419	1469
1420	1470
1421	1471
1422	1472
1423	1473
1424	1474
1425	1475
1426	1476
1427	1477
1428	1478
1429	1479
1430	1480
1431	1481
1432	1482
1433	1483
1434	1484
1435	1485
1436	1486
1437	1487
1438	1488
1439	1489
1440	1490
1441	1491
1442	1492
1443	1493
1444	1494
1445	1495
1446	1496
1447	1497
1448	1498
1449	1499

```
1500 // Mutual exclusion lock.
                                                                                   1550 // Mutual exclusion spin locks.
1501 struct spinlock {
                                                                                   1551
1502
      uint locked;
                          // Is the lock held?
                                                                                   1552 #include "types.h"
                                                                                   1553 #include "defs.h"
1503
1504
      // For debugging:
                                                                                   1554 #include "param.h"
1505
       char *name;
                          // Name of lock.
                                                                                   1555 #include "x86.h"
1506
       struct cpu *cpu;
                         // The cpu holding the lock.
                                                                                   1556 #include "memlayout.h"
1507
       uint pcs[10];
                          // The call stack (an array of program counters)
                                                                                   1557 #include "mmu.h"
1508
                          // that locked the lock.
                                                                                   1558 #include "proc.h"
1509 };
                                                                                   1559 #include "spinlock.h"
1510
                                                                                   1560
1511
                                                                                   1561 void
1512
                                                                                   1562 initlock(struct spinlock *lk, char *name)
1513
                                                                                   1563 {
                                                                                   1564 1k->name = name:
1514
1515
                                                                                   1565 1k \rightarrow 1ocked = 0;
                                                                                   1566 1k - \text{cpu} = 0;
1516
1517
                                                                                   1567 }
1518
                                                                                   1568
1519
                                                                                   1569 // Acquire the lock.
1520
                                                                                   1570 // Loops (spins) until the lock is acquired.
1521
                                                                                   1571 // Holding a lock for a long time may cause
                                                                                   1572 // other CPUs to waste time spinning to acquire it.
1522
1523
                                                                                   1573 void
1524
                                                                                   1574 acquire(struct spinlock *lk)
1525
                                                                                   1575 {
1526
                                                                                   1576
                                                                                          pushcli(); // disable interrupts to avoid deadlock.
1527
                                                                                          if(holding(lk))
                                                                                   1577
1528
                                                                                   1578
                                                                                            panic("acquire");
1529
                                                                                   1579
1530
                                                                                   1580
                                                                                         // The xchg is atomic.
1531
                                                                                   1581
                                                                                         while(xchg(&lk->locked, 1) != 0)
                                                                                   1582
1532
                                                                                            ;
1533
                                                                                   1583
1534
                                                                                   1584 // Tell the C compiler and the processor to not move loads or stores
1535
                                                                                   1585 // past this point, to ensure that the critical section's memory
1536
                                                                                   1586
                                                                                         // references happen after the lock is acquired.
1537
                                                                                   1587
                                                                                          __sync_synchronize();
1538
                                                                                   1588
1539
                                                                                   1589
                                                                                         // Record info about lock acquisition for debugging.
1540
                                                                                   1590
                                                                                          1k \rightarrow cpu = mycpu();
1541
                                                                                   1591
                                                                                          getcallerpcs(&lk, lk->pcs);
1542
                                                                                   1592 }
1543
                                                                                   1593
1544
                                                                                   1594
                                                                                   1595
1545
1546
                                                                                   1596
1547
                                                                                   1597
1548
                                                                                   1598
1549
                                                                                   1599
```

```
1600 // Release the lock.
                                                                                   1650 // Pushcli/popcli are like cli/sti except that they are matched:
1601 void
                                                                                   1651 // it takes two popcli to undo two pushcli. Also, if interrupts
1602 release(struct spinlock *lk)
                                                                                   1652 // are off, then pushcli, popcli leaves them off.
1603 {
                                                                                   1653
1604 if(!holding(lk))
                                                                                   1654 void
         panic("release");
                                                                                   1655 pushcli(void)
1605
1606
                                                                                   1656 {
1607
      1k \rightarrow pcs[0] = 0;
                                                                                   1657 int eflags;
1608 \quad 1k \rightarrow cpu = 0;
                                                                                   1658
1609
                                                                                   1659
                                                                                          eflags = readeflags();
1610 // Tell the C compiler and the processor to not move loads or stores
                                                                                   1660
                                                                                          cli();
1611 // past this point, to ensure that all the stores in the critical
                                                                                   1661
                                                                                         if(mycpu()->ncli == 0)
                                                                                   1662
                                                                                            mycpu()->intena = eflags & FL_IF;
1612 // section are visible to other cores before the lock is released.
1613 // Both the C compiler and the hardware may re-order loads and
                                                                                   1663
                                                                                          mycpu()->ncli += 1;
                                                                                   1664 }
1614 // stores; __sync_synchronize() tells them both not to.
1615
      __sync_synchronize();
                                                                                   1665
1616
                                                                                   1666 void
1617 // Release the lock, equivalent to lk \rightarrow locked = 0.
                                                                                   1667 popcli(void)
1618 // This code can't use a C assignment, since it might
                                                                                   1668 {
1619
      // not be atomic. A real OS would use C atomics here.
                                                                                   1669
                                                                                         if(readeflags()&FL_IF)
1620
       asm volatile("movl $0, %0" : "+m" (lk->locked) : );
                                                                                   1670
                                                                                            panic("popcli - interruptible");
1621
                                                                                   1671
                                                                                          if(--mycpu()->ncli < 0)</pre>
1622 popcli();
                                                                                   1672
                                                                                            panic("popcli");
1623 }
                                                                                   1673
                                                                                          if(mycpu()->ncli == 0 && mycpu()->intena)
1624
                                                                                   1674
                                                                                            sti():
1625 // Record the current call stack in pcs[] by following the %ebp chain.
                                                                                   1675 }
1626 void
                                                                                   1676
1627 getcallerpcs(void *v, uint pcs[])
                                                                                   1677
1628 {
                                                                                   1678
1629 uint *ebp;
                                                                                   1679
1630
      int i;
                                                                                   1680
1631
                                                                                   1681
1632
       ebp = (uint*)v - 2;
                                                                                   1682
1633
       for(i = 0; i < 10; i++){
                                                                                   1683
1634
        if(ebp == 0 || ebp < (uint*)KERNBASE || ebp == (uint*)Oxffffffff)</pre>
                                                                                   1684
1635
                                                                                   1685
           break:
1636
         pcs[i] = ebp[1];
                             // saved %eip
                                                                                   1686
1637
         ebp = (uint*)ebp[0]; // saved %ebp
                                                                                   1687
1638
                                                                                   1688
1639
       for(; i < 10; i++)
                                                                                   1689
1640
        pcs[i] = 0;
                                                                                   1690
1641 }
                                                                                   1691
1642
                                                                                   1692
1643 // Check whether this cpu is holding the lock.
                                                                                   1693
1644 int
                                                                                   1694
1645 holding(struct spinlock *lock)
                                                                                   1695
1646 {
                                                                                   1696
1647 return lock->locked && lock->cpu == mycpu();
                                                                                   1697
1648 }
                                                                                   1698
1649
                                                                                   1699
```

Sheet 16 Sheet 16

```
1700 #include "param.h"
                                                                                 1750
                                                                                          // entries. if necessarv.
1701 #include "types.h"
                                                                                 1751
                                                                                          *pde = V2P(pgtab) | PTE_P | PTE_W | PTE_U;
1702 #include "defs.h"
                                                                                 1752 }
1703 #include "x86.h"
                                                                                 1753 return &pgtab[PTX(va)];
1704 #include "memlayout.h"
                                                                                 1754 }
1705 #include "mmu.h"
                                                                                 1755
1706 #include "proc.h"
                                                                                 1756 // Create PTEs for virtual addresses starting at va that refer to
1707 #include "elf.h"
                                                                                 1757 // physical addresses starting at pa. va and size might not
1708
                                                                                 1758 // be page-aligned.
                                                                                 1759 static int
1709 extern char data[]; // defined by kernel.ld
1710 pde_t *kpgdir; // for use in scheduler()
                                                                                 1760 mappages(pde_t *pgdir, void *va, uint size, uint pa, int perm)
                                                                                 1762 char *a, *last;
1712 // Set up CPU's kernel segment descriptors.
1713 // Run once on entry on each CPU.
                                                                                 1763
                                                                                        pte_t *pte;
1714 void
                                                                                 1764
1715 seginit(void)
                                                                                 1765
                                                                                        a = (char*)PGROUNDDOWN((uint)va);
1716 {
                                                                                 1766
                                                                                        last = (char*)PGROUNDDOWN(((uint)va) + size - 1);
1717 struct cpu *c:
                                                                                 1767
                                                                                        for(::){
1718
                                                                                 1768
                                                                                          if((pte = walkpgdir(pgdir, a, 1)) == 0)
1719 // Map "logical" addresses to virtual addresses using identity map.
                                                                                 1769
                                                                                            return -1;
1720 // Cannot share a CODE descriptor for both kernel and user
                                                                                 1770
                                                                                          if(*pte & PTE P)
1721 // because it would have to have DPL_USR, but the CPU forbids
                                                                                 1771
                                                                                            panic("remap"):
1722 // an interrupt from CPL=0 to DPL=3.
                                                                                 1772
                                                                                          *pte = pa | perm | PTE_P;
1723 c = &cpus[cpuid()];
                                                                                 1773
                                                                                          if(a == last)
1724 c->qdt[SEG_KCODE] = SEG(STA_X|STA_R, 0, 0xfffffffff, 0);
                                                                                 1774
                                                                                            break:
1725
      c->qdt[SEG_KDATA] = SEG(STA_W, 0, 0xffffffff, 0);
                                                                                 1775
                                                                                          a += PGSIZE;
1726 c->qdt[SEG_UCODE] = SEG(STA_X|STA_R, 0, 0xffffffff, DPL_USER);
                                                                                 1776
                                                                                          pa += PGSIZE;
1727 c->gdt[SEG_UDATA] = SEG(STA_W, 0, 0xffffffff, DPL_USER);
                                                                                 1777 }
                                                                                 1778 return 0;
1728 lqdt(c->gdt, sizeof(c->gdt));
1729 }
                                                                                 1779 }
1730
                                                                                 1780
1731 // Return the address of the PTE in page table pgdir
                                                                                 1781 // There is one page table per process, plus one that's used when
1732 // that corresponds to virtual address va. If alloc!=0,
                                                                                 1782 // a CPU is not running any process (kpgdir). The kernel uses the
1733 // create any required page table pages.
                                                                                 1783 // current process's page table during system calls and interrupts;
1734 static pte_t *
                                                                                 1784 // page protection bits prevent user code from using the kernel's
1735 walkpgdir(pde_t *pgdir, const void *va, int alloc)
                                                                                 1785 // mappings.
1736 {
                                                                                 1786 //
1737 pde_t *pde;
                                                                                 1787 // setupkvm() and exec() set up every page table like this:
1738
      pte_t *pgtab;
                                                                                 1788 //
1739
                                                                                 1789 // 0..KERNBASE: user memory (text+data+stack+heap), mapped to
1740
      pde = &pgdir[PDX(va)];
                                                                                 1790 //
                                                                                                        phys memory allocated by the kernel
1741 if(*pde & PTE_P){
                                                                                 1791 //
                                                                                           KERNBASE..KERNBASE+EXTMEM: mapped to 0..EXTMEM (for I/O space)
1742
        pgtab = (pte_t*)P2V(PTE_ADDR(*pde));
                                                                                 1792 //
                                                                                           KERNBASE+EXTMEM..data: mapped to EXTMEM..V2P(data)
1743
      } else {
                                                                                 1793 //
                                                                                                        for the kernel's instructions and r/o data
1744
        if(!alloc || (pgtab = (pte_t*)kalloc()) == 0)
                                                                                 1794 //
                                                                                           data..KERNBASE+PHYSTOP: mapped to V2P(data)..PHYSTOP,
1745
          return 0;
                                                                                 1795 //
                                                                                                                          rw data + free physical memory
        // Make sure all those PTE_P bits are zero.
1746
                                                                                 1796 // Oxfe000000..0: mapped direct (devices such as ioapic)
1747
        memset(pgtab, 0, PGSIZE);
                                                                                 1797 //
1748
        // The permissions here are overly generous, but they can
                                                                                 1798 // The kernel allocates physical memory for its heap and for user memory
1749
        // be further restricted by the permissions in the page table
                                                                                 1799 // between V2P(end) and the end of physical memory (PHYSTOP)
```

Sheet 17 Sheet 17

```
1800 // (directly addressable from end..P2V(PHYSTOP)).
                                                                                  1850 // Switch h/w page table register to the kernel-only page table,
                                                                                  1851 // for when no process is running.
1801
1802 // This table defines the kernel's mappings, which are present in
                                                                                  1852 void
1803 // every process's page table.
                                                                                  1853 switchkvm(void)
1804 static struct kmap {
                                                                                  1854 {
                                                                                  1855 lcr3(V2P(kpgdir)); // switch to the kernel page table
1805 void *virt;
1806 uint phys_start;
                                                                                  1856 }
1807 uint phys_end;
                                                                                  1857
1808 int perm;
                                                                                  1858 // Switch TSS and h/w page table to correspond to process p.
1809 \} kmap[] = {
                                                                                  1859 void
1810 { (void*)KERNBASE, 0,
                                        EXTMEM.
                                                   PTE_W}, // I/O space
                                                                                  1860 switchuvm(struct proc *p)
1811 { (void*)KERNLINK, V2P(KERNLINK), V2P(data), 0},
                                                         // kern text+rodata
                                                                                  1861 {
                                        PHYSTOP,
                                                                                  1862 if(p == 0)
1812 { (void*)data.
                         V2P(data),
                                                   PTE_W}, // kern data+memory
1813 { (void*)DEVSPACE, DEVSPACE,
                                        0,
                                                   PTE_W}, // more devices
                                                                                  1863
                                                                                           panic("switchuvm: no process");
1814 }:
                                                                                  1864
                                                                                         if(p\rightarrow kstack == 0)
1815
                                                                                  1865
                                                                                           panic("switchuvm: no kstack");
1816 // Set up kernel part of a page table.
                                                                                  1866
                                                                                         if(p->pgdir == 0)
1817 pde t*
                                                                                  1867
                                                                                           panic("switchuvm: no pgdir");
1818 setupkvm(void)
                                                                                  1868
1819 {
                                                                                  1869
                                                                                         pushcli();
                                                                                         mycpu()->gdt[SEG_TSS] = SEG16(STS_T32A, &mycpu()->ts,
1820 pde t *padir:
                                                                                  1870
1821 struct kmap *k;
                                                                                  1871
                                                                                                                        sizeof(mycpu()->ts)-1, 0);
1822
                                                                                  1872
                                                                                         mycpu()->gdt[SEG_TSS].s = 0;
1823 if((pgdir = (pde_t*)kalloc()) == 0)
                                                                                  1873
                                                                                         mycpu()->ts.ss0 = SEG_KDATA << 3;</pre>
1824
        return 0:
                                                                                  1874
                                                                                         mvcpu()->ts.esp0 = (uint)p->kstack + KSTACKSIZE:
1825
      memset(pgdir, 0, PGSIZE);
                                                                                         // setting IOPL=0 in eflags *and* iomb beyond the tss segment limit
1826 if (P2V(PHYSTOP) > (void*)DEVSPACE)
                                                                                  1876
                                                                                        // forbids I/O instructions (e.g., inb and outb) from user space
1827
        panic("PHYSTOP too high");
                                                                                  1877
                                                                                         mycpu()->ts.iomb = (ushort) 0xFFFF;
1828
       for(k = kmap; k < kmap[NELEM(kmap)]; k++)
                                                                                  1878
                                                                                        ltr(SEG_TSS << 3);</pre>
1829
        if(mappages(pgdir, k->virt, k->phys_end - k->phys_start,
                                                                                  1879 lcr3(V2P(p->pqdir)); // switch to process's address space
1830
                     (uint)k \rightarrow phys_start, k \rightarrow perm) < 0) {
                                                                                  1880
                                                                                        popcli();
1831
                                                                                  1881 }
           freevm(pgdir);
1832
           return 0;
                                                                                  1882
1833
        }
                                                                                  1883 // Load the initcode into address 0 of pgdir.
1834 return pgdir;
                                                                                  1884 // sz must be less than a page.
1835 }
                                                                                  1885 void
1836
                                                                                  1886 inituvm(pde_t *pgdir, char *init, uint sz)
1837 // Allocate one page table for the machine for the kernel address
                                                                                  1887 {
1838 // space for scheduler processes.
                                                                                  1888 char *mem:
1839 void
                                                                                  1889
1840 kvmalloc(void)
                                                                                  1890
                                                                                       if(sz >= PGSIZE)
1841 {
                                                                                  1891
                                                                                           panic("inituvm: more than a page");
1842
      kpgdir = setupkvm();
                                                                                  1892 mem = kalloc();
1843
      switchkvm();
                                                                                  1893
                                                                                         memset(mem, 0, PGSIZE);
1844 }
                                                                                         mappages(pgdir, 0, PGSIZE, V2P(mem), PTE_W|PTE_U);
1845
                                                                                  1895
                                                                                         memmove(mem, init, sz);
1846
                                                                                  1896 }
1847
                                                                                  1897
1848
                                                                                  1898
1849
                                                                                  1899
```

Sheet 18 Sheet 18

```
1900 // Load a program segment into pgdir. addr must be page-aligned
                                                                                  1950
                                                                                             return 0;
1901 // and the pages from addr to addr+sz must already be mapped.
                                                                                  1951
1902 int
                                                                                  1952 }
1903 loaduvm(pde_t *pgdir, char *addr, struct inode *ip, uint offset, uint sz)
                                                                                  1953
                                                                                         return newsz;
1904 {
                                                                                  1954 }
1905
                                                                                  1955
      uint i, pa, n;
1906
                                                                                  1956 // Deallocate user pages to bring the process size from oldsz to
      pte_t *pte;
1907
                                                                                  1957 // newsz. oldsz and newsz need not be page-aligned, nor does newsz
1908
      if((uint) addr % PGSIZE != 0)
                                                                                  1958 // need to be less than oldsz. oldsz can be larger than the actual
                                                                                  1959 // process size. Returns the new process size.
1909
        panic("loaduvm: addr must be page aligned");
1910
      for(i = 0; i < sz; i += PGSIZE){
                                                                                  1960 int
1911
        if((pte = walkpgdir(pgdir, addr+i, 0)) == 0)
                                                                                  1961 deallocuvm(pde_t *pgdir, uint oldsz, uint newsz)
1912
          panic("loaduvm: address should exist");
                                                                                  1962 {
1913
        pa = PTE_ADDR(*pte);
                                                                                  1963
                                                                                        pte_t *pte;
1914
        if(sz - i < PGSIZE)
                                                                                  1964
                                                                                         uint a, pa;
1915
          n = sz - i;
                                                                                  1965
1916
        else
                                                                                  1966
                                                                                         if(newsz >= oldsz)
1917
          n = PGSIZE:
                                                                                  1967
                                                                                           return oldsz:
1918
                                                                                  1968
        if(readi(ip, P2V(pa), offset+i, n) != n)
1919
          return -1;
                                                                                  1969
                                                                                         a = PGROUNDUP(newsz);
1920 }
                                                                                  1970
                                                                                         for(: a < oldsz: a += PGSIZE){
1921 return 0;
                                                                                  1971
                                                                                           pte = walkpgdir(pgdir, (char*)a, 0);
1922 }
                                                                                  1972
                                                                                           if(!pte)
1923
                                                                                  1973
                                                                                             a = PGADDR(PDX(a) + 1, 0, 0) - PGSIZE;
1924 // Allocate page tables and physical memory to grow process from oldsz to
                                                                                  1974
                                                                                           else if((*pte & PTE_P) != 0){
1925 // newsz, which need not be page aligned. Returns new size or 0 on error.
                                                                                  1975
                                                                                             pa = PTE_ADDR(*pte);
1926 int
                                                                                  1976
                                                                                             if(pa == 0)
1927 allocuvm(pde_t *pgdir, uint oldsz, uint newsz)
                                                                                  1977
                                                                                               panic("kfree");
1928 {
                                                                                  1978
                                                                                             char *v = P2V(pa);
1929
      char *mem;
                                                                                  1979
                                                                                             kfree(v);
1930
                                                                                  1980
      uint a;
                                                                                              *pte = 0;
1931
                                                                                  1981
1932
      if(newsz >= KERNBASE)
                                                                                  1982 }
1933
        return 0;
                                                                                  1983
                                                                                         return newsz;
1934
      if(newsz < oldsz)</pre>
                                                                                  1984 }
1935
        return oldsz;
                                                                                  1985
1936
                                                                                  1986
1937
      a = PGROUNDUP(oldsz);
                                                                                  1987
1938
      for(; a < newsz; a += PGSIZE){
                                                                                  1988
1939
        mem = kalloc();
                                                                                  1989
1940
        if(mem == 0){
                                                                                  1990
1941
          cprintf("allocuvm out of memory\n");
                                                                                  1991
1942
          deallocuvm(pgdir, newsz, oldsz);
                                                                                  1992
1943
          return 0;
                                                                                  1993
1944
                                                                                  1994
        }
1945
        memset(mem, 0, PGSIZE);
                                                                                  1995
1946
        if(mappages(pgdir, (char*)a, PGSIZE, V2P(mem), PTE_W|PTE_U) < 0){
                                                                                  1996
1947
          cprintf("allocuvm out of memory (2)\n");
                                                                                  1997
1948
                                                                                  1998
          deallocuvm(pgdir, newsz, oldsz);
1949
           kfree(mem);
                                                                                  1999
```

Sheet 19 Sheet 19

```
2000 // Free a page table and all the physical memory pages
                                                                                 2050
                                                                                         flags = PTE FLAGS(*pte):
2001 // in the user part.
                                                                                 2051
                                                                                         if((mem = kalloc()) == 0)
2002 void
                                                                                 2052
                                                                                           goto bad;
2003 freevm(pde_t *pgdir)
                                                                                         memmove(mem, (char*)P2V(pa), PGSIZE);
                                                                                 2053
2004 {
                                                                                 2054
                                                                                         if(mappages(d, (void*)i, PGSIZE, V2P(mem), flags) < 0)</pre>
2005 uint i;
                                                                                 2055
                                                                                           goto bad;
                                                                                 2056 }
2006
                                                                                 2057 return d;
2007 if(pgdir == 0)
2008
        panic("freevm: no pgdir");
                                                                                 2058
      deallocuvm(pgdir, KERNBASE, 0);
                                                                                 2059 bad:
2009
2010
      for(i = 0; i < NPDENTRIES; i++){</pre>
                                                                                 2060 freevm(d);
                                                                                 2061 return 0;
2011
       if(pgdir[i] & PTE_P){
2012
          char * v = P2V(PTE_ADDR(pgdir[i]));
                                                                                 2062 }
2013
          kfree(v);
                                                                                 2063
2014
        }
                                                                                 2064
2015 }
                                                                                 2065
2016 kfree((char*)pgdir);
                                                                                 2066
2017 }
                                                                                 2067
2018
                                                                                 2068
2019 // Clear PTE_U on a page. Used to create an inaccessible
                                                                                 2069
2020 // page beneath the user stack.
                                                                                 2070
2021 void
                                                                                 2071
2022 clearpteu(pde_t *pgdir, char *uva)
                                                                                 2072
2023 {
                                                                                 2073
2024 pte_t *pte;
                                                                                 2074
2025
                                                                                 2075
2026 pte = walkpgdir(pgdir, uva, 0);
                                                                                 2076
2027 if(pte == 0)
                                                                                 2077
2028
      panic("clearpteu");
                                                                                 2078
                                                                                 2079
2029 *pte &= ~PTE_U;
2030 }
                                                                                 2080
2031
                                                                                 2081
                                                                                 2082
2032 // Given a parent process's page table, create a copy
2033 // of it for a child.
                                                                                 2083
2034 pde_t*
                                                                                 2084
2035 copyuvm(pde_t *pqdir, uint sz)
                                                                                 2085
2036 {
                                                                                 2086
2037 pde_t *d;
                                                                                 2087
2038 pte_t *pte;
                                                                                 2088
2039 uint pa, i, flags;
                                                                                 2089
2040
      char *mem;
                                                                                 2090
2041
                                                                                 2091
2042 if((d = setupkvm()) == 0)
                                                                                 2092
2043
        return 0;
                                                                                 2093
2044
      for(i = 0; i < sz; i += PGSIZE){
                                                                                 2094
2045
        if((pte = walkpgdir(pgdir, (void *) i, 0)) == 0)
                                                                                 2095
2046
          panic("copyuvm: pte should exist");
                                                                                 2096
2047
                                                                                 2097
        if(!(*pte & PTE_P))
2048
          panic("copyuvm: page not present");
                                                                                 2098
2049
        pa = PTE_ADDR(*pte);
                                                                                 2099
```

```
2100 // Map user virtual address to kernel address.
                                                                                2150 // Blank page.
2101 char*
                                                                                2151
2102 uva2ka(pde_t *pgdir, char *uva)
                                                                                2152
2103 {
                                                                                2153
2104 pte_t *pte;
                                                                                2154
2105
                                                                                2155
2106 pte = walkpgdir(pgdir, uva, 0);
                                                                                2156
2107 if((*pte & PTE_P) == 0)
                                                                                2157
2108
        return 0;
                                                                                2158
2109 if((*pte & PTE_U) == 0)
                                                                                2159
2110
        return 0;
                                                                                2160
2111 return (char*)P2V(PTE_ADDR(*pte));
                                                                                2161
2112 }
                                                                                2162
2113
                                                                                2163
2114 // Copy len bytes from p to user address va in page table pgdir.
                                                                                2164
2115 // Most useful when pgdir is not the current page table.
                                                                                2165
2116 // uva2ka ensures this only works for PTE_U pages.
                                                                                2166
2117 int
                                                                                2167
2118 copyout(pde_t *pgdir, uint va, void *p, uint len)
                                                                                2168
2119 {
                                                                                2169
2120 char *buf, *pa0;
                                                                                2170
2121 uint n, va0;
                                                                                2171
2122
                                                                                2172
2123
      buf = (char*)p;
                                                                                2173
2124
      while(len > 0){
                                                                                2174
2125
        va0 = (uint)PGROUNDDOWN(va);
                                                                                2175
2126
                                                                                2176
        pa0 = uva2ka(pgdir, (char*)va0);
2127
        if(pa0 == 0)
                                                                                2177
2128
          return -1;
                                                                                2178
2129
        n = PGSIZE - (va - va0);
                                                                                2179
2130
                                                                                2180
        if(n > len)
2131
          n = len;
                                                                                2181
2132
        memmove(pa0 + (va - va0), buf, n);
                                                                                2182
2133
        len -= n;
                                                                                2183
2134
        buf += n;
                                                                                2184
2135
        va = va0 + PGSIZE;
                                                                                2185
2136 }
                                                                                2186
2137 return 0;
                                                                                2187
2138 }
                                                                                2188
2139
                                                                                2189
2140
                                                                                2190
2141
                                                                                2191
2142
                                                                                2192
2143
                                                                                2193
2144
                                                                                2194
2145
                                                                                2195
2146
                                                                                2196
2147
                                                                                2197
2148
                                                                                2198
2149
                                                                                2199
```

2200 // Blank page.	2250 // Blank page.
2201 // Brank page.	2250 // Brank page. 2251
2202	2252
2203	2253
2204	2254
2205	2255
2206	2256
2207	2257
2208	2258
2209	2259
2210	2260
2211	2261
2212	2262
2213	2263
2214	2264
2215	2265
2216	2266
2217	2267
2218	2268
2219	2269
2220	2270
2221	2271
2222	2272
2223	2273
2224	2274
2225	2275
2226	2276
2227	2277
2228	2278
2229	2279
2230	2280
2231	2281
2232	2282
2233	2283
2234	2284
2235	2285
2236	2286
2237	2287
2238	2288
2239	2289
2240	2290
2241	2291
2242	2292
2243	2293
2244	2294
2245	2295
2246	2296
2247	2297
2248	2298
2249	2299

```
2300 // Per-CPU state
                                                                                 2350
                                                                                       char name[16];
                                                                                                                     // Process name (debugging)
2301 struct cpu {
                                                                                 2351 };
2302
      uchar apicid;
                                   // Local APIC ID
                                                                                 2352
2303
      struct context *scheduler;
                                   // swtch() here to enter scheduler
                                                                                 2353 // Process memory is laid out contiguously, low addresses first:
2304
      struct taskstate ts;
                                   // Used by x86 to find stack for interrupt
                                                                                 2354 //
                                                                                           text
                                  // x86 global descriptor table
      struct segdesc gdt[NSEGS];
                                                                                           original data and bss
2305
                                                                                 2355 //
2306
      volatile uint started;
                                   // Has the CPU started?
                                                                                 2356 //
                                                                                           fixed-size stack
2307
      int ncli;
                                   // Depth of pushcli nesting.
                                                                                 2357 //
                                                                                           expandable heap
2308 int intena;
                                   // Were interrupts enabled before pushcli?
                                                                                 2358
                                                                                 2359
2309 struct proc *proc;
                                   // The process running on this cpu or null
2310 };
                                                                                 2360
2311
                                                                                 2361
                                                                                 2362
2312 extern struct cpu cpus[NCPU];
2313 extern int ncpu;
                                                                                 2363
2314
                                                                                 2364
2315
                                                                                 2365
2316 // Saved registers for kernel context switches.
                                                                                 2366
2317 // Don't need to save all the segment registers (%cs. etc).
                                                                                 2367
2318 // because they are constant across kernel contexts.
                                                                                 2368
2319 // Don't need to save %eax, %ecx, %edx, because the
                                                                                 2369
2320 // x86 convention is that the caller has saved them.
                                                                                 2370
2321 // Contexts are stored at the bottom of the stack they
                                                                                 2371
2322 // describe; the stack pointer is the address of the context.
                                                                                 2372
2323 // The layout of the context matches the layout of the stack in swtch.S
                                                                                 2373
2324 // at the "Switch stacks" comment. Switch doesn't save eip explicitly.
                                                                                 2374
2325 // but it is on the stack and allocproc() manipulates it.
                                                                                 2375
2326 struct context {
                                                                                 2376
2327
                                                                                 2377
      uint edi;
2328 uint esi;
                                                                                 2378
2329 uint ebx;
                                                                                 2379
2330 uint ebp;
                                                                                 2380
2331 uint eip;
                                                                                 2381
2332 };
                                                                                 2382
2333
                                                                                 2383
2334 enum procstate { UNUSED, EMBRYO, SLEEPING, RUNNABLE, RUNNING, ZOMBIE };
                                                                                 2384
                                                                                 2385
2335
2336 // Per-process state
                                                                                 2386
2337 struct proc {
                                                                                 2387
2338 uint sz;
                                   // Size of process memory (bytes)
                                                                                 2388
2339
      pde_t* pgdir;
                                   // Page table
                                                                                 2389
2340
      char *kstack;
                                   // Bottom of kernel stack for this process
                                                                                 2390
2341
      enum procstate state;
                                   // Process state
                                                                                 2391
2342
      int pid;
                                   // Process ID
                                                                                 2392
2343
      struct proc *parent;
                                   // Parent process
                                                                                 2393
2344
      struct trapframe *tf;
                                   // Trap frame for current syscall
                                                                                 2394
2345
      struct context *context;
                                   // swtch() here to run process
                                                                                 2395
2346
      void *chan;
                                   // If non-zero, sleeping on chan
                                                                                 2396
2347
      int killed;
                                   // If non-zero, have been killed
                                                                                 2397
      struct file *ofile[NOFILE]; // Open files
2348
                                                                                 2398
2349
      struct inode *cwd;
                                   // Current directory
                                                                                 2399
```

Sheet 23 Sheet 23

```
2400 #include "types.h"
                                                                                  2450 }
2401 #include "defs.h"
                                                                                  2451 panic("unknown apicid\n");
2402 #include "param.h"
                                                                                  2452 }
2403 #include "memlayout.h"
                                                                                  2453
2404 #include "mmu.h"
                                                                                  2454 // Disable interrupts so that we are not rescheduled
2405 #include "x86.h"
                                                                                  2455 // while reading proc from the cpu structure
2406 #include "proc.h"
                                                                                  2456 struct proc*
2407 #include "spinlock.h"
                                                                                  2457 myproc(void) {
2408
                                                                                  2458 struct cpu *c;
2409 struct {
                                                                                  2459 struct proc *p;
2410 struct spinlock lock;
                                                                                  2460 pushcli();
2411 struct proc proc[NPROC];
                                                                                  2461 c = mycpu();
2412 } ptable;
                                                                                  2462 p = c \rightarrow proc;
2413
                                                                                  2463 popcli();
2414 static struct proc *initproc;
                                                                                  2464 return p;
2415
                                                                                  2465 }
2416 int nextpid = 1;
                                                                                  2466
2417 extern void forkret(void):
                                                                                  2467
2418 extern void trapret(void);
                                                                                  2468 // Look in the process table for an UNUSED proc.
2419
                                                                                  2469 // If found, change state to EMBRYO and initialize
2420 static void wakeup1(void *chan):
                                                                                  2470 // state required to run in the kernel.
2421
                                                                                  2471 // Otherwise return 0.
2422 void
                                                                                  2472 static struct proc*
2423 pinit(void)
                                                                                  2473 allocproc(void)
2424 {
                                                                                  2474 {
2425 initlock(&ptable.lock, "ptable");
                                                                                  2475 struct proc *p;
2426 }
                                                                                  2476
                                                                                        char *sp;
2427
                                                                                  2477
2428 // Must be called with interrupts disabled
                                                                                  2478
                                                                                        acquire(&ptable.lock);
2429 int
                                                                                  2479
2430 cpuid() {
                                                                                  2480
                                                                                         for(p = ptable.proc; p < &ptable.proc[NPROC]; p++)</pre>
                                                                                          if(p->state == UNUSED)
2431 return mycpu()-cpus;
                                                                                  2481
                                                                                  2482
2432 }
                                                                                             goto found;
2433
                                                                                  2483
2434 // Must be called with interrupts disabled to avoid the caller being
                                                                                  2484
                                                                                         release(&ptable.lock);
                                                                                  2485
                                                                                         return 0:
2435 // rescheduled between reading lapicid and running through the loop.
2436 struct cpu*
                                                                                  2486
2437 mycpu(void)
                                                                                  2487 found:
2438 {
                                                                                  2488
                                                                                         p->state = EMBRYO;
2439 int apicid, i;
                                                                                  2489
                                                                                        p->pid = nextpid++;
2440
                                                                                  2490
2441
      if(readeflags()&FL_IF)
                                                                                  2491
                                                                                        release(&ptable.lock);
2442
        panic("mycpu called with interrupts enabled\n");
                                                                                  2492
2443
                                                                                  2493
                                                                                        // Allocate kernel stack.
                                                                                        if((p->kstack = kalloc()) == 0){
2444
      apicid = lapicid();
                                                                                  2494
2445 // APIC IDs are not guaranteed to be contiguous. Maybe we should have
                                                                                  2495
                                                                                           p->state = UNUSED;
2446 // a reverse map, or reserve a register to store &cpus[i].
                                                                                  2496
                                                                                           return 0;
2447
      for (i = 0; i < ncpu; ++i) {
                                                                                  2497 }
2448
                                                                                  2498
                                                                                        sp = p->kstack + KSTACKSIZE;
        if (cpus[i].apicid == apicid)
                                                                                  2499
2449
           return &cpus[i];
```

```
2500 // Leave room for trap frame.
                                                                                  2550
                                                                                         p->state = RUNNABLE:
2501 sp -= sizeof *p->tf;
                                                                                  2551
2502 p->tf = (struct trapframe*)sp;
                                                                                  2552 release(&ptable.lock);
2503
                                                                                  2553 }
2504 // Set up new context to start executing at forkret,
                                                                                  2554
2505 // which returns to trapret.
                                                                                  2555 // Grow current process's memory by n bytes.
2506 \text{ sp } -= 4;
                                                                                  2556 // Return 0 on success, -1 on failure.
2507 *(uint*)sp = (uint)trapret;
                                                                                  2557 int
2508
                                                                                  2558 growproc(int n)
2509 sp -= sizeof *p->context;
                                                                                  2559 {
2510 p->context = (struct context*)sp;
                                                                                  2560 uint sz;
2511 memset(p->context, 0, sizeof *p->context);
                                                                                  2561 struct proc *curproc = myproc();
2512 p->context->eip = (uint)forkret;
                                                                                  2562
2513
                                                                                  2563 sz = curproc \rightarrow sz;
2514 return p;
                                                                                  2564 if (n > 0)
2515 }
                                                                                  2565
                                                                                           if((sz = allocuvm(curproc->pgdir, sz, sz + n)) == 0)
2516
                                                                                  2566
                                                                                             return -1;
2517
                                                                                  2567 } else if(n < 0){
2518 // Set up first user process.
                                                                                  2568
                                                                                           if((sz = deallocuvm(curproc->pqdir, sz, sz + n)) == 0)
2519 void
                                                                                  2569
                                                                                             return -1;
2520 userinit(void)
                                                                                  2570 }
2521 {
                                                                                  2571 curproc->sz = sz;
2522 struct proc *p;
                                                                                  2572 switchuvm(curproc);
2523
      extern char _binary_initcode_start[], _binary_initcode_size[];
                                                                                  2573 return 0;
2524
                                                                                  2574 }
2525 p = allocproc();
                                                                                  2575
2526
                                                                                  2576 // Create a new process copying p as the parent.
2527 initproc = p;
                                                                                  2577 // Sets up stack to return as if from system call.
                                                                                  2578 // Caller must set state of returned proc to RUNNABLE.
2528 if((p \rightarrow pqdir = setupkvm()) == 0)
2529
                                                                                  2579 int
      panic("userinit: out of memory?");
2530 inituvm(p->pgdir, _binary_initcode_start, (int)_binary_initcode_size);
                                                                                  2580 fork(void)
                                                                                  2581 {
2531 p\rightarrow sz = PGSIZE;
2532 memset(p\rightarrow tf, 0, sizeof(*p\rightarrow tf));
                                                                                  2582 int i, pid;
2533 p->tf->cs = (SEG_UCODE << 3) | DPL_USER;</pre>
                                                                                  2583 struct proc *np;
2534 p\rightarrow tf\rightarrow ds = (SEG\_UDATA << 3) \mid DPL\_USER;
                                                                                  2584 struct proc *curproc = myproc();
2535 p->tf->es = p->tf->ds;
                                                                                  2585
2536 p->tf->ss = p->tf->ds;
                                                                                  2586 // Allocate process.
2537
      p->tf->eflags = FL_IF;
                                                                                  2587 if((np = allocproc()) == 0){
2538 p->tf->esp = PGSIZE;
                                                                                  2588
                                                                                           return -1;
2539 p->tf->eip = 0; // beginning of initcode.S
                                                                                  2589 }
2540
                                                                                  2590
                                                                                  2591 // Copy process state from proc.
2541 safestrcpy(p->name, "initcode", sizeof(p->name));
2542
      p->cwd = namei("/");
                                                                                  2592 if((np->pgdir = copyuvm(curproc->pgdir, curproc->sz)) == 0){
2543
                                                                                  2593
                                                                                           kfree(np->kstack);
2544 // this assignment to p->state lets other cores
                                                                                  2594
                                                                                           np->kstack = 0;
                                                                                  2595
2545 // run this process. the acquire forces the above
                                                                                           np->state = UNUSED;
2546 // writes to be visible, and the lock is also needed
                                                                                  2596
                                                                                           return -1;
2547 // because the assignment might not be atomic.
                                                                                  2597 }
2548
      acquire(&ptable.lock);
                                                                                  2598 np->sz = curproc->sz;
2549
                                                                                  2599
                                                                                         np->parent = curproc;
```

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```
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                                                                                  Aug 29 15:52 2017 xv6/proc.c Page 6
2600
       *np->tf = *curproc->tf;
                                                                                   2650
                                                                                         // Parent might be sleeping in wait().
2601
                                                                                   2651
                                                                                         wakeup1(curproc->parent);
2602
      // Clear %eax so that fork returns 0 in the child.
                                                                                   2652
                                                                                         // Pass abandoned children to init.
2603
       np->tf->eax = 0;
                                                                                   2653
2604
                                                                                   2654
                                                                                         for(p = ptable.proc; p < &ptable.proc[NPROC]; p++){</pre>
2605
       for(i = 0; i < NOFILE; i++)
                                                                                   2655
                                                                                           if(p->parent == curproc){
2606
        if(curproc->ofile[i])
                                                                                   2656
                                                                                              p->parent = initproc;
2607
          np->ofile[i] = filedup(curproc->ofile[i]);
                                                                                   2657
                                                                                              if(p->state == ZOMBIE)
2608
       np->cwd = idup(curproc->cwd);
                                                                                   2658
                                                                                                wakeup1(initproc);
2609
                                                                                   2659
2610
       safestrcpy(np->name, curproc->name, sizeof(curproc->name));
                                                                                   2660
                                                                                        }
2611
                                                                                   2661
2612
                                                                                   2662
       pid = np->pid;
                                                                                         // Jump into the scheduler, never to return.
2613
                                                                                   2663
                                                                                         curproc->state = ZOMBIE;
2614
       acquire(&ptable.lock);
                                                                                   2664
                                                                                         sched():
2615
                                                                                   2665
                                                                                         panic("zombie exit");
2616
       np->state = RUNNABLE;
                                                                                   2666 }
2617
                                                                                   2667
2618
       release(&ptable.lock);
                                                                                   2668 // Wait for a child process to exit and return its pid.
2619
                                                                                   2669 // Return -1 if this process has no children.
2620 return pid:
                                                                                   2670 int
2621 }
                                                                                   2671 wait(void)
2622
                                                                                   2672 {
2623 // Exit the current process. Does not return.
                                                                                   2673 struct proc *p;
2624 // An exited process remains in the zombie state
                                                                                   2674
                                                                                         int havekids. pid:
2625 // until its parent calls wait() to find out it exited.
                                                                                   2675
                                                                                         struct proc *curproc = myproc();
2626 void
                                                                                   2676
2627 exit(void)
                                                                                   2677
                                                                                         acquire(&ptable.lock);
2628 {
                                                                                   2678
                                                                                          for(;;){
2629 struct proc *curproc = myproc();
                                                                                   2679
                                                                                           // Scan through table looking for exited children.
2630 struct proc *p;
                                                                                   2680
                                                                                           havekids = 0;
2631
      int fd;
                                                                                   2681
                                                                                           for(p = ptable.proc; p < &ptable.proc[NPROC]; p++){</pre>
2632
                                                                                   2682
                                                                                             if(p->parent != curproc)
2633
      if(curproc == initproc)
                                                                                   2683
                                                                                               continue;
2634
        panic("init exiting");
                                                                                   2684
                                                                                              havekids = 1;
2635
                                                                                   2685
                                                                                              if(p->state == ZOMBIE){
2636 // Close all open files.
                                                                                   2686
                                                                                               // Found one.
2637
       for(fd = 0; fd < NOFILE; fd++){</pre>
                                                                                   2687
                                                                                                pid = p->pid;
2638
        if(curproc->ofile[fd]){
                                                                                   2688
                                                                                               kfree(p->kstack);
2639
           fileclose(curproc->ofile[fd]);
                                                                                   2689
                                                                                               p->kstack = 0;
2640
           curproc->ofile[fd] = 0;
                                                                                   2690
                                                                                                freevm(p->pqdir);
2641
        }
                                                                                   2691
                                                                                               p->pid = 0;
2642
      }
                                                                                   2692
                                                                                               p->parent = 0;
2643
                                                                                   2693
                                                                                                p->name[0] = 0;
2644
       begin_op();
                                                                                   2694
                                                                                               p->killed = 0;
2645
       iput(curproc->cwd);
                                                                                   2695
                                                                                               p->state = UNUSED;
2646
       end_op();
                                                                                   2696
                                                                                                release(&ptable.lock);
2647
       curproc -> cwd = 0;
                                                                                   2697
                                                                                                return pid;
                                                                                   2698
2648
                                                                                             }
                                                                                           }
2649
       acquire(&ptable.lock);
                                                                                   2699
```

```
2700
         // No point waiting if we don't have any children.
                                                                                   2750 // Per-CPU process scheduler.
2701
         if(!havekids || curproc->killed){
                                                                                   2751 // Each CPU calls scheduler() after setting itself up.
2702
           release(&ptable.lock);
                                                                                   2752 // Scheduler never returns. It loops, doing:
2703
                                                                                   2753 // - choose a process to run
           return -1;
2704
                                                                                   2754 // - swtch to start running that process
2705
                                                                                   2755 // - eventually that process transfers control
2706
         // Wait for children to exit. (See wakeup1 call in proc_exit.)
                                                                                   2756 //
                                                                                                via swtch back to the scheduler.
2707
         sleep(curproc, &ptable.lock);
                                                                                   2757 void
2708 }
                                                                                   2758 scheduler(void)
2709 }
                                                                                   2759 {
2710
                                                                                   2760 struct proc *p;
2711
                                                                                   2761 struct cpu *c = mycpu();
2712
                                                                                   2762 c -> proc = 0;
2713
                                                                                   2763
2714
                                                                                   2764
                                                                                          for(;;){
2715
                                                                                   2765
                                                                                            // Enable interrupts on this processor.
2716
                                                                                   2766
                                                                                            sti();
2717
                                                                                   2767
2718
                                                                                   2768
                                                                                            // Loop over process table looking for process to run.
2719
                                                                                   2769
                                                                                            acquire(&ptable.lock);
2720
                                                                                   2770
                                                                                            for(p = ptable.proc; p < &ptable.proc[NPROC]; p++){</pre>
2721
                                                                                   2771
                                                                                              if(p->state != RUNNABLE)
2722
                                                                                   2772
                                                                                                continue;
2723
                                                                                   2773
2724
                                                                                   2774
                                                                                              // Switch to chosen process. It is the process's job
2725
                                                                                   2775
                                                                                              // to release ptable.lock and then reacquire it
                                                                                   2776
2726
                                                                                              // before jumping back to us.
2727
                                                                                   2777
                                                                                              c \rightarrow proc = p;
2728
                                                                                   2778
                                                                                              switchuvm(p);
2729
                                                                                   2779
                                                                                              p->state = RUNNING;
2730
                                                                                   2780
2731
                                                                                   2781
                                                                                              swtch(&(c->scheduler), p->context);
2732
                                                                                   2782
                                                                                              switchkvm();
2733
                                                                                   2783
2734
                                                                                   2784
                                                                                              // Process is done running for now.
2735
                                                                                   2785
                                                                                              // It should have changed its p->state before coming back.
2736
                                                                                   2786
                                                                                              c \rightarrow proc = 0;
2737
                                                                                   2787
2738
                                                                                   2788
                                                                                            release(&ptable.lock);
2739
                                                                                   2789
2740
                                                                                   2790 }
                                                                                   2791 }
2741
2742
                                                                                   2792
2743
                                                                                   2793
                                                                                   2794
2744
2745
                                                                                   2795
2746
                                                                                   2796
2747
                                                                                   2797
2748
                                                                                   2798
                                                                                   2799
2749
```

```
2800 // Enter scheduler. Must hold only ptable.lock
2801 // and have changed proc->state. Saves and restores
2802 // intena because intena is a property of this
2803 // kernel thread, not this CPU. It should
2804 // be proc->intena and proc->ncli, but that would
2805 // break in the few places where a lock is held but
2806 // there's no process.
2807 void
2808 sched(void)
2809 {
2810 int intena;
2811
      struct proc *p = myproc();
2812
2813
      if(!holding(&ptable.lock))
2814
        panic("sched ptable.lock");
2815
      if(mycpu()->ncli != 1)
2816
        panic("sched locks");
2817 if(p->state == RUNNING)
2818
        panic("sched running");
2819 if(readeflags()&FL_IF)
2820
        panic("sched interruptible"):
2821 intena = mycpu()->intena;
2822
      swtch(&p->context, mycpu()->scheduler);
2823 mycpu()->intena = intena;
2824 }
2825
2826 // Give up the CPU for one scheduling round.
2827 void
2828 yield(void)
2829 {
2830 acquire(&ptable.lock);
2831 myproc()->state = RUNNABLE;
2832 sched();
2833
      release(&ptable.lock);
2834 }
2835
2836
2837
2838
2839
2840
2841
2842
2843
2844
2845
2846
2847
2848
2849
```

```
2850 // A fork child's very first scheduling by scheduler()
2851 // will swtch here. "Return" to user space.
2852 void
2853 forkret(void)
2854 {
2855 static int first = 1;
2856
      // Still holding ptable.lock from scheduler.
2857
      release(&ptable.lock);
2858
2859 if (first) {
2860
        // Some initialization functions must be run in the context
2861
        // of a regular process (e.g., they call sleep), and thus cannot
2862
        // be run from main().
2863
        first = 0;
        iinit(ROOTDEV):
2864
2865
        initlog(ROOTDEV);
2866 }
2867
2868 // Return to "caller", actually trapret (see allocproc).
2869 }
2870
2871 // Atomically release lock and sleep on chan.
2872 // Reacquires lock when awakened.
2873 void
2874 sleep(void *chan, struct spinlock *lk)
2875 {
2876 struct proc *p = myproc();
2877
2878
     if(p == 0)
2879
        panic("sleep");
2880
2881 if(1k == 0)
2882
        panic("sleep without lk");
2883
2884 // Must acquire ptable.lock in order to
2885 // change p->state and then call sched.
2886 // Once we hold ptable.lock, we can be
2887 // quaranteed that we won't miss any wakeup
2888 // (wakeup runs with ptable.lock locked),
2889 // so it's okay to release lk.
2890 if(lk != &ptable.lock){
        acquire(&ptable.lock);
2891
2892
        release(lk);
2893 }
2894 // Go to sleep.
2895
      p->chan = chan;
2896
      p->state = SLEEPING;
2897
2898
     sched();
2899
```

```
2900 // Tidy up.
2901
      p->chan = 0;
2902
2903
      // Reacquire original lock.
2904 if(lk != &ptable.lock){
2905
        release(&ptable.lock);
2906
        acquire(lk);
2907 }
2908 }
2909
2910
2911
2912
2913
2914
2915
2916
2917
2918
2919
2920
2921
2922
2923
2924
2925
2926
2927
2928
2929
2930
2931
2932
2933
2934
2935
2936
2937
2938
2939
2940
2941
2942
2943
2944
2945
2946
2947
2948
2949
```

```
2950 // Wake up all processes sleeping on chan.
2951 // The ptable lock must be held.
2952 static void
2953 wakeup1(void *chan)
2954 {
2955 struct proc *p;
2956
2957
      for(p = ptable.proc; p < &ptable.proc[NPROC]; p++)</pre>
2958
        if(p->state == SLEEPING && p->chan == chan)
2959
           p->state = RUNNABLE;
2960 }
2961
2962 // Wake up all processes sleeping on chan.
2963 void
2964 wakeup(void *chan)
2965 {
2966 acquire(&ptable.lock);
2967 wakeup1(chan):
2968 release(&ptable.lock);
2969 }
2970
2971 // Kill the process with the given pid.
2972 // Process won't exit until it returns
2973 // to user space (see trap in trap.c).
2974 int
2975 kill(int pid)
2976 {
2977 struct proc *p;
2978
2979
      acquire(&ptable.lock);
2980
      for(p = ptable.proc; p < &ptable.proc[NPROC]; p++){</pre>
2981
        if(p->pid == pid){
2982
           p->killed = 1;
2983
          // Wake process from sleep if necessary.
2984
          if(p->state == SLEEPING)
2985
            p->state = RUNNABLE;
2986
           release(&ptable.lock);
2987
           return 0;
2988
2989 }
2990
      release(&ptable.lock);
2991
      return -1;
2992 }
2993
2994
2995
2996
2997
2998
2999
```

```
3000 // Print a process listing to console. For debugging.
                                                                                  3050 # Context switch
3001 // Runs when user types ^P on console.
                                                                                  3051 #
3002 // No lock to avoid wedging a stuck machine further.
                                                                                  3052 # void swtch(struct context **old, struct context *new);
3003 void
                                                                                  3053 #
3004 procdump(void)
                                                                                  3054 # Save current register context in old
3005 {
                                                                                  3055 # and then load register context from new.
3006 static char *states[] = {
                                                                                  3056
                                                                                  3057 .globl swtch
3007
       [UNUSED]
                   "unused",
3008
      [EMBRYO]
                   "embryo",
                                                                                  3058 swtch:
      [SLEEPING]
                  "sleep "
                                                                                  3059
                                                                                        movl 4(%esp), %eax
3009
3010
      [RUNNABLE]
                  "runble",
                                                                                  3060
                                                                                        movl 8(%esp), %edx
3011
                   "run ".
       [RUNNING]
                                                                                  3061
3012
      [ZOMBIE]
                   "zombie"
                                                                                  3062 # Save old callee-save registers
3013 };
                                                                                  3063
                                                                                         push1 %ebp
                                                                                         push1 %ebx
3014 int i:
                                                                                  3064
3015
      struct proc *p;
                                                                                  3065
                                                                                        pushl %esi
3016
      char *state;
                                                                                  3066
                                                                                        pushl %edi
3017
      uint pc[10];
                                                                                  3067
3018
                                                                                  3068 # Switch stacks
3019
       for(p = ptable.proc; p < &ptable.proc[NPROC]; p++){</pre>
                                                                                  3069
                                                                                        movl %esp, (%eax)
3020
        if(p->state == UNUSED)
                                                                                  3070
                                                                                        movl %edx. %esp
3021
           continue:
                                                                                  3071
3022
        if(p->state >= 0 && p->state < NELEM(states) && states[p->state])
                                                                                  3072
                                                                                         # Load new callee-save registers
3023
          state = states[p->state];
                                                                                  3073
                                                                                         popl %edi
3024
        else
                                                                                  3074
                                                                                        popl %esi
3025
           state = "???";
                                                                                  3075
                                                                                         popl %ebx
        cprintf("%d %s %s", p->pid, state, p->name);
3026
                                                                                  3076
                                                                                         popl %ebp
3027
        if(p->state == SLEEPING){
                                                                                  3077
                                                                                         ret
3028
          getcallerpcs((uint*)p->context->ebp+2, pc);
                                                                                  3078
3029
                                                                                  3079
          for(i=0; i<10 && pc[i] != 0; i++)
3030
                                                                                  3080
            cprintf(" %p", pc[i]);
        }
3031
                                                                                  3081
3032
        cprintf("\n");
                                                                                  3082
3033 }
                                                                                  3083
3034 }
                                                                                  3084
3035
                                                                                  3085
3036
                                                                                  3086
3037
                                                                                  3087
3038
                                                                                  3088
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                                                                                  3089
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                                                                                  3096
                                                                                  3097
3047
3048
                                                                                  3098
3049
                                                                                  3099
```

Sheet 30 Sheet 30

```
3100 // Physical memory allocator, intended to allocate
                                                                                 3150 void
                                                                                 3151 freerange(void *vstart, void *vend)
3101 // memory for user processes, kernel stacks, page table pages,
3102 // and pipe buffers. Allocates 4096-byte pages.
                                                                                 3152 {
3103
                                                                                 3153 char *p;
3104 #include "types.h"
                                                                                 3154 p = (char*)PGROUNDUP((uint)vstart);
3105 #include "defs.h"
                                                                                 3155 for(; p + PGSIZE <= (char*)vend; p += PGSIZE)
3106 #include "param.h"
                                                                                 3156
                                                                                          kfree(p);
3107 #include "memlayout.h"
                                                                                 3157 }
3108 #include "mmu.h"
                                                                                 3158
3109 #include "spinlock.h"
                                                                                 3159 // Free the page of physical memory pointed at by v,
3110
                                                                                 3160 // which normally should have been returned by a
3111 void freerange(void *vstart, void *vend);
                                                                                 3161 // call to kalloc(). (The exception is when
3112 extern char end[]; // first address after kernel loaded from ELF file
                                                                                 3162 // initializing the allocator; see kinit above.)
3113
                       // defined by the kernel linker script in kernel.ld
                                                                                 3163 void
3114
                                                                                 3164 kfree(char *v)
                                                                                 3165 {
3115 struct run {
3116 struct run *next;
                                                                                 3166 struct run *r;
3117 }:
                                                                                 3167
3118
                                                                                 3168
                                                                                       if((uint)v % PGSIZE || v < end || V2P(v) >= PHYSTOP)
3119 struct {
                                                                                 3169
                                                                                          panic("kfree");
3120 struct spinlock lock:
                                                                                 3170
3121 int use lock:
                                                                                 3171 // Fill with junk to catch dangling refs.
3122 struct run *freelist;
                                                                                 3172
                                                                                        memset(v, 1, PGSIZE);
3123 } kmem;
                                                                                 3173
                                                                                 3174 if(kmem.use_lock)
3124
3125 // Initialization happens in two phases.
                                                                                 3175
                                                                                          acquire(&kmem.lock);
3126 // 1. main() calls kinit1() while still using entrypgdir to place just
                                                                                 3176
                                                                                       r = (struct run*)v;
3127 // the pages mapped by entrypgdir on free list.
                                                                                 3177
                                                                                        r->next = kmem.freelist;
3128 // 2. main() calls kinit2() with the rest of the physical pages
                                                                                 3178
                                                                                        kmem.freelist = r;
3129 // after installing a full page table that maps them on all cores.
                                                                                 3179
                                                                                       if(kmem.use_lock)
3130 void
                                                                                 3180
                                                                                          release(&kmem.lock);
3131 kinit1(void *vstart, void *vend)
                                                                                 3181 }
                                                                                 3182
3132 {
3133 initlock(&kmem.lock, "kmem");
                                                                                 3183 // Allocate one 4096-byte page of physical memory.
3134 kmem.use\_lock = 0;
                                                                                 3184 // Returns a pointer that the kernel can use.
3135 freerange(vstart, vend);
                                                                                 3185 // Returns 0 if the memory cannot be allocated.
3136 }
                                                                                 3186 char*
3137
                                                                                 3187 kalloc(void)
3138 void
                                                                                 3188 {
3139 kinit2(void *vstart, void *vend)
                                                                                 3189 struct run *r;
3140 {
                                                                                 3190
3141 freerange(vstart, vend);
                                                                                 3191 if(kmem.use lock)
3142
      kmem.use_lock = 1;
                                                                                 3192
                                                                                          acquire(&kmem.lock);
3143 }
                                                                                 3193
                                                                                        r = kmem.freelist;
3144
                                                                                 3194 if(r)
                                                                                          kmem.freelist = r->next;
3145
                                                                                 3195
3146
                                                                                 3196
                                                                                       if(kmem.use_lock)
3147
                                                                                 3197
                                                                                          release(&kmem.lock);
                                                                                 3198 return (char*)r;
3148
3149
                                                                                 3199 }
```

Sheet 31 Sheet 31

```
3200 // x86 trap and interrupt constants.
                                                                                   3250 #!/usr/bin/perl -w
3201
                                                                                   3251
3202 // Processor-defined:
                                                                                   3252 # Generate vectors.S, the trap/interrupt entry points.
3203 #define T_DIVIDE
                              0
                                     // divide error
                                                                                   3253 # There has to be one entry point per interrupt number
3204 #define T_DEBUG
                              1
                                     // debug exception
                                                                                   3254 # since otherwise there's no way for trap() to discover
3205 #define T_NMI
                              2
                                     // non-maskable interrupt
                                                                                   3255 # the interrupt number.
3206 #define T_BRKPT
                              3
                                     // breakpoint
                                                                                   3256
                              4
3207 #define T_OFLOW
                                     // overflow
                                                                                   3257 print "# generated by vectors.pl - do not edit\n";
3208 #define T_BOUND
                              5
                                     // bounds check
                                                                                   3258 print "# handlers\n";
3209 #define T_ILLOP
                                                                                   3259 print ".globl alltraps\n";
                              6
                                     // illegal opcode
3210 #define T_DEVICE
                              7
                                     // device not available
                                                                                   3260 for(my i = 0; i < 256; i++)
                              8
3211 #define T DBLFLT
                                     // double fault
                                                                                   3261
                                                                                            print ".globl vector$i\n";
3212 // #define T_COPROC
                              9
                                     // reserved (not used since 486)
                                                                                   3262
                                                                                            print "vector$i:\n";
3213 #define T_TSS
                             10
                                     // invalid task switch segment
                                                                                   3263
                                                                                            if(!(\$i == 8 \mid | (\$i >= 10 \&\& \$i <= 14) \mid | \$i == 17)){}
3214 #define T SEGNP
                             11
                                     // segment not present
                                                                                   3264
                                                                                                print " push1 \$0\n";
3215 #define T_STACK
                             12
                                     // stack exception
                                                                                   3265
                                                                                            }
3216 #define T_GPFLT
                             13
                                     // general protection fault
                                                                                   3266
                                                                                            print " push1 \$$i\n";
3217 #define T PGFLT
                             14
                                     // page fault
                                                                                   3267
                                                                                            print " jmp alltraps\n";
3218 // #define T RES
                             15
                                     // reserved
                                                                                   3268 }
3219 #define T_FPERR
                             16
                                     // floating point error
                                                                                   3269
3220 #define T ALIGN
                             17
                                     // aligment check
                                                                                   3270 print "\n# vector table\n":
3221 #define T MCHK
                             18
                                     // machine check
                                                                                   3271 print ".data\n":
                                                                                   3272 print ".globl vectors\n";
3222 #define T_SIMDERR
                             19
                                     // SIMD floating point error
3223
                                                                                   3273 print "vectors:\n";
                                                                                   3274 \text{ for(mv $i = 0: $i < 256: $i++)}
3224 // These are arbitrarily chosen, but with care not to overlap
3225 // processor defined exceptions or interrupt vectors.
                                                                                   3275
                                                                                            print " .long vector$i\n";
3226 #define T_SYSCALL
                             64
                                     // system call
                                                                                   3276 }
3227 #define T_DEFAULT
                                     // catchall
                                                                                   3277
                            500
3228
                                                                                   3278 # sample output:
3229 #define T_IRQ0
                                                                                   3279 # # handlers
                                     // IRQ 0 corresponds to int T_IRQ
                             32
3230
                                                                                   3280 #
                                                                                            .globl alltraps
3231 #define IRQ_TIMER
                              0
                                                                                   3281 #
                                                                                            .globl vector0
                                                                                   3282 #
                                                                                            vector0:
3232 #define IRQ_KBD
                              1
3233 #define IRQ_COM1
                              4
                                                                                   3283 #
                                                                                              push1 $0
3234 #define IRQ_IDE
                             14
                                                                                   3284 #
                                                                                              push1 $0
                             19
3235 #define IRO ERROR
                                                                                   3285 #
                                                                                              jmp alltraps
3236 #define IRQ_SPURIOUS
                             31
                                                                                   3286 #
3237
                                                                                   3287 #
3238
                                                                                   3288 #
                                                                                            # vector table
3239
                                                                                   3289 #
                                                                                            .data
3240
                                                                                   3290 #
                                                                                            .globl vectors
3241
                                                                                   3291 #
                                                                                            vectors:
3242
                                                                                   3292 #
                                                                                               .long vector0
3243
                                                                                   3293 #
                                                                                               .long vector1
3244
                                                                                   3294 #
                                                                                               .long vector2
                                                                                   3295 #
3245
                                                                                            . . .
3246
                                                                                   3296
3247
                                                                                   3297
3248
                                                                                   3298
3249
                                                                                   3299
```

Sheet 32 Sheet 32

```
3300 #include "mmu.h"
3301
3302 # vectors.S sends all traps here.
3303 .globl alltraps
3304 alltraps:
3305 # Build trap frame.
3306 push1 %ds
3307
      push1 %es
3308 push1 %fs
3309 push1 %gs
3310
      pushal
3311
3312 # Set up data segments.
3313
      movw $(SEG_KDATA<<3), %ax
3314 movw %ax. %ds
3315 movw %ax, %es
3316
3317 # Call trap(tf), where tf=%esp
3318 pushl %esp
      call trap
3319
3320 add1 $4, %esp
3321
3322 # Return falls through to trapret...
3323 .globl trapret
3324 trapret:
3325 popal
3326
      popl %gs
3327 pop1 %fs
3328 pop1 %es
3329
      popl %ds
3330 addl $0x8, %esp # trapno and errcode
3331 iret
3332
3333
3334
3335
3336
3337
3338
3339
3340
3341
3342
3343
3344
3345
3346
3347
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3349
```

```
3350 #include "types.h"
3351 #include "defs.h"
3352 #include "param.h"
3353 #include "memlayout.h"
3354 #include "mmu.h"
3355 #include "proc.h"
3356 #include "x86.h"
3357 #include "traps.h"
3358 #include "spinlock.h"
3359
3360 // Interrupt descriptor table (shared by all CPUs).
3361 struct gatedesc idt[256];
3362 extern uint vectors[]; // in vectors.S: array of 256 entry pointers
3363 struct spinlock tickslock;
3364 uint ticks:
3365
3366 void
3367 tvinit(void)
3368 {
3369 int i;
3370
3371 for(i = 0; i < 256; i++)
3372
        SETGATE(idt[i], 0, SEG_KCODE<<3, vectors[i], 0);</pre>
3373 SETGATE(idt[T_SYSCALL], 1, SEG_KCODE<<3, vectors[T_SYSCALL], DPL_USER);</pre>
3374
3375 initlock(&tickslock, "time");
3376 }
3377
3378 void
3379 idtinit(void)
3380 {
3381 lidt(idt, sizeof(idt));
3382 }
3383
3384
3385
3386
3387
3388
3389
3390
3391
3392
3393
3394
3395
3396
3397
3398
3399
```

```
3400 void
3401 trap(struct trapframe *tf)
3402 {
3403
      if(tf->trapno == T_SYSCALL){
3404
        if(myproc()->killed)
3405
          exit();
3406
        myproc()->tf = tf;
3407
        syscall();
3408
        if(myproc()->killed)
3409
          exit();
3410
        return;
3411
      }
3412
3413
      switch(tf->trapno){
3414
       case T_IRQ0 + IRQ_TIMER:
3415
        if(cpuid() == 0){
3416
          acquire(&tickslock);
3417
          ticks++:
3418
          wakeup(&ticks);
3419
          release(&tickslock);
3420
3421
        lapiceoi();
3422
        break;
3423
       case T_IRQ0 + IRQ_IDE:
3424
        ideintr():
3425
        lapiceoi():
3426
        break;
3427
       case T_IRQ0 + IRQ_IDE+1:
3428
        // Bochs generates spurious IDE1 interrupts.
3429
        break:
3430
      case T_IRQ0 + IRQ_KBD:
3431
        kbdintr();
3432
        lapiceoi();
3433
        break;
3434
       case T_IRQ0 + IRQ_COM1:
3435
        uartintr();
3436
        lapiceoi();
3437
        break;
3438
       case T_IRQ0 + 7:
3439
       case T_IRQ0 + IRQ_SPURIOUS:
3440
        cprintf("cpu%d: spurious interrupt at %x:%x\n",
3441
                 cpuid(), tf->cs, tf->eip);
3442
        lapiceoi();
3443
        break;
3444
3445
3446
3447
3448
3449
```

```
default:
3450
3451
        if(myproc() == 0 || (tf->cs&3) == 0){}
3452
          // In kernel, it must be our mistake.
3453
           cprintf("unexpected trap %d from cpu %d eip %x (cr2=0x%x)\n",
3454
                   tf->trapno, cpuid(), tf->eip, rcr2());
3455
           panic("trap");
3456
3457
        // In user space, assume process misbehaved.
3458
        cprintf("pid %d %s: trap %d err %d on cpu %d "
3459
                "eip 0x%x addr 0x%x--kill proc\n",
3460
                myproc()->pid, myproc()->name, tf->trapno,
3461
                tf->err, cpuid(), tf->eip, rcr2());
3462
        myproc()->killed = 1;
3463 }
3464
3465
      // Force process exit if it has been killed and is in user space.
     // (If it is still executing in the kernel, let it keep running
     // until it gets to the regular system call return.)
3468
     if(myproc() && myproc()->killed && (tf->cs&3) == DPL_USER)
3469
        exit();
3470
3471 // Force process to give up CPU on clock tick.
      // If interrupts were on while locks held, would need to check nlock.
3473
     if(myproc() && myproc()->state == RUNNING &&
3474
         tf->trapno == T_IRQ0+IRQ_TIMER)
3475
        yield();
3476
3477
      // Check if the process has been killed since we yielded
3478
      if(myproc() && myproc()->killed && (tf->cs&3) == DPL_USER)
3479
        exit();
3480 }
3481
3482
3483
3484
3485
3486
3487
3488
3489
3490
3491
3492
3493
3494
3495
3496
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```

```
3500 // System call numbers
3501 #define SYS_fork
3502 #define SYS_exit
3503 #define SYS_wait
3504 #define SYS_pipe
3505 #define SYS_read
3506 #define SYS_kill
3507 #define SYS_exec
3508 #define SYS_fstat 8
3509 #define SYS_chdir 9
3510 #define SYS_dup
3511 #define SYS_getpid 11
3512 #define SYS_sbrk 12
3513 #define SYS_sleep 13
3514 #define SYS_uptime 14
3515 #define SYS_open 15
3516 #define SYS_write 16
3517 #define SYS mknod 17
3518 #define SYS unlink 18
3519 #define SYS_link 19
3520 #define SYS mkdir 20
3521 #define SYS close 21
3522
3523
3524
3525
3526
3527
3528
3529
3530
3531
3532
3533
3534
3535
3536
3537
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3547
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3549
```

```
3550 #include "types.h"
3551 #include "defs.h"
3552 #include "param.h"
3553 #include "memlayout.h"
3554 #include "mmu.h"
3555 #include "proc.h"
3556 #include "x86.h"
3557 #include "syscall.h"
3558
3559 // User code makes a system call with INT T_SYSCALL.
3560 // System call number in %eax.
3561 // Arguments on the stack, from the user call to the C
3562 // library system call function. The saved user %esp points
3563 // to a saved program counter, and then the first argument.
3565 // Fetch the int at addr from the current process.
3566 int
3567 fetchint(uint addr, int *ip)
3568 {
3569 struct proc *curproc = myproc();
3570
3571 if(addr >= curproc->sz || addr+4 > curproc->sz)
3572
        return -1;
3573 *ip = *(int*)(addr);
3574 return 0:
3575 }
3576
3577 // Fetch the nul-terminated string at addr from the current process.
3578 // Doesn't actually copy the string - just sets *pp to point at it.
3579 // Returns length of string, not including nul.
3580 int
3581 fetchstr(uint addr, char **pp)
3582 {
3583 char *s, *ep;
3584 struct proc *curproc = myproc();
3585
3586 if(addr >= curproc->sz)
3587
        return -1;
3588 *pp = (char*)addr;
3589
      ep = (char*)curproc->sz;
3590
      for(s = *pp; s < ep; s++){
        if(*s == 0)
3591
3592
          return s - *pp;
3593 }
3594 return −1;
3595 }
3596
3597
3598
3599
```

```
3600 // Fetch the nth 32-bit system call argument.
                                                                                   3650 extern int sys_chdir(void);
                                                                                   3651 extern int sys_close(void);
3601 int
3602 argint(int n, int *ip)
                                                                                   3652 extern int sys_dup(void);
3603 {
                                                                                   3653 extern int sys_exec(void);
3604 return fetchint((myproc()->tf->esp) + 4 + 4*n, ip);
                                                                                   3654 extern int sys_exit(void);
3605 }
                                                                                   3655 extern int sys_fork(void);
3606
                                                                                   3656 extern int sys_fstat(void);
3607 // Fetch the nth word-sized system call argument as a pointer
                                                                                   3657 extern int sys_getpid(void);
3608 // to a block of memory of size bytes. Check that the pointer
                                                                                   3658 extern int sys_kill(void);
                                                                                   3659 extern int sys_link(void);
3609 // lies within the process address space.
3610 int
                                                                                   3660 extern int sys_mkdir(void);
3611 argptr(int n, char **pp, int size)
                                                                                   3661 extern int sys_mknod(void);
3612 {
                                                                                   3662 extern int sys_open(void);
3613 int i;
                                                                                   3663 extern int sys_pipe(void);
3614
      struct proc *curproc = mvproc():
                                                                                   3664 extern int sys_read(void);
3615
                                                                                   3665 extern int sys_sbrk(void);
3616
      if(argint(n, \&i) < 0)
                                                                                   3666 extern int sys_sleep(void);
3617
        return -1:
                                                                                   3667 extern int svs unlink(void):
3618
      if(size < 0 || (uint)i >= curproc->sz || (uint)i+size > curproc->sz)
                                                                                   3668 extern int sys_wait(void);
3619
        return -1;
                                                                                   3669 extern int sys_write(void);
3620
      *pp = (char*)i:
                                                                                   3670 extern int svs uptime(void):
3621
      return 0;
                                                                                   3671
3622 }
                                                                                   3672 static int (*syscalls[])(void) = {
3623
                                                                                   3673 [SYS_fork]
                                                                                                      sys_fork,
3624 // Fetch the nth word-sized system call argument as a string pointer.
                                                                                   3674 [SYS exit]
                                                                                                      sys_exit,
3625 // Check that the pointer is valid and the string is nul-terminated.
                                                                                   3675 [SYS_wait]
                                                                                                      sys_wait,
3626 // (There is no shared writable memory, so the string can't change
                                                                                   3676 [SYS_pipe]
                                                                                                      sys_pipe,
3627 // between this check and being used by the kernel.)
                                                                                   3677 [SYS_read]
                                                                                                      sys_read,
                                                                                   3678 [SYS_kill]
3628 int
                                                                                                      sys_kill,
3629 argstr(int n, char **pp)
                                                                                   3679 [SYS_exec]
                                                                                                      sys_exec,
3630 {
                                                                                   3680 [SYS_fstat]
                                                                                                      sys_fstat,
3631 int addr;
                                                                                   3681 [SYS_chdir]
                                                                                                      sys_chdir,
      if(argint(n, &addr) < 0)</pre>
3632
                                                                                   3682 [SYS_dup]
                                                                                                      sys_dup,
3633
         return -1;
                                                                                   3683 [SYS_getpid] sys_getpid,
3634
      return fetchstr(addr, pp);
                                                                                   3684 [SYS_sbrk]
                                                                                                      sys_sbrk,
3635 }
                                                                                   3685 [SYS_sleep]
                                                                                                      sys_sleep,
3636
                                                                                   3686 [SYS_uptime] sys_uptime,
3637
                                                                                   3687 [SYS_open]
                                                                                                      sys_open,
3638
                                                                                   3688 [SYS_write]
                                                                                                      sys_write,
3639
                                                                                   3689 [SYS_mknod]
                                                                                                      sys_mknod,
3640
                                                                                   3690 [SYS_unlink] sys_unlink,
3641
                                                                                   3691 [SYS_link]
                                                                                                      sys_link,
3642
                                                                                   3692 [SYS_mkdir]
                                                                                                      sys_mkdir,
3643
                                                                                   3693 [SYS_close]
                                                                                                      sys_close,
3644
                                                                                   3694 };
3645
                                                                                   3695
3646
                                                                                   3696
3647
                                                                                   3697
3648
                                                                                   3698
3649
                                                                                   3699
```

Sheet 36 Sheet 36

```
3700 void
                                                                                3750 #include "types.h"
                                                                                3751 #include "x86.h"
3701 syscall(void)
3702 {
                                                                                3752 #include "defs.h"
                                                                                3753 #include "date.h"
3703 int num;
3704 struct proc *curproc = myproc();
                                                                                3754 #include "param.h"
                                                                                3755 #include "memlayout.h"
3705
                                                                                3756 #include "mmu.h"
3706 num = curproc->tf->eax;
3707
      if(num > 0 && num < NELEM(syscalls) && syscalls[num]) {</pre>
                                                                                3757 #include "proc.h"
3708
        curproc->tf->eax = syscalls[num]();
                                                                                3758
3709 } else {
                                                                                3759 int
3710
       cprintf("%d %s: unknown sys call %d\n",
                                                                                3760 sys_fork(void)
3711
                curproc->pid, curproc->name, num);
                                                                                3761 {
3712
        curproc -> tf -> eax = -1;
                                                                                3762 return fork();
3713 }
                                                                                3763 }
3714 }
                                                                                3764
3715
                                                                                3765 int
3716
                                                                                3766 sys_exit(void)
3717
                                                                                3767 {
                                                                                3768 exit();
3718
3719
                                                                                3769 return 0; // not reached
                                                                                3770 }
3720
3721
                                                                                3771
3722
                                                                                3772 int
3723
                                                                                3773 sys_wait(void)
3724
                                                                                3774 {
3725
                                                                                3775 return wait();
3726
                                                                                3776 }
3727
                                                                                3777
3728
                                                                                3778 int
3729
                                                                                3779 sys_kill(void)
                                                                                3780 {
3730
                                                                                3781 int pid;
3731
3732
                                                                                3782
                                                                                3783 if(argint(0, &pid) < 0)
3733
3734
                                                                                3784
                                                                                       return -1;
3735
                                                                                3785 return kill(pid);
3736
                                                                                3786 }
3737
                                                                                3787
3738
                                                                                3788 int
3739
                                                                                3789 sys_getpid(void)
3740
                                                                                3790 {
3741
                                                                                3791 return myproc()->pid;
3742
                                                                                3792 }
                                                                                3793
3743
3744
                                                                                3794
3745
                                                                                3795
3746
                                                                                3796
3747
                                                                                3797
3748
                                                                                3798
3749
                                                                                3799
```

```
3850 struct buf {
3800 int
3801 sys_sbrk(void)
                                                                                3851 int flags;
3802 {
                                                                                3852 uint dev;
3803 int addr;
                                                                                3853 uint blockno;
3804 int n;
                                                                                3854 struct sleeplock lock;
3805
                                                                                3855 uint refcnt;
3806 if(argint(0, &n) < 0)
                                                                               3856 struct buf *prev; // LRU cache list
                                                                                3857 struct buf *next;
3807
        return -1;
3808 addr = myproc()->sz;
                                                                                3858 struct buf *qnext; // disk queue
3809 if(growproc(n) < 0)
                                                                                3859 uchar data[BSIZE];
3810
        return -1;
                                                                               3860 };
3811 return addr;
                                                                                3861 #define B_VALID 0x2 // buffer has been read from disk
3812 }
                                                                                3862 #define B_DIRTY 0x4 // buffer needs to be written to disk
3813
                                                                                3863
3814 int
                                                                               3864
3815 sys_sleep(void)
                                                                               3865
3816 {
                                                                                3866
3817 int n;
                                                                                3867
3818
      uint ticks0;
                                                                                3868
3819
                                                                                3869
3820 if(argint(0, &n) < 0)
                                                                                3870
3821
        return -1;
                                                                                3871
3822
      acquire(&tickslock);
                                                                                3872
3823
      ticks0 = ticks;
                                                                                3873
3824
      while(ticks - ticks0 < n){</pre>
                                                                                3874
3825
       if(myproc()->killed){
                                                                                3875
3826
          release(&tickslock);
                                                                                3876
3827
                                                                                3877
          return -1;
3828
                                                                                3878
3829
                                                                                3879
        sleep(&ticks, &tickslock);
3830 }
                                                                                3880
3831 release(&tickslock);
                                                                                3881
3832 return 0;
                                                                                3882
3833 }
                                                                                3883
3834
                                                                                3884
3835 // return how many clock tick interrupts have occurred
                                                                                3885
3836 // since start.
                                                                                3886
3837 int
                                                                                3887
3838 sys_uptime(void)
                                                                                3888
3839 {
                                                                                3889
3840 uint xticks;
                                                                                3890
3841
                                                                                3891
3842 acquire(&tickslock);
                                                                                3892
3843
      xticks = ticks;
                                                                                3893
3844
      release(&tickslock);
                                                                                3894
3845
      return xticks;
                                                                                3895
3846 }
                                                                                3896
3847
                                                                                3897
3848
                                                                                3898
3849
                                                                                3899
```

3900 // Long-term locks for processes	3950 #define O_RDONLY 0x000
3901 struct sleeplock {	3951 #define O_WRONLY 0x001
3902 uint locked; // Is the lock held?	3952 #define O_RDWR 0x002
3903 struct spinlock lk; // spinlock protecting this sleep lock	3953 #define O_CREATE 0x200
3904	3954
3905 // For debugging:	3955
3906 char *name; // Name of lock.	3956
3907 int pid; // Process holding lock	3957
3908 };	3958
3909	3959
3910	3960
3911	3961
3912	3962
3913	3963
3914	3964
3915	3965
3916	3966
3917	3967
3918	3968
3919	3969
3920	3970
3921	3971
3922	3972
3923	3973
3924	3974
3925	3975
3926	3976
3927	3977
3928	3978
3929	3979
3930	3980
3931	3981
3932	3982
3933	3983
3934	3984
3935	3985
3936	3986
3937	3987
3938	3988
3939	3989
3940	3990
3941	3991
3942	3992
3943	3993
3944	3994
3945	3995
3946	3996
3947	3997
3948	3998
3949	3999

```
4000 #define T_DIR 1 // Directory
4001 #define T_FILE 2 // File
4002 #define T_DEV 3 // Device
4003
4004 struct stat {
4005 short type; // Type of file
4006
      int dev:
                   // File system's disk device
4007
      uint ino;
                   // Inode number
4008
      short nlink; // Number of links to file
      uint size; // Size of file in bytes
4009
4010 };
4011
4012
4013
4014
4015
4016
4017
4018
4019
4020
4021
4022
4023
4024
4025
4026
4027
4028
4029
4030
4031
4032
4033
4034
4035
4036
4037
4038
4039
4040
4041
4042
4043
4044
4045
4046
4047
4048
4049
```

```
4050 // On-disk file system format.
4051 // Both the kernel and user programs use this header file.
4052
4053
4054 #define ROOTINO 1 // root i-number
4055 #define BSIZE 512 // block size
4056
4057 // Disk layout:
4058 // [ boot block | super block | log | inode blocks |
                                                free bit map | data blocks]
4059 //
4060 //
4061 // mkfs computes the super block and builds an initial file system. The
4062 // super block describes the disk layout:
4063 struct superblock {
4064 uint size:
                         // Size of file system image (blocks)
4065
      uint nblocks:
                         // Number of data blocks
     uint ninodes;
4066
                         // Number of inodes.
4067 uint nlog:
                         // Number of log blocks
4068 uint logstart;
                         // Block number of first log block
4069
      uint inodestart; // Block number of first inode block
4070 uint bmapstart:
                        // Block number of first free map block
4071 };
4072
4073 #define NDIRECT 12
4074 #define NINDIRECT (BSIZE / sizeof(uint))
4075 #define MAXFILE (NDIRECT + NINDIRECT)
4076
4077 // On-disk inode structure
4078 struct dinode {
4079 short type;
                            // File type
4080 short major;
                            // Major device number (T_DEV only)
4081 short minor;
                            // Minor device number (T_DEV only)
4082 short nlink;
                            // Number of links to inode in file system
4083 uint size;
                            // Size of file (bytes)
4084 uint addrs[NDIRECT+1]; // Data block addresses
4085 };
4086
4087
4088
4089
4090
4091
4092
4093
4094
4095
4096
4097
4098
4099
```

```
4100 // Inodes per block.
                                                                                 4150 struct file {
4101 #define IPB
                          (BSIZE / sizeof(struct dinode))
                                                                                 4151 enum { FD_NONE, FD_PIPE, FD_INODE } type;
4102
                                                                                 4152 int ref; // reference count
4103 // Block containing inode i
                                                                                 4153 char readable;
4104 #define IBLOCK(i, sb)
                              ((i) / IPB + sb.inodestart)
                                                                                 4154 char writable;
4105
                                                                                 4155 struct pipe *pipe;
4106 // Bitmap bits per block
                                                                                 4156 struct inode *ip;
4107 #define BPB
                          (BSIZE*8)
                                                                                 4157 uint off;
4108
                                                                                 4158 };
4109 // Block of free map containing bit for block b
                                                                                 4159
4110 #define BBLOCK(b, sb) (b/BPB + sb.bmapstart)
                                                                                 4160
                                                                                 4161 // in-memory copy of an inode
4112 // Directory is a file containing a sequence of dirent structures.
                                                                                 4162 struct inode {
4113 #define DIRSIZ 14
                                                                                 4163 uint dev;
                                                                                                            // Device number
4114
                                                                                 4164
                                                                                       uint inum:
                                                                                                            // Inode number
4115 struct dirent {
                                                                                 4165 int ref;
                                                                                                            // Reference count
4116 ushort inum;
                                                                                 4166
                                                                                       struct sleeplock lock; // protects everything below here
4117
      char name[DIRSIZ]:
                                                                                 4167
                                                                                       int valid:
                                                                                                           // inode has been read from disk?
4118 };
                                                                                 4168
                                                                                 4169 short type;
4119
                                                                                                            // copy of disk inode
4120
                                                                                 4170 short major:
4121
                                                                                 4171 short minor:
4122
                                                                                 4172 short nlink;
4123
                                                                                 4173 uint size;
4124
                                                                                 4174    uint addrs[NDIRECT+1];
4125
                                                                                 4175 };
4126
                                                                                 4176
4127
                                                                                 4177 // table mapping major device number to
4128
                                                                                 4178 // device functions
4129
                                                                                 4179 struct devsw {
4130
                                                                                 4180 int (*read)(struct inode*, char*, int);
4131
                                                                                 4181 int (*write)(struct inode*, char*, int);
4132
                                                                                 4182 };
4133
                                                                                 4183
4134
                                                                                 4184 extern struct devsw devsw[];
4135
                                                                                 4185
4136
                                                                                 4186 #define CONSOLE 1
4137
                                                                                 4187
4138
                                                                                 4188
4139
                                                                                 4189
4140
                                                                                 4190
4141
                                                                                 4191
4142
                                                                                 4192
4143
                                                                                 4193
4144
                                                                                 4194
4145
                                                                                 4195
4146
                                                                                 4196
4147
                                                                                 4197
4148
                                                                                 4198
4149
                                                                                 4199
```

```
4200 // Simple PIO-based (non-DMA) IDE driver code.
                                                                                  4250 void
                                                                                  4251 ideinit(void)
4201
4202 #include "types.h"
                                                                                  4252 {
4203 #include "defs.h"
                                                                                  4253 int i;
4204 #include "param.h"
                                                                                  4254
4205 #include "memlayout.h"
                                                                                  4255 initlock(&idelock, "ide");
4206 #include "mmu.h"
                                                                                  4256
                                                                                        ioapicenable(IRQ_IDE, ncpu - 1);
4207 #include "proc.h"
                                                                                  4257
                                                                                        idewait(0);
4208 #include "x86.h"
                                                                                  4258
4209 #include "traps.h"
                                                                                  4259
                                                                                        // Check if disk 1 is present
4210 #include "spinlock.h"
                                                                                  4260
                                                                                        outb(0x1f6, 0xe0 | (1<<4));
4211 #include "sleeplock.h"
                                                                                  4261 for(i=0; i<1000; i++){
4212 #include "fs.h"
                                                                                  4262
                                                                                          if(inb(0x1f7) != 0){
4213 #include "buf.h"
                                                                                  4263
                                                                                             havedisk1 = 1;
4214
                                                                                  4264
                                                                                             break:
4215 #define SECTOR SIZE
                                                                                  4265
                           512
4216 #define IDE BSY
                           0x80
                                                                                  4266 }
4217 #define IDE DRDY
                           0x40
                                                                                  4267
4218 #define IDE DF
                           0x20
                                                                                  4268 // Switch back to disk 0.
4219 #define IDE_ERR
                           0x01
                                                                                  4269
                                                                                        outb(0x1f6, 0xe0 | (0<<4));
4220
                                                                                  4270 }
4221 #define IDE CMD READ 0x20
                                                                                  4271
4222 #define IDE_CMD_WRITE 0x30
                                                                                  4272 // Start the request for b. Caller must hold idelock.
4223 #define IDE_CMD_RDMUL 0xc4
                                                                                  4273 static void
                                                                                  4274 idestart(struct buf *b)
4224 #define IDE CMD WRMUL 0xc5
4225
                                                                                  4275 {
                                                                                  4276 if(b == 0)
4226 // idequeue points to the buf now being read/written to the disk.
4227 // idequeue->qnext points to the next buf to be processed.
                                                                                  4277
                                                                                          panic("idestart");
4228 // You must hold idelock while manipulating queue.
                                                                                  4278
                                                                                       if(b->blockno >= FSSIZE)
4229
                                                                                  4279
                                                                                          panic("incorrect blockno");
4230 static struct spinlock idelock;
                                                                                  4280
                                                                                       int sector_per_block = BSIZE/SECTOR_SIZE;
4231 static struct buf *idequeue;
                                                                                        int sector = b->blockno * sector_per_block;
                                                                                  4281
4232
                                                                                  4282
                                                                                        int read_cmd = (sector_per_block == 1) ? IDE_CMD_READ : IDE_CMD_RDMUL;
4233 static int havedisk1;
                                                                                  4283
                                                                                        int write_cmd = (sector_per_block == 1) ? IDE_CMD_WRITE : IDE_CMD_WRMUL;
4234 static void idestart(struct buf*);
                                                                                  4284
                                                                                       if (sector_per_block > 7) panic("idestart");
4235
                                                                                  4285
4236 // Wait for IDE disk to become ready.
                                                                                  4286
4237 static int
                                                                                  4287
                                                                                        idewait(0);
4238 idewait(int checkerr)
                                                                                  4288
                                                                                        outb(0x3f6, 0); // generate interrupt
4239 {
                                                                                  4289
                                                                                        outb(0x1f2, sector_per_block); // number of sectors
4240 int r;
                                                                                  4290
                                                                                        outb(0x1f3, sector & 0xff);
4241
                                                                                  4291
                                                                                        outb(0x1f4, (sector >> 8) & 0xff);
4242
      while(((r = inb(0x1f7)) & (IDE_BSY|IDE_DRDY)) != IDE_DRDY)
                                                                                  4292
                                                                                        outb(0x1f5, (sector >> 16) & 0xff);
4243
                                                                                  4293
                                                                                        outb(0x1f6, 0xe0 | ((b\rightarrow dev\&1)<<4) | ((sector>>24)\&0x0f));
4244 if(checkerr && (r & (IDE_DF|IDE_ERR)) != 0)
                                                                                  4294
                                                                                        if(b->flags & B_DIRTY){
4245
        return -1;
                                                                                  4295
                                                                                          outb(0x1f7, write_cmd);
4246
      return 0;
                                                                                  4296
                                                                                          outs1(0x1f0, b->data, BSIZE/4);
4247 }
                                                                                  4297 } else {
4248
                                                                                  4298
                                                                                          outb(0x1f7, read_cmd);
4249
                                                                                  4299 }
```

```
4400 // Buffer cache.
                                                                                   4450
                                                                                          // Create linked list of buffers
4401 //
                                                                                   4451
                                                                                          bcache.head.prev = &bcache.head;
4402 // The buffer cache is a linked list of buf structures holding
                                                                                   4452
                                                                                          bcache.head.next = &bcache.head;
4403 // cached copies of disk block contents. Caching disk blocks
                                                                                   4453
                                                                                          for(b = bcache.buf; b < bcache.buf+NBUF; b++){</pre>
4404 // in memory reduces the number of disk reads and also provides
                                                                                   4454
                                                                                            b->next = bcache.head.next;
4405 // a synchronization point for disk blocks used by multiple processes.
                                                                                   4455
                                                                                            b->prev = &bcache.head;
4406 //
                                                                                   4456
                                                                                            initsleeplock(&b->lock, "buffer");
4407 // Interface:
                                                                                   4457
                                                                                            bcache.head.next->prev = b;
4408 // * To get a buffer for a particular disk block, call bread.
                                                                                   4458
                                                                                            bcache.head.next = b;
4409 // * After changing buffer data, call bwrite to write it to disk.
                                                                                   4459 }
4410 // * When done with the buffer, call brelse.
                                                                                   4460 }
4411 // * Do not use the buffer after calling brelse.
                                                                                   4461
4412 // * Only one process at a time can use a buffer,
                                                                                   4462 // Look through buffer cache for block on device dev.
4413 //
            so do not keep them longer than necessary.
                                                                                   4463 // If not found, allocate a buffer.
4414 //
                                                                                   4464 // In either case, return locked buffer.
                                                                                   4465 static struct buf*
4415 // The implementation uses two state flags internally:
4416 // * B_VALID: the buffer data has been read from the disk.
                                                                                   4466 bget(uint dev, uint blockno)
4417 // * B DIRTY: the buffer data has been modified
                                                                                   4467 {
            and needs to be written to disk.
                                                                                   4468 struct buf *b;
4418 //
4419
                                                                                   4469
4420 #include "types.h"
                                                                                   4470
                                                                                          acquire(&bcache.lock):
4421 #include "defs.h"
                                                                                   4471
4422 #include "param.h"
                                                                                   4472
                                                                                         // Is the block already cached?
4423 #include "spinlock.h"
                                                                                   4473
                                                                                          for(b = bcache.head.next; b != &bcache.head; b = b->next){
4424 #include "sleeplock.h"
                                                                                   4474
                                                                                            if(b->dev == dev && b->blockno == blockno){
4425 #include "fs.h"
                                                                                   4475
                                                                                              b->refcnt++:
4426 #include "buf.h"
                                                                                   4476
                                                                                               release(&bcache.lock);
4427
                                                                                   4477
                                                                                               acquiresleep(&b->lock);
4428 struct {
                                                                                   4478
                                                                                               return b;
4429 struct spinlock lock;
                                                                                   4479
4430 struct buf buf[NBUF];
                                                                                   4480 }
4431
                                                                                   4481
4432 // Linked list of all buffers, through prev/next.
                                                                                   4482 // Not cached; recycle an unused buffer.
4433 // head.next is most recently used.
                                                                                   4483
                                                                                          // Even if refcnt==0, B_DIRTY indicates a buffer is in use
4434 struct buf head;
                                                                                          // because log.c has modified it but not yet committed it.
4435 } bcache;
                                                                                   4485
                                                                                          for(b = bcache.head.prev; b != &bcache.head; b = b->prev){
4436
                                                                                   4486
                                                                                            if(b\rightarrow refcnt == 0 \&\& (b\rightarrow flags \& B\_DIRTY) == 0) 
4437 void
                                                                                   4487
                                                                                              b->dev = dev:
4438 binit(void)
                                                                                   4488
                                                                                              b->blockno = blockno:
4439 {
                                                                                   4489
                                                                                              b\rightarrow flags = 0;
4440 struct buf *b;
                                                                                   4490
                                                                                              b \rightarrow refcnt = 1:
4441
                                                                                   4491
                                                                                               release(&bcache.lock);
4442
       initlock(&bcache.lock, "bcache");
                                                                                   4492
                                                                                              acquiresleep(&b->lock);
4443
                                                                                   4493
                                                                                               return b;
4444
                                                                                   4494
4445
                                                                                   4495 }
4446
                                                                                   4496
                                                                                          panic("bget: no buffers");
4447
                                                                                   4497 }
4448
                                                                                   4498
4449
                                                                                   4499
```

Sheet 44 Sheet 44

```
4500 // Return a locked buf with the contents of the indicated block.
                                                                                4550 // Blank page.
4501 struct buf*
                                                                                4551
4502 bread(uint dev, uint blockno)
                                                                                4552
4503 {
                                                                                4553
4504 struct buf *b;
                                                                                4554
4505
                                                                                4555
4506 b = bget(dev, blockno);
                                                                                4556
4507 if((b->flags & B_VALID) == 0) {
                                                                                4557
4508
       iderw(b);
                                                                                4558
4509 }
                                                                                4559
4510 return b;
                                                                                4560
4511 }
                                                                                4561
4512
                                                                                4562
4513 // Write b's contents to disk. Must be locked.
                                                                                4563
4514 void
                                                                                4564
4515 bwrite(struct buf *b)
                                                                                4565
4516 {
                                                                                4566
4517 if(!holdingsleep(&b->lock))
                                                                                4567
4518
        panic("bwrite");
                                                                                4568
4519
      b->flags |= B_DIRTY;
                                                                                4569
4520 iderw(b);
                                                                                4570
4521 }
                                                                                4571
4522
                                                                                4572
4523 // Release a locked buffer.
                                                                                4573
4524 // Move to the head of the MRU list.
                                                                                4574
4525 void
                                                                                4575
4526 brelse(struct buf *b)
                                                                                4576
4527 {
                                                                                4577
4528 if(!holdingsleep(&b->lock))
                                                                                4578
4529
                                                                                4579
        panic("brelse");
                                                                                4580
4530
4531
      releasesleep(&b->lock);
                                                                                4581
4532
                                                                                4582
4533
      acquire(&bcache.lock);
                                                                                4583
4534
      b->refcnt--;
                                                                                4584
4535 if (b->refcnt == 0) {
                                                                                4585
4536
        // no one is waiting for it.
                                                                                4586
4537
        b->next->prev = b->prev;
                                                                                4587
4538
        b->prev->next = b->next;
                                                                                4588
4539
        b->next = bcache.head.next;
                                                                                4589
4540
        b->prev = &bcache.head;
                                                                                4590
4541
        bcache.head.next->prev = b;
                                                                                4591
4542
        bcache.head.next = b;
                                                                                4592
4543 }
                                                                                4593
4544
                                                                                4594
4545
      release(&bcache.lock);
                                                                                4595
4546 }
                                                                                4596
4547
                                                                                4597
4548
                                                                                4598
4549
                                                                                4599
```

```
4650 int
4600 // Sleeping locks
                                                                                      4651 holdingsleep(struct sleeplock *lk)
4601
4602 #include "types.h"
                                                                                      4652 {
4603 #include "defs.h"
                                                                                      4653 int r;
4604 #include "param.h"
                                                                                      4654
4605 #include "x86.h"
                                                                                      4655 acquire(\&lk \rightarrow lk);
4606 #include "memlayout.h"
                                                                                      4656
                                                                                            r = 1k -> 1ocked;
4607 #include "mmu.h"
                                                                                      4657
                                                                                            release(&lk->lk);
4608 #include "proc.h"
                                                                                      4658
                                                                                             return r;
4609 #include "spinlock.h"
                                                                                      4659 }
4610 #include "sleeplock.h"
                                                                                      4660
4611
                                                                                      4661
4612 void
                                                                                      4662
4613 initsleeplock(struct sleeplock *lk, char *name)
                                                                                      4663
                                                                                      4664
4615 initlock(&lk->lk, "sleep lock");
                                                                                      4665
4616 1k->name = name;
                                                                                      4666
4617 1k \rightarrow 1ocked = 0;
                                                                                      4667
4618 \quad 1k - pid = 0;
                                                                                      4668
4619 }
                                                                                      4669
4620
                                                                                      4670
4621 void
                                                                                      4671
4622 acquiresleep(struct sleeplock *lk)
                                                                                      4672
4623 {
                                                                                      4673
4624 acquire(\&lk \rightarrow lk);
                                                                                      4674
4625
       while (lk->locked) {
                                                                                      4675
4626
        sleep(lk, &lk->lk);
                                                                                      4676
4627 }
                                                                                      4677
4628 1k \rightarrow 1ocked = 1;
                                                                                      4678
4629 1k \rightarrow pid = myproc() \rightarrow pid;
                                                                                      4679
4630 release(&lk->lk);
                                                                                      4680
4631 }
                                                                                      4681
4632
                                                                                      4682
4633 void
                                                                                      4683
4634 releasesleep(struct sleeplock *lk)
                                                                                      4684
4635 {
                                                                                      4685
4636 acquire(&lk->lk);
                                                                                      4686
4637 1k \rightarrow 1ocked = 0;
                                                                                      4687
4638 1k - pid = 0;
                                                                                      4688
4639 wakeup(1k);
                                                                                      4689
4640 release(&lk->lk);
                                                                                      4690
4641 }
                                                                                      4691
4642
                                                                                      4692
4643
                                                                                      4693
4644
                                                                                      4694
4645
                                                                                      4695
4646
                                                                                      4696
4647
                                                                                      4697
4648
                                                                                      4698
4649
                                                                                      4699
```

```
4700 #include "types.h"
                                                                                 4750 struct log log;
4701 #include "defs.h"
                                                                                 4751
4702 #include "param.h"
                                                                                 4752 static void recover_from_log(void);
4703 #include "spinlock.h"
                                                                                 4753 static void commit();
4704 #include "sleeplock.h"
                                                                                 4754
4705 #include "fs.h"
                                                                                 4755 void
4706 #include "buf.h"
                                                                                 4756 initlog(int dev)
4707
                                                                                 4757 {
4708 // Simple logging that allows concurrent FS system calls.
                                                                                 4758 if (sizeof(struct logheader) >= BSIZE)
                                                                                          panic("initlog: too big logheader");
                                                                                 4759
4709 //
4710 // A log transaction contains the updates of multiple FS system
                                                                                 4760
4711 // calls. The logging system only commits when there are
                                                                                 4761 struct superblock sb:
4712 // no FS system calls active. Thus there is never
                                                                                 4762 initlock(&log.lock, "log");
4713 // any reasoning required about whether a commit might
                                                                                 4763
                                                                                        readsb(dev, &sb);
4714 // write an uncommitted system call's updates to disk.
                                                                                 4764
                                                                                       log.start = sb.logstart:
4715 //
                                                                                 4765
                                                                                        log.size = sb.nlog;
4716 // A system call should call begin_op()/end_op() to mark
                                                                                 4766
                                                                                        log.dev = dev;
4717 // its start and end. Usually begin op() just increments
                                                                                 4767
                                                                                        recover_from_log();
4718 // the count of in-progress FS system calls and returns.
                                                                                 4768 }
4719 // But if it thinks the log is close to running out, it
                                                                                 4769
4720 // sleeps until the last outstanding end op() commits.
                                                                                 4770 // Copy committed blocks from log to their home location
4721 //
                                                                                 4771 static void
4722 // The log is a physical re-do log containing disk blocks.
                                                                                 4772 install_trans(void)
4723 // The on-disk log format:
                                                                                 4773 {
                                                                                 4774 int tail:
4724 // header block, containing block #s for block A, B, C, ...
                                                                                 4775
4725 //
         block A
4726 //
         block B
                                                                                 4776
                                                                                        for (tail = 0; tail < log.lh.n; tail++) {</pre>
4727 // block C
                                                                                          struct buf *lbuf = bread(log.dev, log.start+tail+1); // read log block
                                                                                 4777
                                                                                 4778
                                                                                          struct buf *dbuf = bread(log.dev, log.lh.block[tail]); // read dst
4728 // ...
4729 // Log appends are synchronous.
                                                                                 4779
                                                                                          memmove(dbuf->data, lbuf->data, BSIZE); // copy block to dst
4730
                                                                                 4780
                                                                                          bwrite(dbuf); // write dst to disk
4731 // Contents of the header block, used for both the on-disk header block
                                                                                 4781
                                                                                          brelse(lbuf);
                                                                                 4782
4732 // and to keep track in memory of logged block# before commit.
                                                                                          brelse(dbuf);
4733 struct logheader {
                                                                                 4783 }
4734 int n;
                                                                                 4784 }
4735 int block[LOGSIZE];
                                                                                 4785
4736 };
                                                                                 4786 // Read the log header from disk into the in-memory log header
4737
                                                                                 4787 static void
4738 struct log {
                                                                                 4788 read head(void)
4739 struct spinlock lock;
                                                                                 4789 {
4740 int start;
                                                                                 4790 struct buf *buf = bread(log.dev, log.start);
4741 int size:
                                                                                 4791 struct logheader *lh = (struct logheader *) (buf->data);
4742 int outstanding; // how many FS sys calls are executing.
                                                                                 4792 int i;
4743 int committing; // in commit(), please wait.
                                                                                 4793
                                                                                        log.lh.n = lh->n;
4744 int dev:
                                                                                 4794
                                                                                        for (i = 0; i < log.lh.n; i++) {
4745 struct logheader lh;
                                                                                          log.lh.block[i] = lh->block[i];
                                                                                 4795
4746 };
                                                                                 4796 }
4747
                                                                                 4797 brelse(buf);
                                                                                 4798 }
4748
4749
                                                                                 4799
```

```
4800 // Write in-memory log header to disk.
                                                                                  4850 // called at the end of each FS system call.
4801 // This is the true point at which the
                                                                                  4851 // commits if this was the last outstanding operation.
4802 // current transaction commits.
                                                                                  4852 void
4803 static void
                                                                                  4853 end_op(void)
4804 write_head(void)
                                                                                  4854 {
4805 {
                                                                                  4855 int do_commit = 0;
4806 struct buf *buf = bread(log.dev, log.start);
                                                                                  4856
4807
      struct logheader *hb = (struct logheader *) (buf->data);
                                                                                  4857
                                                                                         acquire(&log.lock);
4808 int i;
                                                                                  4858
                                                                                         log.outstanding -= 1;
      hb \rightarrow n = log.lh.n;
4809
                                                                                  4859
                                                                                         if(log.committing)
4810 for (i = 0; i < log.lh.n; i++) {
                                                                                  4860
                                                                                           panic("log.committing");
4811
       hb->block[i] = log.lh.block[i];
                                                                                  4861
                                                                                        if(\log.outstanding == 0){
4812 }
                                                                                  4862
                                                                                           do_{commit} = 1;
4813 bwrite(buf);
                                                                                  4863
                                                                                           log.committing = 1;
4814 brelse(buf);
                                                                                  4864 } else {
4815 }
                                                                                  4865
                                                                                           // begin_op() may be waiting for log space,
4816
                                                                                  4866
                                                                                           // and decrementing log.outstanding has decreased
4817 static void
                                                                                  4867
                                                                                           // the amount of reserved space.
4818 recover_from_log(void)
                                                                                  4868
                                                                                           wakeup(&log);
4819 {
                                                                                  4869
                                                                                         release(&log.lock);
4820 read head():
                                                                                  4870
4821 install_trans(); // if committed, copy from log to disk
                                                                                  4871
4822 \quad log.lh.n = 0;
                                                                                  4872
                                                                                         if(do_commit){
4823 write_head(); // clear the log
                                                                                  4873
                                                                                           // call commit w/o holding locks, since not allowed
                                                                                  4874
4824 }
                                                                                           // to sleep with locks.
                                                                                  4875
4825
                                                                                           commit();
4826 // called at the start of each FS system call.
                                                                                  4876
                                                                                           acquire(&log.lock);
4827 void
                                                                                  4877
                                                                                           log.committing = 0;
4828 begin_op(void)
                                                                                  4878
                                                                                           wakeup(&log);
4829 {
                                                                                  4879
                                                                                           release(&log.lock);
4830 acquire(&log.lock);
                                                                                  4880 }
4831
      while(1){
                                                                                  4881 }
4832
        if(log.committing){
                                                                                  4882
4833
           sleep(&log, &log.lock);
                                                                                  4883 // Copy modified blocks from cache to log.
4834
        } else if(log.lh.n + (log.outstanding+1)*MAXOPBLOCKS > LOGSIZE){
                                                                                  4884 static void
4835
          // this op might exhaust log space; wait for commit.
                                                                                  4885 write_log(void)
4836
           sleep(&log, &log.lock);
                                                                                  4886 {
4837
        } else {
                                                                                  4887 int tail;
4838
          log.outstanding += 1;
                                                                                  4888
4839
           release(&log.lock);
                                                                                  4889
                                                                                         for (tail = 0; tail < log.lh.n; tail++) {</pre>
4840
          break;
                                                                                  4890
                                                                                           struct buf *to = bread(log.dev, log.start+tail+1); // log block
4841
        }
                                                                                  4891
                                                                                           struct buf *from = bread(log.dev, log.lh.block[tail]); // cache block
4842 }
                                                                                  4892
                                                                                           memmove(to->data, from->data, BSIZE);
4843 }
                                                                                  4893
                                                                                           bwrite(to); // write the log
4844
                                                                                  4894
                                                                                           brelse(from);
4845
                                                                                  4895
                                                                                           brelse(to);
4846
                                                                                  4896 }
4847
                                                                                  4897 }
4848
                                                                                  4898
4849
                                                                                  4899
```

4950 // File system implementation. Five layers:

```
4900 static void
4901 commit()
4902 {
4903 if (\log.1h.n > 0) {
4904
        write_log();
                         // Write modified blocks from cache to log
4905
                         // Write header to disk -- the real commit
        write_head();
4906
        install_trans(); // Now install writes to home locations
4907
        log.1h.n = 0;
4908
                        // Erase the transaction from the log
        write_head();
4909 }
4910 }
4911
4912 // Caller has modified b->data and is done with the buffer.
4913 // Record the block number and pin in the cache with B_DIRTY.
4914 // commit()/write_log() will do the disk write.
4915 //
4916 // log_write() replaces bwrite(); a typical use is:
4917 // bp = bread(...)
4918 // modify bp->data[]
4919 // log_write(bp)
4920 //
         brelse(bp)
4921 void
4922 log_write(struct buf *b)
4923 {
4924 int i:
4925
4926
      if (log.lh.n >= LOGSIZE || log.lh.n >= log.size - 1)
        panic("too big a transaction");
4927
      if (log.outstanding < 1)</pre>
4928
4929
        panic("log_write outside of trans");
4930
4931
      acquire(&log.lock);
4932
      for (i = 0; i < log.lh.n; i++) {
4933
        if (log.lh.block[i] == b->blockno) // log absorbtion
4934
          break:
4935
4936 log.lh.block[i] = b->blockno;
4937 if (i == log.lh.n)
4938
        loa.lh.n++:
4939
      b->flags |= B_DIRTY; // prevent eviction
4940
      release(&log.lock);
4941 }
4942
4943
4944
4945
4946
4947
4948
4949
```

```
4951 // + Blocks: allocator for raw disk blocks.
4952 // + Log: crash recovery for multi-step updates.
4953 // + Files: inode allocator, reading, writing, metadata.
4954 // + Directories: inode with special contents (list of other inodes!)
4955 // + Names: paths like /usr/rtm/xv6/fs.c for convenient naming.
4956 //
4957 // This file contains the low-level file system manipulation
4958 // routines. The (higher-level) system call implementations
4959 // are in sysfile.c.
4960
4961 #include "types.h"
4962 #include "defs.h"
4963 #include "param.h"
4964 #include "stat.h"
4965 #include "mmu.h"
4966 #include "proc.h"
4967 #include "spinlock.h"
4968 #include "sleeplock.h"
4969 #include "fs.h"
4970 #include "buf.h"
4971 #include "file.h"
4972
4973 #define min(a, b) ((a) < (b) ? (a) : (b))
4974 static void itrunc(struct inode*):
4975 // there should be one superblock per disk device, but we run with
4976 // only one device
4977 struct superblock sb;
4978
4979 // Read the super block.
4980 void
4981 readsb(int dev, struct superblock *sb)
4982 {
4983 struct buf *bp;
4984
4985
      bp = bread(dev, 1);
4986
      memmove(sb, bp->data, sizeof(*sb));
4987
      brelse(bp);
4988 }
4989
4990
4991
4992
4993
4994
4995
4996
4997
4998
4999
```

```
5000 // Zero a block.
5001 static void
5002 bzero(int dev, int bno)
5003 {
5004 struct buf *bp;
5005
5006 bp = bread(dev, bno);
5007 memset(bp->data, 0, BSIZE);
5008 log_write(bp);
5009 brelse(bp);
5010 }
5011
5012 // Blocks.
5013
5014 // Allocate a zeroed disk block.
5015 static uint
5016 balloc(uint dev)
5017 {
5018 int b. bi. m:
5019
      struct buf *bp;
5020
5021
      bp = 0:
5022
      for(b = 0; b < sb.size; b += BPB){
5023
        bp = bread(dev, BBLOCK(b, sb));
        for(bi = 0: bi < BPB && b + bi < sb.size: bi++){
5024
5025
          m = 1 \ll (bi \% 8);
5026
          if((bp->data[bi/8] \& m) == 0){ // Is block free?}
            bp->data[bi/8] |= m; // Mark block in use.
5027
5028
            log_write(bp);
5029
            brelse(bp);
5030
            bzero(dev, b + bi);
5031
            return b + bi;
5032
          }
5033
        }
5034
        brelse(bp);
5035
5036
      panic("balloc: out of blocks");
5037 }
5038
5039
5040
5041
5042
5043
5044
5045
5046
5047
5048
5049
```

```
5050 // Free a disk block.
5051 static void
5052 bfree(int dev, uint b)
5053 {
5054 struct buf *bp;
5055 int bi, m;
5056
readsb(dev, &sb);
5058 bp = bread(dev, BBLOCK(b, sb));
5059
      bi = b % BPB;
5060 m = 1 \ll (bi \% 8);
5061 	 if((bp->data[bi/8] \& m) == 0)
5062
        panic("freeing free block");
5063 bp->data[bi/8] &= ~m;
5064 log write(bp):
5065 brelse(bp);
5066 }
5067
5068 // Inodes.
5069 //
5070 // An inode describes a single unnamed file.
5071 // The inode disk structure holds metadata: the file's type.
5072 // its size, the number of links referring to it, and the
5073 // list of blocks holding the file's content.
5074 //
5075 // The inodes are laid out sequentially on disk at
5076 // sb.startinode. Each inode has a number, indicating its
5077 // position on the disk.
5078 //
5079 // The kernel keeps a cache of in-use inodes in memory
5080 // to provide a place for synchronizing access
5081 // to inodes used by multiple processes. The cached
5082 // inodes include book-keeping information that is
5083 // not stored on disk: ip->ref and ip->valid.
5084 //
5085 // An inode and its in-memory representation go through a
5086 // sequence of states before they can be used by the
5087 // rest of the file system code.
5088 //
5089 // * Allocation: an inode is allocated if its type (on disk)
5090 // is non-zero, ialloc() allocates, and iput() frees if
5091 // the reference and link counts have fallen to zero.
5092 //
5093 // * Referencing in cache: an entry in the inode cache
5094 // is free if ip->ref is zero. Otherwise ip->ref tracks
5095 // the number of in-memory pointers to the entry (open
5096 // files and current directories). iget() finds or
5097 // creates a cache entry and increments its ref; iput()
         decrements ref.
5098 //
5099 //
```

```
5100 // * Valid: the information (type, size, &c) in an inode
                                                                                 5150 }
5101 // cache entry is only correct when ip->valid is 1.
                                                                                 5151
5102 // ilock() reads the inode from
                                                                                 5152
                                                                                        readsb(dev, &sb);
5103 // the disk and sets ip->valid, while iput() clears
                                                                                 5153
                                                                                        cprintf("sb: size %d nblocks %d ninodes %d nlog %d logstart %d\
5104 // ip->valid if ip->ref has fallen to zero.
                                                                                 5154 inodestart %d bmap start %d\n", sb.size, sb.nblocks,
5105 //
                                                                                 5155
                                                                                                sb.ninodes, sb.nlog, sb.logstart, sb.inodestart,
5106 // * Locked: file system code may only examine and modify
                                                                                 5156
                                                                                                sb.bmapstart);
5107 // the information in an inode and its content if it
                                                                                 5157 }
5108 // has first locked the inode.
                                                                                 5158
5109 //
                                                                                 5159 static struct inode* iget(uint dev, uint inum);
5110 // Thus a typical sequence is:
                                                                                 5160
5111 // ip = iget(dev, inum)
                                                                                 5161
5112 // ilock(ip)
                                                                                 5162
5113 // ... examine and modify ip->xxx ...
                                                                                 5163
5114 // iunlock(ip)
                                                                                 5164
5115 // iput(ip)
                                                                                 5165
5116 //
                                                                                 5166
5117 // ilock() is separate from iget() so that system calls can
                                                                                 5167
5118 // get a long-term reference to an inode (as for an open file)
                                                                                 5168
5119 // and only lock it for short periods (e.g., in read()).
                                                                                 5169
5120 // The separation also helps avoid deadlock and races during
                                                                                 5170
5121 // pathname lookup. iget() increments ip->ref so that the inode
                                                                                 5171
5122 // stays cached and pointers to it remain valid.
                                                                                 5172
5123 //
                                                                                 5173
5124 // Many internal file system functions expect the caller to
                                                                                 5174
5125 // have locked the inodes involved; this lets callers create
                                                                                 5175
5126 // multi-step atomic operations.
                                                                                 5176
5127 //
                                                                                 5177
5128 // The icache.lock spin-lock protects the allocation of icache
                                                                                 5178
5129 // entries. Since ip->ref indicates whether an entry is free,
                                                                                 5179
5130 // and ip->dev and ip->inum indicate which i-node an entry
                                                                                 5180
5131 // holds, one must hold icache.lock while using any of those fields.
                                                                                 5181
5132 //
                                                                                 5182
5133 // An ip->lock sleep-lock protects all ip-> fields other than ref,
                                                                                 5183
5134 // dev, and inum. One must hold ip->lock in order to
                                                                                 5184
5135 // read or write that inode's ip->valid, ip->size, ip->type, &c.
                                                                                 5185
5136
                                                                                 5186
5137 struct {
                                                                                 5187
5138 struct spinlock lock;
                                                                                 5188
5139 struct inode inode[NINODE];
                                                                                 5189
5140 } icache;
                                                                                 5190
5141
                                                                                 5191
5142 void
                                                                                 5192
5143 iinit(int dev)
                                                                                 5193
5144 {
                                                                                 5194
5145 int i = 0;
                                                                                 5195
5146
                                                                                 5196
5147
      initlock(&icache.lock, "icache");
                                                                                 5197
      for(i = 0; i < NINODE; i++) {
5148
                                                                                 5198
5149
        initsleeplock(&icache.inode[i].lock, "inode");
                                                                                 5199
```

Sheet 51 Sheet 51

```
5200 // Allocate an inode on device dev.
5201 // Mark it as allocated by giving it type type.
5202 // Returns an unlocked but allocated and referenced inode.
5203 struct inode*
5204 ialloc(uint dev, short type)
5205 {
5206 int inum;
5207
      struct buf *bp;
      struct dinode *dip;
5208
5209
5210
      for(inum = 1; inum < sb.ninodes; inum++){</pre>
5211
        bp = bread(dev, IBLOCK(inum, sb));
5212
        dip = (struct dinode*)bp->data + inum%IPB;
5213
        if(dip->type == 0){ // a free inode
5214
          memset(dip, 0, sizeof(*dip));
5215
          dip->type = type;
          log_write(bp); // mark it allocated on the disk
5216
5217
          brelse(bp):
5218
          return iget(dev, inum);
5219
5220
        brelse(bp):
5221 }
5222 panic("ialloc: no inodes");
5223 }
5224
5225 // Copy a modified in-memory inode to disk.
5226 // Must be called after every change to an ip->xxx field
5227 // that lives on disk, since i-node cache is write-through.
5228 // Caller must hold ip->lock.
5229 void
5230 iupdate(struct inode *ip)
5231 {
5232 struct buf *bp;
5233 struct dinode *dip;
5234
5235 bp = bread(ip->dev, IBLOCK(ip->inum, sb));
5236 dip = (struct dinode*)bp->data + ip->inum%IPB;
5237
      dip->type = ip->type;
5238 dip->major = ip->major;
5239 dip->minor = ip->minor;
5240 dip->nlink = ip->nlink;
5241 dip->size = ip->size;
5242 memmove(dip->addrs, ip->addrs, sizeof(ip->addrs));
5243 log_write(bp);
5244 brelse(bp);
5245 }
5246
5247
5248
5249
```

```
5250 // Find the inode with number inum on device dev
5251 // and return the in-memory copy. Does not lock
5252 // the inode and does not read it from disk.
5253 static struct inode*
5254 iget(uint dev, uint inum)
5255 {
5256 struct inode *ip, *empty;
5257
5258
      acquire(&icache.lock);
5259
5260
     // Is the inode already cached?
5261
     emptv = 0:
5262
      for(ip = &icache.inode[0]; ip < &icache.inode[NINODE]; ip++){</pre>
5263
        if(ip->ref > 0 \&\& ip->dev == dev \&\& ip->inum == inum){}
5264
           ip->ref++:
5265
          release(&icache.lock);
5266
          return ip;
5267
5268
        if(empty == 0 && ip->ref == 0)
                                        // Remember empty slot.
5269
           empty = ip;
5270
     }
5271
5272
      // Recycle an inode cache entry.
5273
      if(empty == 0)
5274
        panic("iget: no inodes");
5275
5276
     ip = empty;
5277
      ip->dev = dev;
5278
      ip->inum = inum;
5279 ip->ref = 1;
5280 ip\rightarrow valid = 0;
5281
      release(&icache.lock);
5282
5283 return ip;
5284 }
5285
5286 // Increment reference count for ip.
5287 // Returns ip to enable ip = idup(ip1) idiom.
5288 struct inode*
5289 idup(struct inode *ip)
5290 {
5291 acquire(&icache.lock);
5292 ip->ref++;
5293
      release(&icache.lock);
5294
      return ip:
5295 }
5296
5297
5298
5299
```

5350 // Drop a reference to an in-memory inode.

```
5300 // Lock the given inode.
5301 // Reads the inode from disk if necessary.
5302 void
5303 ilock(struct inode *ip)
5304 {
5305 struct buf *bp;
5306
       struct dinode *dip;
5307
5308
       if(ip == 0 \mid \mid ip \rightarrow ref < 1)
         panic("ilock");
5309
5310
5311
       acquiresleep(&ip->lock);
5312
5313
       if(ip\rightarrow valid == 0){
         bp = bread(ip->dev, IBLOCK(ip->inum, sb));
5314
5315
         dip = (struct dinode*)bp->data + ip->inum%IPB;
5316
         ip->type = dip->type;
5317
         ip->maior = dip->maior:
5318
         ip->minor = dip->minor;
5319
         ip->nlink = dip->nlink;
5320
         ip->size = dip->size:
5321
         memmove(ip->addrs, dip->addrs, sizeof(ip->addrs));
5322
         brelse(bp);
5323
         ip->valid = 1;
5324
         if(ip->type == 0)
5325
           panic("ilock: no type");
5326 }
5327 }
5328
5329 // Unlock the given inode.
5330 void
5331 iunlock(struct inode *ip)
5332 {
if (ip == 0 \mid | !holdingsleep(\&ip \rightarrow lock) \mid | ip \rightarrow ref < 1)
5334
         panic("iunlock");
5335
5336
       releasesleep(&ip->lock);
5337 }
5338
5339
5340
5341
5342
5343
5344
5345
5346
5347
5348
5349
```

```
5351 // If that was the last reference, the inode cache entry can
5352 // be recycled.
5353 // If that was the last reference and the inode has no links
5354 // to it, free the inode (and its content) on disk.
5355 // All calls to iput() must be inside a transaction in
5356 // case it has to free the inode.
5357 void
5358 iput(struct inode *ip)
5359 {
5360 acquiresleep(&ip->lock);
5361 if(ip->valid && ip->nlink == 0){
5362
        acquire(&icache.lock);
5363
        int r = ip->ref;
5364
        release(&icache.lock):
5365
        if(r == 1)
5366
          // inode has no links and no other references: truncate and free.
5367
          itrunc(ip):
5368
          ip->type = 0;
5369
          iupdate(ip);
5370
           ip->valid = 0:
5371
5372 }
5373
      releasesleep(&ip->lock);
5374
5375
      acquire(&icache.lock);
5376
      ip->ref--;
      release(&icache.lock);
5377
5378 }
5379
5380 // Common idiom: unlock, then put.
5381 void
5382 iunlockput(struct inode *ip)
5383 {
5384 iunlock(ip);
5385 iput(ip);
5386 }
5387
5388
5389
5390
5391
5392
5393
5394
5395
5396
5397
5398
5399
```

```
5400 // Inode content
                                                                                  5450 // Truncate inode (discard contents).
5401 //
                                                                                  5451 // Only called when the inode has no links
5402 // The content (data) associated with each inode is stored
                                                                                  5452 // to it (no directory entries referring to it)
5403 // in blocks on the disk. The first NDIRECT block numbers
                                                                                  5453 // and has no in-memory reference to it (is
5404 // are listed in ip->addrs[]. The next NINDIRECT blocks are
                                                                                  5454 // not an open file or current directory).
5405 // listed in block ip->addrs[NDIRECT].
                                                                                  5455 static void
5406
                                                                                  5456 itrunc(struct inode *ip)
5407 // Return the disk block address of the nth block in inode ip.
                                                                                  5457 {
5408 // If there is no such block, bmap allocates one.
                                                                                  5458 int i, j;
5409 static uint
                                                                                  5459
                                                                                         struct buf *bp;
5410 bmap(struct inode *ip, uint bn)
                                                                                  5460
                                                                                         uint *a;
5411 {
                                                                                  5461
5412 uint addr, *a;
                                                                                  5462
                                                                                         for(i = 0; i < NDIRECT; i++){
5413
      struct buf *bp;
                                                                                  5463
                                                                                           if(ip->addrs[i]){
5414
                                                                                  5464
                                                                                             bfree(ip->dev, ip->addrs[i]);
5415
      if(bn < NDIRECT){</pre>
                                                                                  5465
                                                                                             ip->addrs[i] = 0;
5416
        if((addr = ip->addrs[bn]) == 0)
                                                                                  5466
                                                                                           }
5417
          ip->addrs[bn] = addr = balloc(ip->dev);
                                                                                  5467
                                                                                        }
5418
                                                                                  5468
        return addr:
5419 }
                                                                                  5469
                                                                                         if(ip->addrs[NDIRECT]){
5420 bn -= NDIRECT:
                                                                                  5470
                                                                                           bp = bread(ip->dev, ip->addrs[NDIRECT]);
5421
                                                                                  5471
                                                                                           a = (uint*)bp->data;
5422
      if(bn < NINDIRECT){</pre>
                                                                                  5472
                                                                                           for(j = 0; j < NINDIRECT; j++){
5423
        // Load indirect block, allocating if necessary.
                                                                                  5473
                                                                                             if(a[j])
5424
                                                                                  5474
                                                                                               bfree(ip->dev, a[j]);
        if((addr = ip->addrs[NDIRECT]) == 0)
5425
           ip->addrs[NDIRECT] = addr = balloc(ip->dev);
                                                                                  5475
5426
        bp = bread(ip->dev, addr);
                                                                                  5476
                                                                                           brelse(bp);
5427
        a = (uint*)bp->data;
                                                                                           bfree(ip->dev, ip->addrs[NDIRECT]);
                                                                                  5477
5428
        if((addr = a[bn]) == 0){
                                                                                  5478
                                                                                           ip->addrs[NDIRECT] = 0;
5429
          a[bn] = addr = balloc(ip->dev);
                                                                                  5479
                                                                                        }
5430
          log_write(bp);
                                                                                  5480
5431
                                                                                  5481 ip->size = 0;
5432
        brelse(bp);
                                                                                  5482 iupdate(ip);
5433
        return addr;
                                                                                  5483 }
5434 }
                                                                                  5484
5435
                                                                                  5485 // Copy stat information from inode.
5436
      panic("bmap: out of range");
                                                                                  5486 // Caller must hold ip->lock.
5437 }
                                                                                  5487 void
5438
                                                                                  5488 stati(struct inode *ip, struct stat *st)
5439
                                                                                  5489 {
5440
                                                                                  5490 	ext{ st->dev} = ip->dev;
5441
                                                                                  5491 st->ino = ip->inum;
5442
                                                                                  5492 st->type = ip->type;
5443
                                                                                  5493 st->nlink = ip->nlink;
                                                                                  5494 st->size = ip->size;
5444
5445
                                                                                  5495 }
5446
                                                                                  5496
5447
                                                                                  5497
5448
                                                                                  5498
5449
                                                                                  5499
```

```
5500 // Read data from inode.
                                                                                  5550 // Write data to inode.
5501 // Caller must hold ip->lock.
                                                                                  5551 // Caller must hold ip->lock.
5502 int
                                                                                  5552 int
5503 readi(struct inode *ip, char *dst, uint off, uint n)
                                                                                  5553 writei(struct inode *ip, char *src, uint off, uint n)
5504 {
                                                                                  5554 {
5505 uint tot, m;
                                                                                  5555 uint tot, m;
5506
      struct buf *bp;
                                                                                  5556
                                                                                         struct buf *bp;
5507
                                                                                  5557
5508
      if(ip->type == T_DEV){
                                                                                  5558
                                                                                         if(ip->type == T_DEV){
5509
                                                                                  5559
        if(ip->major < 0 || ip->major >= NDEV || !devsw[ip->major].read)
                                                                                           if(ip->major < 0 || ip->major >= NDEV || !devsw[ip->major].write)
5510
           return -1;
                                                                                  5560
                                                                                             return -1;
5511
        return devsw[ip->major].read(ip, dst, n);
                                                                                  5561
                                                                                           return devsw[ip->major].write(ip, src, n);
5512 }
                                                                                  5562 }
5513
                                                                                  5563
5514 if(off > ip->size || off + n < off)
                                                                                  5564
                                                                                        if(off > ip->size || off + n < off)
5515
        return -1;
                                                                                  5565
                                                                                           return -1;
5516
      if(off + n > ip->size)
                                                                                  5566
                                                                                         if(off + n > MAXFILE*BSIZE)
5517
        n = ip->size - off;
                                                                                  5567
                                                                                           return -1:
5518
                                                                                  5568
5519
       for(tot=0; tot<n; tot+=m, off+=m, dst+=m){</pre>
                                                                                  5569
                                                                                         for(tot=0; tot<n; tot+=m, off+=m, src+=m){</pre>
5520
        bp = bread(ip->dev, bmap(ip, off/BSIZE));
                                                                                  5570
                                                                                           bp = bread(ip->dev, bmap(ip, off/BSIZE));
5521
        m = min(n - tot, BSIZE - off%BSIZE);
                                                                                  5571
                                                                                           m = min(n - tot, BSIZE - off%BSIZE);
5522
        memmove(dst, bp->data + off%BSIZE, m);
                                                                                  5572
                                                                                           memmove(bp->data + off%BSIZE, src, m);
5523
        brelse(bp);
                                                                                  5573
                                                                                           log_write(bp);
5524 }
                                                                                  5574
                                                                                           brelse(bp);
5525
                                                                                  5575 }
      return n;
5526 }
                                                                                  5576
5527
                                                                                  5577
                                                                                         if(n > 0 \& off > ip->size)
5528
                                                                                  5578
                                                                                           ip->size = off;
5529
                                                                                  5579
                                                                                           iupdate(ip);
5530
                                                                                  5580 }
5531
                                                                                  5581 return n;
5532
                                                                                  5582 }
5533
                                                                                  5583
5534
                                                                                  5584
5535
                                                                                  5585
5536
                                                                                  5586
5537
                                                                                  5587
5538
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5539
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5546
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5547
                                                                                  5597
5548
                                                                                  5598
5549
                                                                                  5599
```

Sheet 55 Sheet 55

```
5600 // Directories
5601
5602 int
5603 namecmp(const char *s, const char *t)
5604 {
5605 return strncmp(s, t, DIRSIZ);
5606 }
5607
5608 // Look for a directory entry in a directory.
5609 // If found, set *poff to byte offset of entry.
5610 struct inode*
5611 dirlookup(struct inode *dp, char *name, uint *poff)
5612 {
5613 uint off, inum;
5614
      struct dirent de;
5615
5616
      if(dp->type != T_DIR)
5617
        panic("dirlookup not DIR");
5618
       for(off = 0; off < dp->size; off += sizeof(de)){
5619
5620
        if(readi(dp. (char*)&de. off. sizeof(de)) != sizeof(de))
5621
           panic("dirlookup read");
5622
        if(de.inum == 0)
5623
          continue;
5624
        if(namecmp(name, de.name) == 0){}
5625
          // entry matches path element
5626
          if(poff)
5627
             *poff = off;
5628
          inum = de.inum;
5629
           return iget(dp->dev, inum);
5630
        }
5631 }
5632
5633 return 0;
5634 }
5635
5636
5637
5638
5639
5640
5641
5642
5643
5644
5645
5646
5647
5648
5649
```

```
5650 // Write a new directory entry (name, inum) into the directory dp.
5651 int
5652 dirlink(struct inode *dp, char *name, uint inum)
5653 {
5654 int off;
5655 struct dirent de:
5656
      struct inode *ip;
5657
5658
     // Check that name is not present.
5659
      if((ip = dirlookup(dp, name, 0)) != 0){
5660
        iput(ip);
5661
        return -1:
5662 }
5663
5664
     // Look for an empty dirent.
5665
      for(off = 0; off < dp->size; off += sizeof(de)){
5666
        if(readi(dp, (char*)&de, off, sizeof(de)) != sizeof(de))
5667
           panic("dirlink read");
5668
        if(de.inum == 0)
5669
          break;
5670 }
5671
5672
      strncpy(de.name, name, DIRSIZ);
5673
      de.inum = inum;
      if(writei(dp, (char*)&de, off, sizeof(de)) != sizeof(de))
5674
5675
        panic("dirlink");
5676
5677
      return 0;
5678 }
5679
5680
5681
5682
5683
5684
5685
5686
5687
5688
5689
5690
5691
5692
5693
5694
5695
5696
5697
5698
5699
```

```
5700 // Paths
                                                                                  5750 // Look up and return the inode for a path name.
5701
                                                                                  5751 // If parent != 0, return the inode for the parent and copy the final
5702 // Copy the next path element from path into name.
                                                                                  5752 // path element into name, which must have room for DIRSIZ bytes.
5703 // Return a pointer to the element following the copied one.
                                                                                  5753 // Must be called inside a transaction since it calls iput().
5704 // The returned path has no leading slashes,
                                                                                  5754 static struct inode*
5705 // so the caller can check *path=='\0' to see if the name is the last one.
                                                                                 5755 namex(char *path, int nameiparent, char *name)
5706 // If no name to remove, return 0.
                                                                                  5756 {
5707 //
                                                                                  5757
                                                                                        struct inode *ip, *next;
5708 // Examples:
                                                                                  5758
5709 // skipelem("a/bb/c", name) = "bb/c", setting name = "a"
                                                                                  5759
                                                                                        if(*path == '/')
5710 //
         skipelem("///a//bb", name) = "bb", setting name = "a"
                                                                                  5760
                                                                                          ip = iget(ROOTDEV, ROOTINO);
5711 // skipelem("a", name) = "", setting name = "a"
                                                                                  5761
5712 // \text{skipelem("", name)} = \text{skipelem("///", name)} = 0
                                                                                  5762
                                                                                          ip = idup(myproc()->cwd);
5713 //
                                                                                  5763
5714 static char*
                                                                                  5764
                                                                                        while((path = skipelem(path, name)) != 0){
5715 skipelem(char *path, char *name)
                                                                                  5765
                                                                                          ilock(ip):
5716 {
                                                                                  5766
                                                                                          if(ip->type != T_DIR){
5717
      char *s:
                                                                                  5767
                                                                                            iunlockput(ip):
5718 int len;
                                                                                  5768
                                                                                            return 0;
5719
                                                                                  5769
5720
      while(*path == '/')
                                                                                  5770
                                                                                          if(nameiparent && *path == '\0'){
5721
        path++:
                                                                                  5771
                                                                                            // Stop one level early.
5722 if(*path == 0)
                                                                                  5772
                                                                                            iunlock(ip);
5723
        return 0;
                                                                                  5773
                                                                                             return ip;
                                                                                  5774
5724 s = path:
5725
      while(*path != '/' && *path != 0)
                                                                                  5775
                                                                                          if((next = dirlookup(ip, name, 0)) == 0){
5726
        path++;
                                                                                  5776
                                                                                            iunlockput(ip);
                                                                                  5777
5727
      len = path - s;
                                                                                             return 0;
5728
      if(len >= DIRSIZ)
                                                                                  5778
5729
        memmove(name, s, DIRSIZ);
                                                                                  5779
                                                                                          iunlockput(ip);
5730
                                                                                  5780
      else {
                                                                                          ip = next;
5731
        memmove(name, s, len);
                                                                                  5781 }
5732
        name[len] = 0;
                                                                                  5782 if(nameiparent){
5733 }
                                                                                  5783
                                                                                          iput(ip);
5734 while(*path == '/')
                                                                                  5784
                                                                                          return 0;
                                                                                  5785 }
5735
        path++;
5736
      return path;
                                                                                  5786 return ip;
5737 }
                                                                                  5787 }
5738
                                                                                  5788
                                                                                  5789 struct inode*
5739
5740
                                                                                  5790 namei(char *path)
5741
                                                                                  5791 {
5742
                                                                                  5792 char name[DIRSIZ];
5743
                                                                                  5793
                                                                                         return namex(path, 0, name);
5744
                                                                                  5794 }
                                                                                  5795
5745
5746
                                                                                  5796
5747
                                                                                  5797
5748
                                                                                  5798
5749
                                                                                  5799
```

```
5800 struct inode*
5801 nameiparent(char *path, char *name)
5802 {
5803 return namex(path, 1, name);
5804 }
5805
5806
5807
5808
5809
5810
5811
5812
5813
5814
5815
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```

```
5850 //
5851 // File descriptors
5852 //
5853
5854 #include "types.h"
5855 #include "defs.h"
5856 #include "param.h"
5857 #include "fs.h"
5858 #include "spinlock.h"
5859 #include "sleeplock.h"
5860 #include "file.h"
5861
5862 struct devsw devsw[NDEV];
5863 struct {
5864 struct spinlock lock;
5865 struct file file[NFILE];
5866 } ftable;
5867
5868 void
5869 fileinit(void)
5870 {
5871 initlock(&ftable.lock, "ftable");
5872 }
5873
5874 // Allocate a file structure.
5875 struct file*
5876 filealloc(void)
5877 {
5878 struct file *f;
5879
5880 acquire(&ftable.lock);
5881 for(f = ftable.file; f < ftable.file + NFILE; f++){
5882
        if(f->ref == 0){
5883
          f \rightarrow ref = 1;
5884
          release(&ftable.lock);
5885
          return f;
5886
        }
5887 }
5888 release(&ftable.lock);
5889
      return 0;
5890 }
5891
5892
5893
5894
5895
5896
5897
5898
5899
```

```
5950 // Get metadata about file f.
5951 int
5952 filestat(struct file *f, struct stat *st)
5953 {
5954 if(f->type == FD_INODE){
5955
        ilock(f->ip);
5956
         stati(f->ip, st);
5957
         iunlock(f->ip);
5958
         return 0;
5959 }
5960 return -1;
5961 }
5962
5963 // Read from file f.
5964 int
5965 fileread(struct file *f, char *addr, int n)
5966 {
5967 int r;
5968
5969
     if(f\rightarrow readable == 0)
5970
         return -1:
if(f\rightarrow type == FD\_PIPE)
5972
         return piperead(f->pipe, addr, n);
5973 if(f->type == FD_INODE){
5974
        ilock(f->ip);
5975
         if((r = readi(f->ip, addr, f->off, n)) > 0)
5976
          f \rightarrow off += r;
5977
         iunlock(f->ip);
5978
         return r;
5979 }
5980
      panic("fileread");
5981 }
5982
5983
5984
5985
5986
5987
5988
5989
5990
5991
5992
5993
5994
5995
5996
5997
5998
5999
```

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5944

5945

5946

5947

5948

5949

5936 }

5937 }

begin_op();

end_op();

iput(ff.ip);

```
6000 // Write to file f.
6001 int
6002 filewrite(struct file *f, char *addr, int n)
6003 {
6004 int r;
6005
6006 if(f->writable == 0)
6007
        return -1;
6008
      if(f->type == FD_PIPE)
        return pipewrite(f->pipe, addr, n);
6009
6010
       if(f->type == FD_INODE){
6011
        // write a few blocks at a time to avoid exceeding
6012
        // the maximum log transaction size, including
6013
        // i-node, indirect block, allocation blocks,
6014
        // and 2 blocks of slop for non-aligned writes.
6015
         // this really belongs lower down, since writei()
6016
         // might be writing a device like the console.
6017
         int max = ((LOGSIZE-1-1-2) / 2) * 512;
6018
         int i = 0:
6019
         while(i < n){
6020
          int n1 = n - i:
6021
           if(n1 > max)
6022
            n1 = max;
6023
6024
           begin_op();
6025
           ilock(f->ip);
6026
           if ((r = writei(f \rightarrow ip, addr + i, f \rightarrow off, n1)) > 0)
6027
             f \rightarrow off += r;
6028
           iunlock(f->ip);
6029
           end_op();
6030
6031
           if(r < 0)
6032
            break;
6033
           if(r != n1)
6034
             panic("short filewrite");
6035
           i += r;
6036
        }
6037
         return i == n ? n : -1;
6038 }
6039
       panic("filewrite");
6040 }
6041
6042
6043
6044
6045
6046
6047
6048
6049
```

```
6050 //
6051 // File-system system calls.
6052 // Mostly argument checking, since we don't trust
6053 // user code, and calls into file.c and fs.c.
6054 //
6055
6056 #include "types.h"
6057 #include "defs.h"
6058 #include "param.h"
6059 #include "stat.h"
6060 #include "mmu.h"
6061 #include "proc.h"
6062 #include "fs.h"
6063 #include "spinlock.h"
6064 #include "sleeplock.h"
6065 #include "file.h"
6066 #include "fcntl.h"
6067
6068 // Fetch the nth word-sized system call argument as a file descriptor
6069 // and return both the descriptor and the corresponding struct file.
6070 static int
6071 argfd(int n, int *pfd, struct file **pf)
6072 {
6073 int fd;
      struct file *f:
6074
6075
6076
     if(argint(n, &fd) < 0)
6077
        return -1;
6078
      if(fd < 0 \mid | fd >= NOFILE \mid | (f=myproc()->ofile[fd]) == 0)
6079
        return -1:
6080 if(pfd)
6081
        *pfd = fd;
6082 if(pf)
6083
        *pf = f;
6084 return 0;
6085 }
6086
6087
6088
6089
6090
6091
6092
6093
6094
6095
6096
6097
6098
6099
```

```
6100 // Allocate a file descriptor for the given file.
                                                                                6150 int
6101 // Takes over file reference from caller on success.
                                                                                6151 sys_write(void)
6102 static int
                                                                                6152 {
6103 fdalloc(struct file *f)
                                                                                6153 struct file *f;
6104 {
                                                                                6154 int n;
6105 int fd;
                                                                                6155 char *p;
6106
      struct proc *curproc = myproc();
                                                                                6156
6107
                                                                                6157 if (argfd(0, 0, &f) < 0 \mid | argint(2, &n) < 0 \mid | argptr(1, &p, n) < 0)
6108
      for(fd = 0; fd < NOFILE; fd++){</pre>
                                                                                6158
                                                                                        return -1;
       if(curproc->ofile[fd] == 0){
                                                                                6159 return filewrite(f, p, n);
6109
6110
          curproc->ofile[fd] = f;
                                                                                6160 }
6111
                                                                                6161
          return fd;
6112
       }
                                                                                6162 int
6113 }
                                                                                6163 sys_close(void)
6114 return -1;
                                                                                6164 {
6115 }
                                                                                6165 int fd;
6116
                                                                                6166 struct file *f;
6117 int
                                                                                6167
6118 sys_dup(void)
                                                                                6168 if(argfd(0, &fd, &f) < 0)
6119 {
                                                                                6169
                                                                                       return -1;
6120 struct file *f;
                                                                                6170 myproc()->ofile[fd] = 0;
6121 int fd;
                                                                                6171 fileclose(f);
6122
                                                                                6172 return 0;
6123 if(argfd(0, 0, &f) < 0)
                                                                                6173 }
6124
      return -1:
                                                                                6174
6125 if((fd=fdalloc(f)) < 0)
                                                                                6175 int
6126
       return -1;
                                                                                6176 sys_fstat(void)
6127 filedup(f);
                                                                                6177 {
6128 return fd;
                                                                                6178 struct file *f;
6129 }
                                                                                6179 struct stat *st;
6130
                                                                                6180
6131 int
                                                                                6181 if(argfd(0, 0, &f) < 0 || argptr(1, (void*)&st, sizeof(*st)) < 0)
6132 sys_read(void)
                                                                                6182
                                                                                         return -1:
                                                                                       return filestat(f, st);
6133 {
                                                                                6183
6134 struct file *f;
                                                                                6184 }
6135 int n;
                                                                                6185
6136 char *p;
                                                                                6186
6137
                                                                                6187
6138 if (argfd(0, 0, \&f) < 0 \mid | argint(2, \&n) < 0 \mid | argptr(1, \&p, n) < 0)
                                                                                6188
6139
        return -1;
                                                                                6189
6140 return fileread(f, p, n);
                                                                                6190
6141 }
                                                                                6191
6142
                                                                                6192
6143
                                                                                6193
6144
                                                                                6194
6145
                                                                                6195
6146
                                                                                6196
6147
                                                                                6197
6148
                                                                                6198
6149
                                                                                6199
```

```
6250 // Is the directory dp empty except for "." and ".." ?
6200 // Create the path new as a link to the same inode as old.
6201 int
                                                                                6251 static int
6202 sys_link(void)
                                                                                6252 isdirempty(struct inode *dp)
6203 {
                                                                               6253 {
6204
      char name[DIRSIZ], *new, *old;
                                                                                6254 int off;
6205
      struct inode *dp, *ip;
                                                                                6255 struct dirent de:
6206
                                                                               6256
6207
      if(argstr(0, \&old) < 0 \mid | argstr(1, \&new) < 0)
                                                                               6257
                                                                                      for(off=2*sizeof(de); off<dp->size; off+=sizeof(de)){
6208
        return -1;
                                                                               6258
                                                                                        if(readi(dp, (char*)&de, off, sizeof(de)) != sizeof(de))
6209
                                                                               6259
                                                                                          panic("isdirempty: readi");
6210
      begin_op();
                                                                                6260
                                                                                        if(de.inum != 0)
6211 if((ip = namei(old)) == 0){
                                                                               6261
                                                                                          return 0;
6212
                                                                               6262 }
        end_op();
                                                                                6263 return 1;
6213
        return -1;
6214 }
                                                                               6264 }
6215
                                                                               6265
6216 ilock(ip);
                                                                               6266
6217
      if(ip->type == T_DIR){
                                                                               6267
6218
        iunlockput(ip);
                                                                               6268
6219
        end_op();
                                                                                6269
6220
        return -1;
                                                                               6270
6221 }
                                                                               6271
6222
                                                                               6272
6223
      ip->nlink++;
                                                                               6273
6224 iupdate(ip);
                                                                               6274
6225
      iunlock(ip);
                                                                                6275
6226
                                                                               6276
6227 if((dp = nameiparent(new, name)) == 0)
                                                                                6277
6228
       goto bad;
                                                                                6278
6229 ilock(dp);
                                                                               6279
6230 if(dp->dev != ip->dev || dirlink(dp, name, ip->inum) < 0){
                                                                                6280
6231
        iunlockput(dp);
                                                                                6281
6232
        goto bad;
                                                                               6282
6233 }
                                                                                6283
6234 iunlockput(dp);
                                                                                6284
6235
                                                                               6285
      iput(ip);
6236
                                                                                6286
6237
      end_op();
                                                                                6287
6238
                                                                               6288
6239 return 0;
                                                                                6289
6240
                                                                                6290
6241 bad:
                                                                               6291
6242 ilock(ip);
                                                                                6292
6243 ip->nlink--;
                                                                                6293
6244 iupdate(ip);
                                                                               6294
6245 iunlockput(ip);
                                                                                6295
6246
      end_op();
                                                                                6296
6247
      return -1;
                                                                               6297
6248 }
                                                                                6298
6249
                                                                                6299
```

```
Aug 29 15:52 2017 xv6/sysfile.c Page 6
                                                                                 Aug 29 15:52 2017 xv6/sysfile.c Page 7
6300 int
                                                                                 6350 bad:
6301 sys_unlink(void)
                                                                                 6351 iunlockput(dp);
6302 {
                                                                                 6352 end_op();
6303 struct inode *ip, *dp;
                                                                                 6353 return -1;
6304
      struct dirent de;
                                                                                 6354 }
6305
      char name[DIRSIZ], *path;
                                                                                 6355
6306
      uint off;
                                                                                 6356 static struct inode*
6307
                                                                                 6357 create(char *path, short type, short major, short minor)
6308
      if(argstr(0, &path) < 0)
                                                                                 6358 {
        return -1;
                                                                                 6359
6309
                                                                                        uint off;
6310
                                                                                 6360
                                                                                        struct inode *ip, *dp;
                                                                                       char name[DIRSIZ];
6311
      begin_op();
                                                                                 6361
6312 if((dp = nameiparent(path, name)) == 0){
                                                                                 6362
6313
        end_op();
                                                                                 6363 if((dp = nameiparent(path, name)) == 0)
6314
        return -1;
                                                                                 6364
                                                                                          return 0:
6315 }
                                                                                 6365
                                                                                       ilock(dp);
6316
                                                                                 6366
6317 ilock(dp);
                                                                                 6367
                                                                                       if((ip = dirlookup(dp, name, &off)) != 0){
6318
                                                                                 6368
                                                                                          iunlockput(dp);
6319
      // Cannot unlink "." or "..".
                                                                                 6369
                                                                                          ilock(ip);
6320
      if(namecmp(name, ".") == 0 \mid\mid namecmp(name, "..") == 0)
                                                                                 6370
                                                                                          if(type == T_FILE && ip->type == T_FILE)
6321
        goto bad:
                                                                                 6371
                                                                                            return ip:
6322
                                                                                 6372
                                                                                          iunlockput(ip);
6323
      if((ip = dirlookup(dp, name, &off)) == 0)
                                                                                 6373
                                                                                          return 0;
                                                                                 6374 }
6324
        goto bad:
6325 ilock(ip);
                                                                                 6375
6326
                                                                                 6376
                                                                                       if((ip = ialloc(dp->dev, type)) == 0)
                                                                                          panic("create: ialloc");
6327 if(ip->nlink < 1)
                                                                                 6377
6328
       panic("unlink: nlink < 1");</pre>
                                                                                 6378
6329 if(ip->type == T_DIR && !isdirempty(ip)){
                                                                                 6379 ilock(ip);
6330
        iunlockput(ip);
                                                                                 6380
                                                                                       ip->major = major;
6331
        goto bad;
                                                                                 6381 ip->minor = minor;
6332 }
                                                                                 6382 ip\rightarrow nlink = 1;
6333
                                                                                 6383
                                                                                        iupdate(ip);
6334
      memset(&de, 0, sizeof(de));
                                                                                 6384
      if(writei(dp, (char*)&de, off, sizeof(de)) != sizeof(de))
6335
                                                                                 6385
                                                                                       if(type == T_DIR){ // Create . and .. entries.
6336
        panic("unlink: writei");
                                                                                 6386
                                                                                          dp->nlink++; // for ".."
6337 if(ip\rightarrow type == T\_DIR){
                                                                                 6387
                                                                                          iupdate(dp);
6338
        dp->nlink--;
                                                                                 6388
                                                                                          // No ip->nlink++ for ".": avoid cyclic ref count.
6339
        iupdate(dp);
                                                                                 6389
                                                                                          if(dirlink(ip, ".", ip->inum) < 0 || dirlink(ip, "..", dp->inum) < 0)</pre>
6340 }
                                                                                 6390
                                                                                             panic("create dots");
6341 iunlockput(dp);
                                                                                 6391 }
6342
                                                                                 6392
6343
      ip->nlink--;
                                                                                 6393
                                                                                       if(dirlink(dp, name, ip->inum) < 0)</pre>
      iupdate(ip);
6344
                                                                                 6394
                                                                                          panic("create: dirlink");
6345
      iunlockput(ip);
                                                                                 6395
6346
                                                                                 6396
                                                                                       iunlockput(dp);
6347
      end_op();
                                                                                 6397
```

return 0;

6348

6349

6399 }

6398 return ip;

```
6400 int
                                                                                  6450 int
6401 sys_open(void)
                                                                                  6451 sys_mkdir(void)
6402 {
                                                                                  6452 {
      char *path;
6403
                                                                                  6453 char *path;
6404
      int fd, omode;
                                                                                  6454 struct inode *ip;
6405
      struct file *f;
                                                                                  6455
6406
      struct inode *ip;
                                                                                  6456
                                                                                         begin_op();
6407
                                                                                  6457
                                                                                        if(argstr(0, \&path) < 0 \mid | (ip = create(path, T_DIR, 0, 0)) == 0){
6408
      if(argstr(0, \&path) < 0 \mid | argint(1, \&omode) < 0)
                                                                                  6458
                                                                                           end_op();
        return -1;
6409
                                                                                  6459
                                                                                           return -1;
6410
                                                                                  6460 }
6411
      begin_op();
                                                                                  6461 iunlockput(ip);
6412
                                                                                  6462 end_op();
6413
      if(omode & O_CREATE){
                                                                                  6463 return 0;
6414
                                                                                  6464 }
        ip = create(path, T_FILE, 0, 0);
6415
        if(ip == 0){
                                                                                  6465
6416
          end_op();
                                                                                  6466 int
6417
          return -1:
                                                                                  6467 sys_mknod(void)
6418
                                                                                  6468 {
                                                                                  6469 struct inode *ip;
6419 } else {
6420
       if((ip = namei(path)) == 0){
                                                                                  6470
                                                                                        char *path:
6421
          end_op();
                                                                                  6471
                                                                                        int major, minor;
6422
          return -1;
                                                                                  6472
6423
        }
                                                                                  6473
                                                                                         begin_op();
6424
                                                                                  6474 if((argstr(0, &path)) < 0 ||
        ilock(ip);
6425
        if(ip->type == T_DIR && omode != O_RDONLY){
                                                                                  6475
                                                                                            argint(1, &major) < 0 ||
6426
          iunlockput(ip);
                                                                                  6476
                                                                                            argint(2, \&minor) < 0 \mid \mid
6427
                                                                                  6477
                                                                                            (ip = create(path, T_DEV, major, minor)) == 0){
          end_op();
6428
          return -1;
                                                                                  6478
                                                                                           end_op();
6429
                                                                                  6479
                                                                                           return -1;
        }
6430 }
                                                                                  6480 }
6431
                                                                                  6481 iunlockput(ip);
6432
      if((f = filealloc()) == 0 \mid | (fd = fdalloc(f)) < 0){
                                                                                         end_op();
                                                                                  6482
6433
        if(f)
                                                                                  6483
                                                                                         return 0;
6434
          fileclose(f);
                                                                                  6484 }
6435
                                                                                  6485
        iunlockput(ip);
6436
        end_op();
                                                                                  6486
6437
        return -1;
                                                                                  6487
6438 }
                                                                                  6488
6439
      iunlock(ip);
                                                                                  6489
6440
      end_op();
                                                                                  6490
6441
                                                                                  6491
6442 f->type = FD_INODE;
                                                                                  6492
6443 f \rightarrow ip = ip;
                                                                                  6493
6444 f \rightarrow off = 0;
                                                                                  6494
6445 f->readable = !(omode & O_WRONLY);
                                                                                  6495
6446 f->writable = (omode & O_WRONLY) || (omode & O_RDWR);
                                                                                  6496
6447
      return fd;
                                                                                  6497
6448 }
                                                                                  6498
6449
                                                                                  6499
```

```
6500 int
                                                                               6550 int
6501 sys_chdir(void)
                                                                               6551 sys_pipe(void)
6502 {
                                                                               6552 {
6503 char *path;
                                                                               6553 int *fd;
6504 struct inode *ip;
                                                                               6554 struct file *rf, *wf;
6505 struct proc *curproc = myproc();
                                                                               6555 int fd0, fd1;
6506
                                                                               6556
6507
      begin_op();
                                                                               6557 if(argptr(0, (void*)&fd, 2*sizeof(fd[0])) < 0)
6508 if(argstr(0, &path) < 0 || (ip = namei(path)) == 0){
                                                                               6558
                                                                                        return -1;
6509
                                                                               6559 if(pipealloc(&rf, &wf) < 0)
        end_op();
6510
       return -1;
                                                                               6560
                                                                                        return -1;
6511 }
                                                                               6561 fd0 = -1:
6512 ilock(ip);
                                                                               6562 if((fd0 = fdalloc(rf)) < 0 || (fd1 = fdalloc(wf)) < 0){
6513 if(ip->type != T_DIR){
                                                                               6563
                                                                                       if(fd0 >= 0)
6514
        iunlockput(ip);
                                                                                          myproc()->ofile[fd0] = 0;
                                                                               6564
6515
        end_op();
                                                                               6565
                                                                                        fileclose(rf);
6516
        return -1;
                                                                               6566
                                                                                        fileclose(wf);
6517 }
                                                                               6567
                                                                                        return -1:
6518 iunlock(ip);
                                                                               6568 }
6519 iput(curproc->cwd);
                                                                               6569 fd[0] = fd0;
                                                                               6570 fd[1] = fd1;
6520 end op():
6521 curproc->cwd = ip;
                                                                               6571 return 0;
6522 return 0;
                                                                               6572 }
6523 }
                                                                               6573
6524
                                                                               6574
6525 int
                                                                               6575
6526 sys_exec(void)
                                                                               6576
6527 {
                                                                               6577
6528 char *path, *argv[MAXARG];
                                                                               6578
                                                                               6579
6529 int i;
6530 uint uargv, uarg;
                                                                               6580
6531
                                                                               6581
6532 if(argstr(0, &path) < 0 || argint(1, (int*)&uargv) < 0){
                                                                               6582
6533
        return -1;
                                                                               6583
6534 }
                                                                               6584
6535 memset(argv, 0, sizeof(argv));
                                                                               6585
6536 for(i=0;; i++){
                                                                               6586
6537
       if(i >= NELEM(argv))
                                                                               6587
6538
          return -1;
                                                                               6588
6539
        if(fetchint(uargv+4*i, (int*)&uarg) < 0)</pre>
                                                                               6589
6540
          return -1;
                                                                               6590
6541
        if(uarg == 0){
                                                                               6591
6542
          argv[i] = 0;
                                                                               6592
6543
          break;
                                                                               6593
6544
                                                                               6594
6545
        if(fetchstr(uarg, &argv[i]) < 0)</pre>
                                                                               6595
6546
          return -1;
                                                                               6596
6547 }
                                                                               6597
6548 return exec(path, argv);
                                                                               6598
                                                                               6599
6549 }
```

```
6650
           goto bad:
6651
        if((sz = allocuvm(pgdir, sz, ph.vaddr + ph.memsz)) == 0)
6652
        if(ph.vaddr % PGSIZE != 0)
6653
6654
          goto bad;
6655
        if(loaduvm(pgdir, (char*)ph.vaddr, ip, ph.off, ph.filesz) < 0)
6656
          goto bad;
6657 }
6658 iunlockput(ip);
6659
      end_op();
6660 ip = 0;
6661
6662 // Allocate two pages at the next page boundary.
6663 // Make the first inaccessible. Use the second as the user stack.
6664 sz = PGROUNDUP(sz):
6665 if((sz = allocuvm(pgdir, sz, sz + 2*PGSIZE)) == 0)
6666
        goto bad;
6667
      clearpteu(pgdir, (char*)(sz - 2*PGSIZE));
6668
      sp = sz;
6669
6670
      // Push argument strings, prepare rest of stack in ustack.
6671
      for(argc = 0; argv[argc]; argc++) {
6672
        if(argc >= MAXARG)
6673
          goto bad;
6674
        sp = (sp - (strlen(argv[argc]) + 1)) \& ~3;
6675
        if(copyout(pgdir, sp, argv[argc], strlen(argv[argc]) + 1) < 0)</pre>
6676
          goto bad;
6677
        ustack[3+argc] = sp;
6678 }
      ustack[3+argc] = 0;
6679
6680
6681
      ustack[0] = 0xfffffffff; // fake return PC
6682
      ustack[1] = argc;
6683
      ustack[2] = sp - (argc+1)*4; // argv pointer
6684
6685
      sp = (3+argc+1) * 4;
6686
      if(copyout(pgdir, sp, ustack, (3+argc+1)*4) < 0)</pre>
6687
        goto bad;
6688
6689
      // Save program name for debugging.
6690
      for(last=s=path; *s; s++)
6691
        if(*s == '/')
6692
          last = s+1;
6693
      safestrcpy(curproc->name, last, sizeof(curproc->name));
6694
6695
      // Commit to the user image.
6696
      oldpgdir = curproc->pgdir;
6697
      curproc->pgdir = pgdir;
6698
      curproc->sz = sz;
6699
      curproc->tf->eip = elf.entry; // main
```

6646

6647

6648

6649

continue:

goto bad;

if(ph.memsz < ph.filesz)</pre>

if(ph.vaddr + ph.memsz < ph.vaddr)</pre>

```
6700
      curproc->tf->esp = sp;
6701
      switchuvm(curproc);
      freevm(oldpgdir);
6702
6703
      return 0;
6704
6705 bad:
6706 if(pgdir)
6707
        freevm(pgdir);
6708 if(ip){
6709
        iunlockput(ip);
6710
        end_op();
6711 }
6712 return -1;
6713 }
6714
6715
6716
6717
6718
6719
6720
6721
6722
6723
6724
6725
6726
6727
6728
6729
6730
6731
6732
6733
6734
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6739
6740
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6742
6743
6744
6745
6746
6747
6748
6749
```

```
6750 #include "types.h"
6751 #include "defs.h"
6752 #include "param.h"
6753 #include "mmu.h"
6754 #include "proc.h"
6755 #include "fs.h"
6756 #include "spinlock.h"
6757 #include "sleeplock.h"
6758 #include "file.h"
6759
6760 #define PIPESIZE 512
6761
6762 struct pipe {
6763 struct spinlock lock;
6764 char data[PIPESIZE];
6765 uint nread;
                      // number of bytes read
6766 uint nwrite;
                    // number of bytes written
6767 int readopen; // read fd is still open
6768 int writeopen; // write fd is still open
6769 };
6770
6771 int
6772 pipealloc(struct file **f0, struct file **f1)
6774 struct pipe *p;
6775
6776 p = 0;
6777 *f0 = *f1 = 0;
6778 if((*f0 = filealloc()) == 0 \mid | (*f1 = filealloc()) == 0)
6779
        goto bad;
6780 if((p = (struct pipe*)kalloc()) == 0)
6781
        goto bad;
6782 p \rightarrow readopen = 1;
6783 p->writeopen = 1;
6784 p->nwrite = 0;
6785 p->nread = 0;
6786 initlock(&p->lock, "pipe");
6787 (*f0)->type = FD_PIPE;
6788 (*f0)->readable = 1;
6789 (*f0)->writable = 0;
6790 (*f0)->pipe = p;
6791 (*f1)->type = FD_PIPE;
6792 (*f1)->readable = 0;
6793 (*f1)->writable = 1;
6794 (*f1)->pipe = p;
6795
      return 0;
6796
6797
6798
6799
```

p->data[p->nwrite++ % PIPESIZE] = addr[i];

```
6850 int
6851 piperead(struct pipe *p, char *addr, int n)
6852 {
6853 int i;
6854
6855 acquire(&p->lock);
6856
      while(p->nread == p->nwrite && p->writeopen){
6857
        if(myproc()->killed){
6858
           release(&p->lock);
6859
           return -1;
6860
        }
6861
        sleep(&p->nread, &p->lock);
6862 }
6863
       for(i = 0; i < n; i++){
6864
        if(p->nread == p->nwrite)
6865
           break:
6866
        addr[i] = p->data[p->nread++ % PIPESIZE];
6867
6868
      wakeup(&p->nwrite);
6869
       release(&p->lock);
6870
       return i:
6871 }
6872
6873
6874
6875
6876
6877
6878
6879
6880
6881
6882
6883
6884
6885
6886
6887
6888
6889
6890
6891
6892
6893
6894
6895
6896
6897
6898
6899
```

6844

6846

6847

6848

6849 }

6845 }

wakeup(&p->nread);

release(&p->lock);

return n;

```
6900 #include "types.h"
6901 #include "x86.h"
6902
6903 void*
6904 memset(void *dst, int c, uint n)
6905 {
6906 if ((int)dst\%4 == 0 \&\& n\%4 == 0){
6907
        c \&= 0xFF;
6908
        stosl(dst, (c<<24)|(c<<16)|(c<<8)|c, n/4);
6909 } else
6910
        stosb(dst, c, n);
6911 return dst;
6912 }
6913
6914 int
6915 memcmp(const void *v1, const void *v2, uint n)
6916 {
6917 const uchar *s1, *s2;
6918
6919 s1 = v1;
6920 	 s2 = v2:
6921 while(n-- > 0){
6922
      if(*s1 != *s2)
6923
          return *s1 - *s2;
6924
        s1++, s2++;
6925 }
6926
6927 return 0;
6928 }
6929
6930 void*
6931 memmove(void *dst, const void *src, uint n)
6932 {
6933 const char *s;
6934 char *d;
6935
6936 s = src;
6937 d = dst;
6938 if(s < d \&\& s + n > d){
6939
        s += n;
6940
        d += n;
6941
        while(n-- > 0)
6942
          *--d = *--s;
6943 } else
6944
        while(n-- > 0)
6945
          *d++ = *s++;
6946
6947 return dst;
6948 }
6949
```

```
6950 // memcpy exists to placate GCC. Use memmove.
6951 void*
6952 memcpy(void *dst, const void *src, uint n)
6953 {
6954 return memmove(dst, src, n);
6955 }
6956
6957 int
6958 strncmp(const char *p, const char *q, uint n)
6959 {
6960 while(n > 0 && *p && *p == *q)
6961
        n--, p++, q++;
6962 if(n == 0)
6963
        return 0;
6964 return (uchar)*p - (uchar)*q;
6965 }
6966
6967 char*
6968 strncpy(char *s, const char *t, int n)
6969 {
6970 char *os:
6971
6972 os = s;
6973 while(n-- > 0 \&\& (*s++ = *t++) != 0)
6974
6975 while(n-- > 0)
6976
        *s++ = 0;
6977 return os;
6978 }
6979
6980 // Like strncpy but guaranteed to NUL-terminate.
6981 char*
6982 safestrcpy(char *s, const char *t, int n)
6983 {
6984 char *os;
6985
6986 os = s;
6987 if(n <= 0)
6988
       return os;
6989 while(--n > 0 \&\& (*s++ = *t++) != 0)
6990
6991 *s = 0;
6992 return os;
6993 }
6994
6995
6996
6997
6998
6999
```

```
7000 int
7001 strlen(const char *s)
7002 {
7003 int n;
7004
7005
       for(n = 0; s[n]; n++)
7006
7007
       return n;
7008 }
7009
7010
7011
7012
7013
7014
7015
7016
7017
7018
7019
7020
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7022
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7024
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7040
7041
7042
7043
7044
7045
7046
7047
7048
7049
```

```
7050 // See MultiProcessor Specification Version 1.[14]
7051
7052 struct mp {
                            // floating pointer
                                    // "_MP_"
7053
      uchar signature[4];
                                    // phys addr of MP config table
7054
      void *physaddr;
                                    // 1
7055 uchar length;
                                    // [14]
7056
      uchar specrev;
7057
      uchar checksum;
                                    // all bytes must add up to 0
7058
      uchar type;
                                    // MP system config type
      uchar imcrp;
7059
7060
      uchar reserved[3];
7061 };
7062
7063 struct mpconf {
                            // configuration table header
      uchar signature[4];
                                    // "PCMP"
7065
      ushort length;
                                    // total table length
7066
      uchar version;
                                    // [14]
                                    // all bytes must add up to 0
7067
      uchar checksum:
7068 uchar product[20];
                                    // product id
7069
      uint *oemtable;
                                    // OEM table pointer
7070
      ushort oemlenath:
                                    // OEM table length
7071
      ushort entry:
                                    // entry count
                                    // address of local APIC
7072
      uint *lapicaddr;
7073
      ushort xlength;
                                    // extended table length
7074
                                    // extended table checksum
      uchar xchecksum:
7075
      uchar reserved;
7076 };
7077
7078 struct mpproc {
                            // processor table entry
7079 uchar type;
                                    // entry type (0)
                                    // local APIC id
7080
      uchar apicid;
7081
      uchar version;
                                    // local APIC verison
7082
      uchar flags;
                                    // CPU flags
7083
        #define MPBOOT 0x02
                                      // This proc is the bootstrap processor.
7084
      uchar signature[4];
                                    // CPU signature
7085
      uint feature;
                                    // feature flags from CPUID instruction
7086 uchar reserved[8];
7087 };
7088
7089 struct mpioapic {
                            // I/O APIC table entry
7090
      uchar type;
                                    // entry type (2)
7091 uchar apicno;
                                    // I/O APIC id
7092
      uchar version;
                                    // I/O APIC version
7093
      uchar flags;
                                    // I/O APIC flags
7094 uint *addr:
                                   // I/O APIC address
7095 };
7096
7097
7098
7099
```

7100 // Table entry types	7150 // Blank page.
7101 #define MPPROC 0x00 // One per processor	7151
7102 #define MPBUS 0x01 // One per bus	7152
7103 #define MPIOAPIC 0x02 // One per I/O APIC	7153
7104 #define MPIOINTR 0x03 // One per bus interrupt source	7154
7105 #define MPLINTR 0x04 // One per system interrupt source	7155
7106	7156
7107	7157
7108	7158
7109	7159
7110	7160
7111	7161
7112	7162
7113	7163
7114	7164
7115	7165
7116	7166
7117	7167
7118	7168
7119	7169
7120	7170
7121	7171
7122	7172
7123	7173
7124	7174
7125	7175
7126	7176
7127	7177
7128	7178
7129	7179
7130	7173
7131	
	7181
7132	7182
7133	7183
7134	7184
7135	7185
7136	7186
7137	7187
7138	7188
7139	7189
7140	7190
7141	7191
7142	7192
7143	7193
7144	7194
7145	7195
7146	7196
7147	7197
7148	7198
7149	7199

```
7200 // Multiprocessor support
                                                                                  7250 // Search for the MP Floating Pointer Structure, which according to the
7201 // Search memory for MP description structures.
                                                                                  7251 // spec is in one of the following three locations:
7202 // http://developer.intel.com/design/pentium/datashts/24201606.pdf
                                                                                  7252 // 1) in the first KB of the EBDA;
                                                                                  7253 // 2) in the last KB of system base memory;
7203
7204 #include "types.h"
                                                                                  7254 // 3) in the BIOS ROM between 0xE0000 and 0xFFFFF.
7205 #include "defs.h"
                                                                                  7255 static struct mp*
7206 #include "param.h"
                                                                                  7256 mpsearch(void)
7207 #include "memlayout.h"
                                                                                  7257 {
7208 #include "mp.h"
                                                                                  7258 uchar *bda;
7209 #include "x86.h"
                                                                                  7259
                                                                                         uint p;
7210 #include "mmu.h"
                                                                                  7260
                                                                                         struct mp *mp;
7211 #include "proc.h"
                                                                                  7261
7212
                                                                                  7262
                                                                                         bda = (uchar *) P2V(0x400);
7213 struct cpu cpus[NCPU];
                                                                                  7263
                                                                                        if((p = ((bda[0x0F] << 8) | bda[0x0E]) << 4)){
7214 int ncpu:
                                                                                  7264
                                                                                          if((mp = mpsearch1(p, 1024)))
7215 uchar ioapicid;
                                                                                  7265
                                                                                             return mp;
7216
                                                                                  7266 } else {
7217 static uchar
                                                                                  7267
                                                                                           p = ((bda[0x14] << 8)|bda[0x13])*1024:
7218 sum(uchar *addr, int len)
                                                                                  7268
                                                                                           if((mp = mpsearch1(p-1024, 1024)))
7219 {
                                                                                  7269
                                                                                             return mp;
7220 int i. sum:
                                                                                  7270 }
7221
                                                                                  7271 return mpsearch1(0xF0000, 0x10000);
7222 sum = 0;
                                                                                  7272 }
7223 for(i=0; i<len; i++)
                                                                                  7273
7224
        sum += addr[i]:
                                                                                  7274 // Search for an MP configuration table. For now,
7225 return sum;
                                                                                  7275 // don't accept the default configurations (physaddr == 0).
7226 }
                                                                                  7276 // Check for correct signature, calculate the checksum and,
7227
                                                                                  7277 // if correct, check the version.
7228 // Look for an MP structure in the len bytes at addr.
                                                                                  7278 // To do: check extended table checksum.
7229 static struct mp*
                                                                                  7279 static struct mpconf*
7230 mpsearch1(uint a, int len)
                                                                                  7280 mpconfig(struct mp **pmp)
7231 {
                                                                                  7281 {
7232 uchar *e, *p, *addr;
                                                                                  7282 struct mpconf *conf;
7233
                                                                                  7283 struct mp *mp;
7234 addr = P2V(a);
                                                                                  7284
7235 e = addr+1en:
                                                                                  7285 if((mp = mpsearch()) == 0 \mid \mid mp \rightarrow physaddr == 0)
7236 for(p = addr; p < e; p += sizeof(struct mp))
                                                                                  7286
                                                                                           return 0:
7237
        if(memcmp(p, "\_MP\_", 4) == 0 \&\& sum(p, sizeof(struct mp)) == 0)
                                                                                  7287
                                                                                         conf = (struct mpconf*) P2V((uint) mp->physaddr);
7238
           return (struct mp*)p;
                                                                                  7288
                                                                                        if(memcmp(conf, "PCMP", 4) != 0)
7239 return 0;
                                                                                  7289
                                                                                           return 0;
7240 }
                                                                                  7290 if(conf->version != 1 && conf->version != 4)
7241
                                                                                  7291
                                                                                          return 0:
7242
                                                                                  7292 if(sum((uchar*)conf, conf->length) != 0)
7243
                                                                                  7293
                                                                                           return 0;
7244
                                                                                  7294
                                                                                         *qm = qmq*
7245
                                                                                  7295
                                                                                         return conf;
7246
                                                                                  7296 }
7247
                                                                                  7297
7248
                                                                                  7298
                                                                                  7299
7249
```

```
7300 void
                                                                                 7350 // The local APIC manages internal (non-I/0) interrupts.
7301 mpinit(void)
                                                                                 7351 // See Chapter 8 & Appendix C of Intel processor manual volume 3.
7302 {
                                                                                 7352
7303 uchar *p, *e;
                                                                                 7353 #include "param.h"
7304 int ismp;
                                                                                 7354 #include "types.h"
7305 struct mp *mp;
                                                                                 7355 #include "defs.h"
7306
      struct mpconf *conf;
                                                                                 7356 #include "date.h"
7307
      struct mpproc *proc;
                                                                                 7357 #include "memlayout.h"
7308
      struct mpioapic *ioapic;
                                                                                 7358 #include "traps.h"
                                                                                 7359 #include "mmu.h"
7309
7310 if((conf = mpconfig(\&mp)) == 0)
                                                                                 7360 #include "x86.h"
7311
        panic("Expect to run on an SMP");
                                                                                 7361
7312
      ismp = 1:
                                                                                 7362 // Local APIC registers, divided by 4 for use as uint[] indices.
7313
      lapic = (uint*)conf->lapicaddr;
                                                                                 7363 #define ID
                                                                                                      (0x0020/4) // ID
                                                                                 7364 #define VER
7314
       for(p=(uchar*)(conf+1). e=(uchar*)conf+conf->length: p<e: ){</pre>
                                                                                                      (0x0030/4) // Version
7315
        switch(*p){
                                                                                 7365 #define TPR
                                                                                                      (0x0080/4) // Task Priority
7316
        case MPPROC:
                                                                                 7366 #define EOI
                                                                                                      (0x00B0/4) // EOI
7317
          proc = (struct mpproc*)p:
                                                                                 7367 #define SVR
                                                                                                      (0x00F0/4) // Spurious Interrupt Vector
7318
                                                                                 7368 #define ENABLE
                                                                                                          0x00000100 // Unit Enable
          if(ncpu < NCPU) {
7319
            cpus[ncpu].apicid = proc->apicid; // apicid may differ from ncpu
                                                                                 7369 #define ESR
                                                                                                      (0x0280/4) // Error Status
7320
            ncpu++;
                                                                                 7370 #define ICRLO
                                                                                                      (0x0300/4) // Interrupt Command
7321
                                                                                 7371
                                                                                        #define INIT
                                                                                                           0x00000500 // INIT/RESET
7322
          p += sizeof(struct mpproc);
                                                                                 7372
                                                                                        #define STARTUP
                                                                                                          0x00000600 // Startup IPI
7323
          continue;
                                                                                 7373 #define DELIVS
                                                                                                          0x00001000 // Delivery status
7324
        case MPIOAPIC:
                                                                                 7374 #define ASSERT
                                                                                                          0x00004000
                                                                                                                       // Assert interrupt (vs deassert)
7325
          ioapic = (struct mpioapic*)p;
                                                                                 7375
                                                                                        #define DEASSERT
                                                                                                          0x00000000
                                                                                 7376 #define LEVEL
7326
          ioapicid = ioapic->apicno;
                                                                                                          0x00008000
                                                                                                                      // Level triggered
7327
                                                                                        #define BCAST
                                                                                                                       // Send to all APICs, including self.
          p += sizeof(struct mpioapic);
                                                                                 7377
                                                                                                          0x00080000
7328
          continue:
                                                                                 7378
                                                                                       #define BUSY
                                                                                                          0x00001000
7329
                                                                                 7379 #define FIXED
        case MPBUS:
                                                                                                          0x00000000
7330
        case MPIOINTR:
                                                                                 7380 #define ICRHI
                                                                                                      (0x0310/4) // Interrupt Command [63:32]
7331
        case MPLINTR:
                                                                                 7381 #define TIMER
                                                                                                      (0x0320/4)
                                                                                                                  // Local Vector Table 0 (TIMER)
                                                                                 7382 #define X1
                                                                                                           0x0000000B // divide counts by 1
7332
          p += 8;
7333
          continue:
                                                                                 7383 #define PERIODIC 0x00020000 // Periodic
7334
        default:
                                                                                 7384 #define PCINT
                                                                                                      (0x0340/4) // Performance Counter LVT
7335
                                                                                 7385 #define LINTO
          ismp = 0;
                                                                                                      (0x0350/4)
                                                                                                                 // Local Vector Table 1 (LINTO)
7336
          break;
                                                                                 7386 #define LINT1
                                                                                                      (0x0360/4) // Local Vector Table 2 (LINT1)
7337
        }
                                                                                 7387 #define ERROR
                                                                                                      (0x0370/4)
                                                                                                                  // Local Vector Table 3 (ERROR)
7338 }
                                                                                 7388 #define MASKED
                                                                                                          0x00010000 // Interrupt masked
7339
      if(!ismp)
                                                                                 7389 #define TICR
                                                                                                      (0x0380/4) // Timer Initial Count
7340
        panic("Didn't find a suitable machine");
                                                                                 7390 #define TCCR
                                                                                                      (0x0390/4)
                                                                                                                  // Timer Current Count
7341
                                                                                 7391 #define TDCR
                                                                                                      (0x03E0/4)
                                                                                                                 // Timer Divide Configuration
7342
                                                                                 7392
       if(mp->imcrp){
7343
        // Bochs doesn't support IMCR, so this doesn't run on Bochs.
                                                                                 7393 volatile uint *lapic; // Initialized in mp.c
7344
        // But it would on real hardware.
                                                                                 7394
7345
                                                                                 7395
        outb(0x22, 0x70); // Select IMCR
7346
        outb(0x23, inb(0x23) \mid 1); // Mask external interrupts.
                                                                                 7396
7347
      }
                                                                                 7397
7348 }
                                                                                 7398
7349
                                                                                 7399
```

Sheet 73 Sheet 73

```
7400 static void
                                                                                7450 // Enable interrupts on the APIC (but not on the processor).
7401 lapicw(int index, int value)
                                                                                7451 lapicw(TPR, 0);
7402 {
                                                                                7452 }
7403 lapic[index] = value;
                                                                                7453
7404 lapic[ID]; // wait for write to finish, by reading
                                                                                7454 int
7405 }
                                                                                7455 lapicid(void)
7406
                                                                                7456 {
7407 void
                                                                                7457 if (!lapic)
7408 lapicinit(void)
                                                                                7458
                                                                                         return 0:
                                                                                7459 return lapic[ID] >> 24;
7409 {
7410 if(!lapic)
                                                                                7460 }
7411
        return:
                                                                                7461
7412
                                                                                7462 // Acknowledge interrupt.
7413 // Enable local APIC; set spurious interrupt vector.
                                                                                7463 void
7414 lapicw(SVR. ENABLE | (T IROO + IRO SPURIOUS)):
                                                                                7464 lapiceoi(void)
7415
                                                                                7465 {
7416 // The timer repeatedly counts down at bus frequency
                                                                                7466 if(lapic)
7417 // from lapic[TICR] and then issues an interrupt.
                                                                                7467
                                                                                         lapicw(EOI. 0):
7418 // If xv6 cared more about precise timekeeping,
                                                                                7468 }
7419 // TICR would be calibrated using an external time source.
                                                                                7469
7420 lapicw(TDCR, X1):
                                                                                7470 // Spin for a given number of microseconds.
7421 lapicw(TIMER, PERIODIC | (T_IRQO + IRQ_TIMER));
                                                                                7471 // On real hardware would want to tune this dynamically.
7422
      lapicw(TICR, 10000000);
                                                                                7472 void
7423
                                                                                7473 microdelay(int us)
7424 // Disable logical interrupt lines.
                                                                                7474 {
7425
      lapicw(LINTO, MASKED);
                                                                                7475 }
7426
      lapicw(LINT1, MASKED);
                                                                                7476
7427
                                                                                7477 #define CMOS_PORT
                                                                                                         0x70
7428 // Disable performance counter overflow interrupts
                                                                                7478 #define CMOS_RETURN 0x71
7429 // on machines that provide that interrupt entry.
                                                                                7479
7430 if(((lapic[VER]>>16) & 0xFF) >= 4)
                                                                                7480 // Start additional processor running entry code at addr.
7431
        lapicw(PCINT, MASKED);
                                                                                7481 // See Appendix B of MultiProcessor Specification.
7432
                                                                                7482 void
7433 // Map error interrupt to IRQ_ERROR.
                                                                                7483 lapicstartap(uchar apicid, uint addr)
7434
      lapicw(ERROR, T_IRQ0 + IRQ_ERROR);
                                                                                7484 {
7435
                                                                                7485 int i;
7436 // Clear error status register (requires back-to-back writes).
                                                                                7486
                                                                                      ushort *wrv;
7437
      lapicw(ESR, 0);
                                                                                7487
7438
      lapicw(ESR, 0);
                                                                                7488 // "The BSP must initialize CMOS shutdown code to OAH
7439
                                                                                7489
                                                                                      // and the warm reset vector (DWORD based at 40:67) to point at
7440
      // Ack any outstanding interrupts.
                                                                                7490
                                                                                      // the AP startup code prior to the [universal startup algorithm]."
                                                                                7491 outb(CMOS_PORT, 0xF); // offset 0xF is shutdown code
7441
      lapicw(EOI, 0);
7442
                                                                                7492 outb(CMOS_PORT+1, 0x0A);
7443
      // Send an Init Level De-Assert to synchronise arbitration ID's.
                                                                                7493
                                                                                      wrv = (ushort*)P2V((0x40 << 4 \mid 0x67)); // Warm reset vector
7444
      lapicw(ICRHI. 0):
                                                                                7494
                                                                                      wrv[0] = 0:
      lapicw(ICRLO, BCAST | INIT | LEVEL);
                                                                                      wrv[1] = addr >> 4;
7445
                                                                                7495
7446
      while(lapic[ICRL0] & DELIVS)
                                                                                7496
7447
       ;
                                                                                7497
7448
                                                                                7498
7449
                                                                                7499
```

```
7500
    // "Universal startup algorithm."
                                                                                 7550 // gemu seems to use 24-hour GWT and the values are BCD encoded
7501 // Send INIT (level-triggered) interrupt to reset other CPU.
                                                                                 7551 void cmostime(struct rtcdate *r)
7502 lapicw(ICRHI, apicid<<24);
                                                                                 7552 {
      lapicw(ICRLO, INIT | LEVEL | ASSERT);
7503
                                                                                 7553 struct rtcdate t1, t2;
7504
      microdelay(200);
                                                                                 7554
                                                                                       int sb, bcd;
7505 lapicw(ICRLO, INIT | LEVEL);
                                                                                 7555
7506
      microdelay(100); // should be 10ms, but too slow in Bochs!
                                                                                 7556
                                                                                       sb = cmos_read(CMOS_STATB);
7507
                                                                                 7557
7508 // Send startup IPI (twice!) to enter code.
                                                                                 7558
                                                                                       bcd = (sb & (1 << 2)) == 0;
                                                                                 7559
7509
      // Regular hardware is supposed to only accept a STARTUP
7510 // when it is in the halted state due to an INIT. So the second
                                                                                 7560
                                                                                       // make sure CMOS doesn't modify time while we read it
7511 // should be ignored, but it is part of the official Intel algorithm.
                                                                                 7561
                                                                                       for(;;) {
7512 // Bochs complains about the second one. Too bad for Bochs.
                                                                                 7562
                                                                                         fill_rtcdate(&t1);
                                                                                         if(cmos_read(CMOS_STATA) & CMOS_UIP)
7513
      for(i = 0; i < 2; i++){
                                                                                 7563
        lapicw(ICRHI. apicid<<24):
7514
                                                                                 7564
                                                                                              continue:
7515
        lapicw(ICRLO, STARTUP | (addr>>12));
                                                                                 7565
                                                                                          fill_rtcdate(&t2);
7516
        microdelay(200);
                                                                                 7566
                                                                                         if(memcmp(\&t1, \&t2, sizeof(t1)) == 0)
7517 }
                                                                                 7567
                                                                                           break:
7518 }
                                                                                 7568
                                                                                      }
7519
                                                                                 7569
7520 #define CMOS STATA
                         0x0a
                                                                                 7570
                                                                                       // convert
7521 #define CMOS STATB
                         0x0b
                                                                                 7571 if(bcd) {
7522 #define CMOS_UIP
                        (1 << 7)
                                        // RTC update in progress
                                                                                 7572 #define
                                                                                                 CONV(x)
                                                                                                             (t1.x = ((t1.x >> 4) * 10) + (t1.x & 0xf))
7523
                                                                                 7573
                                                                                         CONV(second);
7524 #define SECS
                                                                                 7574
                    0x00
                                                                                         CONV(minute):
7525 #define MINS
                                                                                 7575
                                                                                         CONV(hour );
                    0x02
7526 #define HOURS
                    0x04
                                                                                 7576
                                                                                         CONV(day);
7527 #define DAY
                                                                                 7577
                    0x07
                                                                                         CONV(month);
7528 #define MONTH
                    0x08
                                                                                 7578
                                                                                         CONV(year );
7529 #define YEAR
                                                                                 7579 #undef
                    0x09
                                                                                                CONV
7530
                                                                                 7580 }
7531 static uint cmos_read(uint reg)
                                                                                 7581
7532 {
                                                                                 7582 *r = t1:
7533 outb(CMOS_PORT, reg);
                                                                                 7583
                                                                                       r->year += 2000;
7534 microdelay(200);
                                                                                 7584 }
7535
                                                                                 7585
7536 return inb(CMOS_RETURN);
                                                                                 7586
7537 }
                                                                                 7587
7538
                                                                                 7588
7539 static void fill_rtcdate(struct rtcdate *r)
                                                                                 7589
7540 {
                                                                                 7590
7541 r->second = cmos_read(SECS);
                                                                                 7591
7542 r->minute = cmos_read(MINS);
                                                                                 7592
7543 r \rightarrow hour = cmos_read(HOURS);
                                                                                 7593
7544 r->dav
                = cmos read(DAY):
                                                                                 7594
7545 r->month = cmos_read(MONTH);
                                                                                 7595
7546 r \rightarrow year = cmos_read(YEAR);
                                                                                 7596
7547 }
                                                                                 7597
7548
                                                                                 7598
7549
                                                                                 7599
```

Sheet 75 Sheet 75

```
7600 // The I/O APIC manages hardware interrupts for an SMP system.
                                                                                  7650 void
7601 // http://www.intel.com/design/chipsets/datashts/29056601.pdf
                                                                                  7651 ioapicinit(void)
7602 // See also picirg.c.
                                                                                  7652 {
7603
                                                                                  7653
                                                                                        int i, id, maxintr;
7604 #include "types.h"
                                                                                  7654
7605 #include "defs.h"
                                                                                  7655
                                                                                        ioapic = (volatile struct ioapic*)IOAPIC;
7606 #include "traps.h"
                                                                                  7656
                                                                                         maxintr = (ioapicread(REG_VER) >> 16) & 0xFF;
7607
                                                                                         id = ioapicread(REG_ID) >> 24;
                                                                                  7657
7608 #define IOAPIC 0xFEC00000 // Default physical address of IO APIC
                                                                                  7658
                                                                                         if(id != ioapicid)
                                                                                           cprintf("ioapicinit: id isn't equal to ioapicid; not a MP\n");
7609
                                                                                  7659
7610 #define REG_ID
                        0x00 // Register index: ID
                                                                                  7660
7611 #define REG VER
                        0x01 // Register index: version
                                                                                  7661
                                                                                        // Mark all interrupts edge-triggered, active high, disabled,
7612 #define REG_TABLE 0x10 // Redirection table base
                                                                                  7662
                                                                                         // and not routed to any CPUs.
7613
                                                                                  7663
                                                                                         for(i = 0; i \le maxintr; i++){
                                                                                           ioapicwrite(REG_TABLE+2*i, INT_DISABLED | (T_IRQ0 + i));
7614 // The redirection table starts at REG TABLE and uses
                                                                                  7664
7615 // two registers to configure each interrupt.
                                                                                  7665
                                                                                           ioapicwrite(REG_TABLE+2*i+1, 0);
7616 // The first (low) register in a pair contains configuration bits.
                                                                                  7666 }
7617 // The second (high) register contains a bitmask telling which
                                                                                  7667 }
7618 // CPUs can serve that interrupt.
                                                                                  7668
7619 #define INT_DISABLED
                           0x00010000 // Interrupt disabled
                                                                                  7669 void
7620 #define INT LEVEL
                            0x00008000 // Level-triggered (vs edge-)
                                                                                  7670 ioapicenable(int irg. int cpunum)
7621 #define INT ACTIVELOW 0x00002000 // Active low (vs high)
                                                                                  7671 {
7622 #define INT_LOGICAL
                            0x00000800 // Destination is CPU id (vs APIC ID)
                                                                                  7672
                                                                                        // Mark interrupt edge-triggered, active high,
7623
                                                                                  7673
                                                                                         // enabled, and routed to the given cpunum,
7624 volatile struct ioapic *ioapic;
                                                                                  7674
                                                                                         // which happens to be that cpu's APIC ID.
                                                                                  7675
                                                                                         ioapicwrite(REG_TABLE+2*irg, T_IRQ0 + irg);
7625
7626 // IO APIC MMIO structure: write reg, then read or write data.
                                                                                  7676
                                                                                         ioapicwrite(REG_TABLE+2*irg+1, cpunum << 24);</pre>
7627 struct ioapic {
                                                                                  7677 }
                                                                                  7678
7628 uint reg;
7629 uint pad[3];
                                                                                  7679
7630 uint data;
                                                                                  7680
7631 };
                                                                                  7681
7632
                                                                                  7682
7633 static uint
                                                                                  7683
7634 ioapicread(int reg)
                                                                                  7684
7635 {
                                                                                  7685
7636 ioapic->reg = reg;
                                                                                  7686
7637
      return ioapic->data;
                                                                                  7687
7638 }
                                                                                  7688
7639
                                                                                  7689
7640 static void
                                                                                  7690
7641 ioapicwrite(int reg, uint data)
                                                                                  7691
7642 {
                                                                                  7692
7643
      ioapic->reg = reg;
                                                                                  7693
      ioapic->data = data;
7644
                                                                                  7694
7645 }
                                                                                  7695
7646
                                                                                  7696
7647
                                                                                  7697
7648
                                                                                  7698
7649
                                                                                  7699
```

Sheet 76 Sheet 76

```
7700 // PC keyboard interface constants
                                                                                    7750 static uchar normalmap[256] =
7701
                                                                                    7751 {
                                      // kbd controller status port(I)
7702 #define KBSTATP
                             0x64
                                                                                    7752
                                                                                          NO,
                                                                                                 0x1B, '1',
                                                                                                                               '5', '6', // 0x00
                                                                                                                                     '\t'.
                                                                                                       '9',
                                                                                                                   '-'.
                                                                                                                          '='
                                                                                                                                '\b',
7703 #define KBS_DIB
                             0x01
                                     // kbd data in buffer
                                                                                    7753
                                                                                           '7',
                                                                                                 '8',
                                                                                                             '0',
7704 #define KBDATAP
                             0x60
                                     // kbd data port(I)
                                                                                    7754
                                                                                           'q',
                                                                                                 'w'.
                                                                                                       'e',
                                                                                                             'r',
                                                                                                                   't',
                                                                                                                         'у',
                                                                                                                                'u', 'i', // 0x10
7705
                                                                                    7755
                                                                                                 'n,
                                                                                                       Ί[,
                                                                                                             ']'.
                                                                                                                   '\n', NO,
                                                                                                                                     's',
                                                                                           'o',
                                                                                                                                'a'.
                                                                                                 'f'.
                                                                                                       'g',
                                                                                                                   'j',
                                                                                                                         'k'
                                                                                                                                '1',
                                                                                                                                      ';', // 0x20
7706 #define NO
                             0
                                                                                    7756
                                                                                           'd',
                                                                                                             'h',
                                                                                                . , , ,
                                                                                           '\''
                                                                                                             '\\',
7707
                                                                                    7757
                                                                                                       NO,
                                                                                                                   'z',
                                                                                                                         'х'.
                                                                                                                                'c',
                                                                                                                                      'v'
7708 #define SHIFT
                              (1 << 0)
                                                                                    7758
                                                                                           'b',
                                                                                                 'n,
                                                                                                       'n,
                                                                                                                         '/',
                                                                                                                                     '*', // 0x30
                                                                                                                               NO.
7709 #define CTL
                                                                                                       NO,
                              (1 << 1)
                                                                                    7759
                                                                                           NO,
                                                                                                                   NO,
                                                                                                                         NO,
                                                                                                             NO,
                                                                                                                               NO.
7710 #define ALT
                              (1<<2)
                                                                                    7760
                                                                                           NO,
                                                                                                NO.
                                                                                                       NO,
                                                                                                             NO,
                                                                                                                   NO.
                                                                                                                         NO,
                                                                                                                               NO,
                                                                                                                                      '7', // 0x40
                                                                                                '9',
                                                                                                      '-'.
                                                                                                                               '+', '1',
7711
                                                                                           '8'.
                                                                                                             '4', '5',
                                                                                                                         '6'.
                                                                                    7761
7712 #define CAPSLOCK
                                                                                    7762
                                                                                           '2', '3',
                                                                                                       '0'.
                                                                                                             '.', NO,
                                                                                                                         NO,
                                                                                                                               NO.
                                                                                                                                      NO,
                              (1 << 3)
                                                                                                                                           // 0x50
7713 #define NUMLOCK
                              (1 << 4)
                                                                                    7763
                                                                                          [0x9C] '\n',
                                                                                                             // KP_Enter
                                                                                           [0xB5] '/',
7714 #define SCROLLLOCK
                             (1 << 5)
                                                                                    7764
                                                                                                             // KP Div
7715
                                                                                    7765
                                                                                           [0xC8] KEY_UP,
                                                                                                             [0xD0] KEY_DN,
                                                                                                             [0xD1] KEY_PGDN,
7716 #define EOESC
                              (1 << 6)
                                                                                    7766
                                                                                           [0xC9] KEY_PGUP,
                                                                                           [0xCB] KEY_LF,
7717
                                                                                    7767
                                                                                                             [0xCD] KEY_RT,
7718 // Special keycodes
                                                                                    7768
                                                                                          [0x97] KEY_HOME,
                                                                                                             [0xCF] KEY_END,
                                                                                          [0xD2] KEY_INS,
7719 #define KEY_HOME
                             0xE0
                                                                                    7769
                                                                                                             [0xD3] KEY_DEL
7720 #define KEY END
                             0xE1
                                                                                    7770 };
7721 #define KEY UP
                             0xE2
                                                                                    7771
7722 #define KEY_DN
                             0xE3
                                                                                    7772 static uchar shiftmap[256] =
7723 #define KEY_LF
                             0xE4
                                                                                    7773 {
                                                                                    7774
                                                                                                       '!'.
                                                                                                                   '#'.
                                                                                                                         '$'.
                                                                                                                                '%', '^', // 0x00
7724 #define KEY RT
                             0xE5
                                                                                          NO.
                                                                                                033.
                                                                                                             '@'.
                                                                                                , , ,
7725 #define KEY_PGUP
                             0xE6
                                                                                    7775
                                                                                           '&',
                                                                                                       '(',
                                                                                                             ')'.
                                                                                                                                '\b', '\t',
                                                                                                 'W',
                                                                                                                                'U', 'I', // 0x10
7726 #define KEY_PGDN
                             0xE7
                                                                                    7776
                                                                                           'Q',
                                                                                                       'Ε',
                                                                                                             'R',
                                                                                                                   Ϋ́,
                                                                                                                         ΥΥ',
                                                                                           '0',
                                                                                                 'Ρ',
                                                                                                                    '\n',
                                                                                                                                'Α',
                                                                                                                                     'S'
7727 #define KEY_INS
                             0xE8
                                                                                    7777
                                                                                                                         NO,
                                                                                                       'G'
                                                                                                                   'J',
                                                                                                                                     ':', // 0x20
7728 #define KEY_DEL
                             0xE9
                                                                                    7778
                                                                                           'D'
                                                                                                 'F',
                                                                                                             'H'.
                                                                                                                         'K'
                                                                                                                                'L',
                                                                                           , ,,
                                                                                                 '~'.
                                                                                                       NO,
                                                                                                             '|',
                                                                                                                   'Ζ',
                                                                                                                         'Х'.
                                                                                                                                'C',
                                                                                                                                     ٧٧'.
7729
                                                                                    7779
                                                                                                                                     '*', // 0x30
                                                                                                       'M',
                                                                                                                         '?',
7730 // C('A') == Control-A
                                                                                    7780
                                                                                           'B',
                                                                                                 'N',
                                                                                                             '<',
                                                                                                                   '>',
                                                                                                                               NO,
                                                                                                 , ,
7731 #define C(x) (x - '@')
                                                                                    7781
                                                                                          NO,
                                                                                                       NO,
                                                                                                             NO,
                                                                                                                   NO.
                                                                                                                         NO,
                                                                                                                               NO,
                                                                                                                                      NO.
                                                                                                                                      '7', // 0x40
                                                                                                       NO,
                                                                                                             NO,
7732
                                                                                    7782
                                                                                          NO,
                                                                                                NO,
                                                                                                                   NO,
                                                                                                                         NO.
                                                                                                                               NO.
                                                                                                '9',
                                                                                                      '-',
7733 static uchar shiftcode[256] =
                                                                                    7783
                                                                                           '8',
                                                                                                             '4',
                                                                                                                   '5',
                                                                                                                         '6',
                                                                                                                                     '1',
                                                                                                '3', '0',
                                                                                                             '.', NO,
7734 {
                                                                                    7784
                                                                                           '2',
                                                                                                                         NO.
                                                                                                                               NO.
                                                                                                                                      NO, // 0x50
                                                                                           [0x9C] '\n',
7735
       [0x1D] CTL,
                                                                                    7785
                                                                                                             // KP_Enter
7736
       [0x2A] SHIFT,
                                                                                    7786
                                                                                          [0xB5] '/',
                                                                                                             // KP_Div
7737
       [0x36] SHIFT,
                                                                                    7787
                                                                                           [0xC8] KEY_UP,
                                                                                                             [0xD0] KEY_DN,
7738
       [0x38] ALT,
                                                                                    7788
                                                                                           [0xC9] KEY_PGUP,
                                                                                                             [0xD1] KEY_PGDN,
7739
       [0x9D] CTL,
                                                                                    7789
                                                                                           [0xCB] KEY_LF,
                                                                                                             [0xCD] KEY_RT,
7740 [0xB8] ALT
                                                                                    7790
                                                                                           [0x97] KEY_HOME,
                                                                                                             [0xCF] KEY_END,
7741 };
                                                                                    7791
                                                                                          [0xD2] KEY_INS,
                                                                                                             [0xD3] KEY_DEL
7742
                                                                                    7792 };
7743 static uchar togglecode[256] =
                                                                                    7793
7744 {
                                                                                    7794
7745
                                                                                    7795
       [0x3A] CAPSLOCK,
7746
       [0x45] NUMLOCK,
                                                                                    7796
7747
       [0x46] SCROLLLOCK
                                                                                    7797
7748 };
                                                                                    7798
7749
                                                                                    7799
```

```
7800 static uchar ctlmap[256] =
                                                                                  7850 #include "types.h"
7801 {
                                                                                  7851 #include "x86.h"
                                 NO,
7802 NO,
                NO,
                         NO,
                                           NO,
                                                    NO,
                                                             NO,
                                                                      NO,
                                                                                  7852 #include "defs.h"
                                                                                  7853 #include "kbd.h"
7803
      NO,
                NO,
                         NO,
                                 NO,
                                           NO,
                                                    NO,
                                                             NO,
                                                                      NO,
7804
      C('Q'), C('W'), C('E'), C('R'), C('T'), C('Y'), C('U'), C('I'),
                                                                                  7854
7805
      C('0'), C('P'), NO,
                                 NO,
                                           '\r',
                                                   NO,
                                                             C('A'), C('S'),
                                                                                  7855 int
7806
      C('D'), C('F'), C('G'), C('H'), C('J'), C('K'), C('L'), NO,
                                                                                  7856 kbdgetc(void)
7807
      NO.
                NO,
                         NO,
                                 C(''\setminus'), C('Z'), C('X'), C('C'), C('V'),
                                                                                  7857 {
7808
      C('B'), C('N'), C('M'), NO,
                                          NO,
                                                   C('/'), NO,
                                                                      NO,
                                                                                  7858 static uint shift;
      [0x9C] '\r',
                         // KP_Enter
7809
                                                                                  7859
                                                                                        static uchar *charcode[4] = {
7810
       [0xB5] C('/'),
                        // KP_Div
                                                                                  7860
                                                                                          normalmap, shiftmap, ctlmap, ctlmap
                                                                                  7861
7811
       [0xC8] KEY_UP,
                         [0xD0] KEY_DN,
                                                                                        };
7812
       [0xC9] KEY_PGUP,
                                                                                  7862
                        [0xD1] KEY_PGDN,
                                                                                        uint st, data, c;
7813
       [0xCB] KEY_LF,
                         [0xCD] KEY_RT,
                                                                                  7863
                                                                                  7864
7814
       [0x97] KEY_HOME, [0xCF] KEY_END,
                                                                                        st = inb(KBSTATP):
7815
       [0xD2] KEY_INS,
                         [0xD3] KEY_DEL
                                                                                  7865
                                                                                        if((st & KBS_DIB) == 0)
7816 };
                                                                                  7866
                                                                                          return -1;
7817
                                                                                  7867
                                                                                         data = inb(KBDATAP):
7818
                                                                                  7868
7819
                                                                                  7869
                                                                                        if(data == 0xE0){
7820
                                                                                  7870
                                                                                          shift |= E0ESC:
7821
                                                                                  7871
                                                                                          return 0;
7822
                                                                                  7872 } else if(data & 0x80){
7823
                                                                                  7873
                                                                                          // Key released
7824
                                                                                  7874
                                                                                          data = (shift & EOESC ? data : data & 0x7F);
7825
                                                                                  7875
                                                                                          shift &= ~(shiftcode[data] | E0ESC);
7826
                                                                                  7876
                                                                                          return 0;
7827
                                                                                        } else if(shift & EOESC){
                                                                                  7877
7828
                                                                                  7878
                                                                                          // Last character was an EO escape; or with 0x80
7829
                                                                                  7879
                                                                                          data = 0x80;
                                                                                  7880
7830
                                                                                          shift &= ~EOESC;
7831
                                                                                  7881 }
                                                                                  7882
7832
7833
                                                                                  7883
                                                                                        shift |= shiftcode[data];
7834
                                                                                        shift ^= togglecode[data];
7835
                                                                                  7885 c = charcode[shift & (CTL | SHIFT)][data];
7836
                                                                                  7886 if(shift & CAPSLOCK){
7837
                                                                                  7887
                                                                                          if('a' <= c && c <= 'z')
7838
                                                                                  7888
                                                                                             c += 'A' - 'a';
                                                                                          else if('A' <= c && c <= 'Z')
7839
                                                                                  7889
7840
                                                                                  7890
                                                                                             c += 'a' - 'A';
7841
                                                                                 7891 }
7842
                                                                                  7892 return c;
7843
                                                                                  7893 }
7844
                                                                                 7894
7845
                                                                                  7895 void
7846
                                                                                  7896 kbdintr(void)
7847
                                                                                  7897 {
7848
                                                                                  7898 consoleintr(kbdgetc);
7849
                                                                                  7899 }
```

Sheet 78 Sheet 78

7900 // Console input and output.	7950
7901 // Input is from the keyboard or serial port.	7951
7902 // Output is written to the screen and serial port.	7952
7903	7953
7904 #include "types.h"	7954
7905 #include "defs.h"	7955
7906 #include "param.h"	7956
7907 #include "traps.h"	7957
7908 #include "spinlock.h"	7958
7909 #include "sleeplock.h"	7959
7910 #include "fs.h"	7960
7911 #include "file.h"	7961
7912 #include "memlayout.h"	7962
7913 #include "mmu.h"	7963
7914 #include "proc.h"	7964
7915 #include "x86.h"	7965
7916	7966
7917 static void consputc(int); 7918	7967
	7968 7969
7919 static int panicked = 0; 7920	7969 7970
7921 static struct {	7971
7922 struct spinlock lock;	7972
7923 int locking;	7973
7924 } cons;	7974
7925	7975
7926 static void	7976
7927 printint(int xx, int base, int sign)	7977
7928 {	7978
7929 static char digits[] = "0123456789abcdef";	7979
7930 char buf[16];	7980
7931 int i;	7981
7932 uint x;	7982
7933	7983
7934 if(sign && (sign = xx < 0))	7984
7935 $x = -xx$;	7985
7936 else	7986
7937 $x = xx;$	7987
7938	7988
7939 i = 0;	7989
7940 do{	7990
7941 buf[i++] = digits[x % base];	7991
7942 }while((x /= base) != 0);	7992
7943	7993
7944 if(sign)	7994
7945 buf[i++] = '-';	7995
7946	7996
7947 while(i >= 0)	7997
7948 consputc(buf[i]);	7998
7949 }	7999

```
8050 if(locking)
8000 // Print to the console. only understands %d, %x, %p, %s.
8001 void
                                                                                 8051
                                                                                          release(&cons.lock);
8002 cprintf(char *fmt, ...)
                                                                                 8052 }
8003 {
                                                                                 8053
8004 int i, c, locking;
                                                                                 8054 void
8005
      uint *argp;
                                                                                 8055 panic(char *s)
8006
      char *s;
                                                                                 8056 {
8007
                                                                                 8057 int i;
8008
      locking = cons.locking;
                                                                                 8058
                                                                                       uint pcs[10];
8009
      if(locking)
                                                                                 8059
8010
        acquire(&cons.lock);
                                                                                 8060 cli();
8011
                                                                                 8061 cons.locking = 0;
8012
      if (fmt == 0)
                                                                                 8062 // use lapiccpunum so that we can call panic from mycpu()
8013
        panic("null fmt");
                                                                                 8063 cprintf("lapicid %d: panic: ", lapicid());
8014
                                                                                 8064 cprintf(s):
8015
      argp = (uint*)(void*)(&fmt + 1);
                                                                                 8065 cprintf("\n");
8016
      for(i = 0; (c = fmt[i] & 0xff) != 0; i++){
                                                                                 8066
                                                                                        getcallerpcs(&s, pcs);
8017
        if(c != '%'){
                                                                                 8067
                                                                                       for(i=0; i<10; i++)
8018
          consputc(c);
                                                                                 8068
                                                                                         cprintf(" %p", pcs[i]);
8019
          continue;
                                                                                 8069
                                                                                        panicked = 1; // freeze other CPU
8020
        }
                                                                                 8070
                                                                                        for(;;)
8021
        c = fmt[++i] & 0xff;
                                                                                 8071
                                                                                          ;
                                                                                 8072 }
8022
        if(c == 0)
8023
          break;
                                                                                 8073
8024
                                                                                 8074
        switch(c){
8025
        case 'd':
                                                                                 8075
8026
          printint(*argp++, 10, 1);
                                                                                 8076
8027
          break;
                                                                                 8077
8028
        case 'x':
                                                                                 8078
8029
        case 'p':
                                                                                 8079
8030
          printint(*argp++, 16, 0);
                                                                                 8080
8031
          break;
                                                                                 8081
8032
        case 's':
                                                                                 8082
          if((s = (char*)*argp++) == 0)
8033
                                                                                 8083
            s = "(null)";
8034
                                                                                 8084
8035
          for(; *s; s++)
                                                                                 8085
8036
            consputc(*s);
                                                                                 8086
8037
          break;
                                                                                 8087
8038
        case '%':
                                                                                 8088
8039
          consputc('%');
                                                                                 8089
8040
          break:
                                                                                 8090
8041
        default:
                                                                                 8091
8042
          // Print unknown % sequence to draw attention.
                                                                                 8092
8043
          consputc('%');
                                                                                 8093
8044
          consputc(c);
                                                                                 8094
8045
          break;
                                                                                 8095
8046
        }
                                                                                 8096
8047
      }
                                                                                 8097
8048
                                                                                 8098
8049
                                                                                 8099
```

```
8100 #define BACKSPACE 0x100
8101 #define CRTPORT 0x3d4
8102 static ushort *crt = (ushort*)P2V(0xb8000); // CGA memory
8103
8104 static void
8105 cgaputc(int c)
8106 {
8107 int pos;
8108
8109 // Cursor position: col + 80*row.
8110 outb(CRTPORT, 14);
8111 pos = inb(CRTPORT+1) << 8;</pre>
8112 outb(CRTPORT, 15);
8113 pos |= inb(CRTPORT+1);
8114
8115 if(c == '\n')
8116
       pos += 80 - pos\%80;
8117 else if(c == BACKSPACE){
8118
        if(pos > 0) --pos;
8119 } else
8120
        crt[pos++] = (c&0xff) \mid 0x0700: // black on white
8121
8122 if(pos < 0 \mid \mid pos > 25*80)
8123
        panic("pos under/overflow");
8124
8125
      if((pos/80) >= 24){ // Scroll up.}
8126
        memmove(crt, crt+80, sizeof(crt[0])*23*80);
8127
        pos -= 80;
        memset(crt+pos, 0, sizeof(crt[0])*(24*80 - pos));
8128
8129 }
8130
8131 outb(CRTPORT, 14);
8132 outb(CRTPORT+1, pos>>8);
8133 outb(CRTPORT, 15);
8134 outb(CRTPORT+1, pos);
8135 \quad crt[pos] = ' ' | 0x0700;
8136 }
8137
8138
8139
8140
8141
8142
8143
8144
8145
8146
8147
8148
8149
```

```
8150 void
8151 consputc(int c)
8152 {
8153 if(panicked){
8154
        cli();
8155
        for(;;)
8156
8157 }
8158
8159 if(c == BACKSPACE){
8160
        uartputc('\b'); uartputc(' '); uartputc('\b');
8161 } else
8162
        uartputc(c);
8163 cgaputc(c);
8164 }
8165
8166 #define INPUT_BUF 128
8167 struct {
8168 char buf[INPUT_BUF];
8169 uint r; // Read index
8170 uint w; // Write index
8171 uint e; // Edit index
8172 } input;
8173
8174 #define C(x) ((x)-'@') // Control-x
8175
8176 void
8177 consoleintr(int (*getc)(void))
8178 {
8179 int c, doprocdump = 0;
8180
8181 acquire(&cons.lock);
8182
      while((c = qetc()) >= 0){
8183
        switch(c){
8184
        case C('P'): // Process listing.
8185
          // procdump() locks cons.lock indirectly; invoke later
8186
          doprocdump = 1;
8187
          break;
8188
        case C('U'): // Kill line.
8189
          while(input.e != input.w &&
8190
                input.buf[(input.e-1) % INPUT_BUF] != '\n'){
8191
            input.e--:
8192
            consputc(BACKSPACE);
8193
8194
          break:
8195
        case C('H'): case '\x7f': // Backspace
8196
          if(input.e != input.w){
8197
            input.e--;
8198
            consputc(BACKSPACE);
8199
```

```
8200
                                                                                  8250
           break;
                                                                                             break;
8201
        default:
                                                                                  8251 }
8202
          if(c != 0 && input.e-input.r < INPUT_BUF){</pre>
                                                                                  8252
                                                                                        release(&cons.lock);
8203
            c = (c == '\r') ? '\n' : c;
                                                                                  8253
                                                                                        ilock(ip);
8204
            input.buf[input.e++ % INPUT_BUF] = c;
                                                                                  8254
8205
                                                                                  8255
            consputc(c);
                                                                                        return target - n;
8206
            if(c == '\n' || c == C('D') || input.e == input.r+INPUT_BUF){
                                                                                 8256 }
8207
              input.w = input.e;
                                                                                  8257
8208
               wakeup(&input.r);
                                                                                  8258 int
8209
                                                                                  8259 consolewrite(struct inode *ip, char *buf, int n)
8210
          }
                                                                                  8260 {
                                                                                  8261 int i;
8211
           break;
8212
                                                                                 8262
8213 }
                                                                                  8263
                                                                                        iunlock(ip);
8214
      release(&cons.lock);
                                                                                  8264
                                                                                        acquire(&cons.lock);
8215
      if(doprocdump) {
                                                                                  8265
                                                                                        for(i = 0; i < n; i++)
8216
        procdump(); // now call procdump() wo. cons.lock held
                                                                                  8266
                                                                                          consputc(buf[i] & 0xff);
8217 }
                                                                                  8267
                                                                                        release(&cons.lock);
8218 }
                                                                                 8268
                                                                                       ilock(ip);
8219
                                                                                  8269
8220 int
                                                                                  8270 return n:
8221 consoleread(struct inode *ip, char *dst, int n)
                                                                                 8271 }
8222 {
                                                                                  8272
8223 uint target;
                                                                                 8273 void
      int c;
                                                                                 8274 consoleinit(void)
8224
8225
                                                                                  8275 {
8226 iunlock(ip);
                                                                                  8276 initlock(&cons.lock, "console");
                                                                                 8277
8227
      target = n;
8228
      acquire(&cons.lock);
                                                                                  8278
                                                                                        devsw[CONSOLE].write = consolewrite;
                                                                                        devsw[CONSOLE].read = consoleread;
8229
      while(n > 0){
                                                                                  8279
8230
        while(input.r == input.w){
                                                                                  8280
                                                                                        cons.locking = 1;
8231
           if(myproc()->killed){
                                                                                  8281
8232
                                                                                  8282 ioapicenable(IRQ_KBD, 0);
            release(&cons.lock);
8233
            ilock(ip);
                                                                                  8283 }
8234
            return -1;
                                                                                  8284
8235
                                                                                 8285
          }
8236
          sleep(&input.r, &cons.lock);
                                                                                  8286
8237
                                                                                  8287
8238
        c = input.buf[input.r++ % INPUT_BUF];
                                                                                  8288
8239
        if(c == C('D')){ // EOF
                                                                                  8289
8240
          if(n < target){</pre>
                                                                                  8290
8241
            // Save ^D for next time, to make sure
                                                                                 8291
8242
            // caller gets a 0-byte result.
                                                                                  8292
8243
            input.r--;
                                                                                  8293
8244
                                                                                  8294
8245
                                                                                  8295
          break;
8246
        }
                                                                                  8296
8247
        *dst++ = c;
                                                                                 8297
8248
                                                                                  8298
8249
        if(c == '\n')
                                                                                  8299
```

```
8300 // Intel 8250 serial port (UART).
                                                                               8350 void
8301
                                                                               8351 uartputc(int c)
8302 #include "types.h"
                                                                               8352 {
8303 #include "defs.h"
                                                                               8353 int i;
8304 #include "param.h"
                                                                               8354
8305 #include "traps.h"
                                                                               8355 if(!uart)
8306 #include "spinlock.h"
                                                                               8356
                                                                                      return;
8307 #include "sleeplock.h"
                                                                               8357 for(i = 0; i < 128 && !(inb(COM1+5) & 0x20); i++)
8308 #include "fs.h"
                                                                               8358
                                                                                        microdelay(10);
8309 #include "file.h"
                                                                               8359 outb(COM1+0, c);
8310 #include "mmu.h"
                                                                               8360 }
8311 #include "proc.h"
                                                                               8361
8312 #include "x86.h"
                                                                               8362 static int
8313
                                                                               8363 uartgetc(void)
8314 #define COM1
                   0x3f8
                                                                               8364 {
8315
                                                                               8365 if(!uart)
8316 static int uart; // is there a uart?
                                                                               8366
                                                                                      return -1;
8317
                                                                               8367 if(!(inb(COM1+5) & 0x01))
8318 void
                                                                               8368
                                                                                      return -1:
8319 uartinit(void)
                                                                               8369 return inb(COM1+0);
8320 {
                                                                               8370 }
8321 char *p;
                                                                               8371
8322
                                                                               8372 void
8323 // Turn off the FIFO
                                                                               8373 uartintr(void)
8324 outb(COM1+2, 0);
                                                                               8374 {
8325
                                                                               8375 consoleintr(uartgetc);
8326 // 9600 baud, 8 data bits, 1 stop bit, parity off.
                                                                               8376 }
8327 outb(COM1+3, 0x80); // Unlock divisor
                                                                               8377
8328 outb(COM1+0, 115200/9600);
                                                                               8378
8329 outb(COM1+1, 0);
                                                                               8379
8330 outb(COM1+3, 0x03);
                          // Lock divisor, 8 data bits.
                                                                               8380
8331 outb(COM1+4, 0);
                                                                               8381
8332 outb(COM1+1, 0x01); // Enable receive interrupts.
                                                                               8382
8333
                                                                               8383
8334 // If status is 0xFF, no serial port.
                                                                               8384
8335 if(inb(COM1+5) == 0xFF)
                                                                               8385
8336
      return;
                                                                               8386
8337 uart = 1;
                                                                               8387
8338
                                                                               8388
8339 // Acknowledge pre-existing interrupt conditions;
                                                                               8389
8340 // enable interrupts.
                                                                               8390
8341 inb(COM1+2);
                                                                               8391
8342 inb(COM1+0);
                                                                               8392
8343
      ioapicenable(IRQ_COM1, 0);
                                                                               8393
8344
                                                                               8394
8345 // Announce that we're here.
                                                                               8395
8346
      for(p="xv6...\n"; *p; p++)
                                                                               8396
                                                                               8397
8347
        uartputc(*p);
8348 }
                                                                               8398
8349
                                                                               8399
```

```
8400 # Initial process execs /init.
8401 # This code runs in user space.
8402
8403 #include "syscall.h"
8404 #include "traps.h"
8405
8406
8407 # exec(init, argv)
8408 .globl start
8409 start:
8410
      push1 $argv
8411
      pushl $init
8412 pushl $0 // where caller pc would be
8413
      mov1 $SYS_exec, %eax
8414 int $T_SYSCALL
8415
8416 # for(;;) exit();
8417 exit:
8418 movl $SYS_exit, %eax
8419 int $T_SYSCALL
8420 jmp exit
8421
8422 # char init[] = "/init\0";
8423 init:
8424
      .string "/init\0"
8425
8426 # char *argv[] = { init, 0 };
8427 .p2align 2
8428 argv:
      .long init
8429
8430
      .long 0
8431
8432
8433
8434
8435
8436
8437
8438
8439
8440
8441
8442
8443
8444
8445
8446
8447
8448
8449
```

```
8450 #include "syscall.h"
8451 #include "traps.h"
8452
8453 #define SYSCALL(name) \
8454
      .globl name; \
8455 name: \
8456
        mov1 $SYS_ ## name, %eax; \
8457
        int $T_SYSCALL; \
8458
        ret
8459
8460 SYSCALL(fork)
8461 SYSCALL(exit)
8462 SYSCALL(wait)
8463 SYSCALL(pipe)
8464 SYSCALL(read)
8465 SYSCALL(write)
8466 SYSCALL(close)
8467 SYSCALL(kill)
8468 SYSCALL(exec)
8469 SYSCALL(open)
8470 SYSCALL(mknod)
8471 SYSCALL(unlink)
8472 SYSCALL(fstat)
8473 SYSCALL(link)
8474 SYSCALL(mkdir)
8475 SYSCALL(chdir)
8476 SYSCALL(dup)
8477 SYSCALL(getpid)
8478 SYSCALL(sbrk)
8479 SYSCALL(sleep)
8480 SYSCALL(uptime)
8481
8482
8483
8484
8485
8486
8487
8488
8489
8490
8491
8492
8493
8494
8495
8496
8497
8498
8499
```

```
8500 // init: The initial user-level program
8501
8502 #include "types.h"
8503 #include "stat.h"
8504 #include "user.h"
8505 #include "fcntl.h"
8506
8507 char *argv[] = { "sh", 0 };
8508
8509 int
8510 main(void)
8511 {
8512 int pid, wpid;
8513
8514
      if(open("console", 0_RDWR) < 0){</pre>
8515
        mknod("console", 1, 1);
8516
        open("console", O_RDWR);
8517 }
8518 dup(0); // stdout
8519
      dup(0); // stderr
8520
8521
      for(;;){
8522
        printf(1, "init: starting sh\n");
8523
        pid = fork();
        if(pid < 0){
8524
8525
          printf(1, "init: fork failed\n");
8526
          exit();
8527
        }
8528
        if(pid == 0){
8529
          exec("sh", argv);
          printf(1, "init: exec sh failed\n");
8530
8531
          exit();
8532
8533
        while((wpid=wait()) >= 0 && wpid != pid)
8534
          printf(1, "zombie!\n");
8535 }
8536 }
8537
8538
8539
8540
8541
8542
8543
8544
8545
8546
8547
8548
8549
```

```
8550 // Shell.
8551
8552 #include "types.h"
8553 #include "user.h"
8554 #include "fcntl.h"
8555
8556 // Parsed command representation
8557 #define EXEC 1
8558 #define REDIR 2
8559 #define PIPE 3
8560 #define LIST 4
8561 #define BACK 5
8562
8563 #define MAXARGS 10
8564
8565 struct cmd {
8566 int type;
8567 };
8568
8569 struct execcmd {
8570 int type;
8571 char *argv[MAXARGS];
8572 char *eargv[MAXARGS];
8573 };
8574
8575 struct redircmd {
8576 int type;
8577 struct cmd *cmd;
8578 char *file;
8579 char *efile;
8580 int mode;
8581 int fd;
8582 };
8583
8584 struct pipecmd {
8585 int type;
8586 struct cmd *left;
8587 struct cmd *right;
8588 };
8589
8590 struct listcmd {
8591 int type;
8592 struct cmd *left;
8593 struct cmd *right;
8594 };
8595
8596 struct backcmd {
8597 int type;
8598 struct cmd *cmd;
8599 };
```

```
8600 int fork1(void); // Fork but panics on failure.
                                                                                  8650
                                                                                        case PIPE:
8601 void panic(char*);
                                                                                  8651
                                                                                           pcmd = (struct pipecmd*)cmd;
8602 struct cmd *parsecmd(char*);
                                                                                  8652
                                                                                           if(pipe(p) < 0)
                                                                                             panic("pipe");
8603
                                                                                  8653
8604 // Execute cmd. Never returns.
                                                                                  8654
                                                                                           if(fork1() == 0){
8605 void
                                                                                  8655
                                                                                             close(1);
8606 runcmd(struct cmd *cmd)
                                                                                  8656
                                                                                             dup(p[1]);
8607 {
                                                                                  8657
                                                                                             close(p[0]);
8608 int p[2];
                                                                                  8658
                                                                                             close(p[1]);
8609 struct backcmd *bcmd;
                                                                                             runcmd(pcmd->left);
                                                                                  8659
8610 struct execcmd *ecmd;
                                                                                  8660
                                                                                           if(fork1() == 0){
8611
      struct listcmd *lcmd:
                                                                                  8661
8612 struct pipecmd *pcmd;
                                                                                  8662
                                                                                             close(0);
      struct redircmd *rcmd;
8613
                                                                                  8663
                                                                                             dup(p[0]);
8614
                                                                                  8664
                                                                                             close(p[0]);
8615 	 if(cmd == 0)
                                                                                  8665
                                                                                             close(p[1]);
8616
        exit();
                                                                                  8666
                                                                                             runcmd(pcmd->right);
8617
                                                                                  8667
8618
      switch(cmd->type){
                                                                                  8668
                                                                                           close(p[0]);
8619
      default:
                                                                                  8669
                                                                                           close(p[1]);
8620
        panic("runcmd");
                                                                                  8670
                                                                                           wait():
8621
                                                                                  8671
                                                                                           wait();
8622
      case EXEC:
                                                                                  8672
                                                                                           break;
8623
        ecmd = (struct execcmd*)cmd;
                                                                                  8673
8624
                                                                                  8674
        if(ecmd->argv[0] == 0)
                                                                                         case BACK:
8625
                                                                                  8675
                                                                                           bcmd = (struct backcmd*)cmd;
          exit();
8626
        exec(ecmd->argv[0], ecmd->argv);
                                                                                  8676
                                                                                           if(fork1() == 0)
8627
        printf(2, "exec %s failed\n", ecmd->argv[0]);
                                                                                  8677
                                                                                             runcmd(bcmd->cmd);
8628
        break;
                                                                                  8678
                                                                                           break;
8629
                                                                                  8679 }
8630
       case REDIR:
                                                                                  8680 exit();
8631
        rcmd = (struct redircmd*)cmd;
                                                                                  8681 }
8632
        close(rcmd->fd);
                                                                                  8682
8633
        if(open(rcmd->file, rcmd->mode) < 0){</pre>
                                                                                  8683 int
8634
          printf(2, "open %s failed\n", rcmd->file);
                                                                                  8684 getcmd(char *buf, int nbuf)
8635
          exit();
                                                                                  8685 {
8636
        }
                                                                                  8686 printf(2, "$ ");
8637
        runcmd(rcmd->cmd);
                                                                                  8687
                                                                                         memset(buf, 0, nbuf);
8638
        break:
                                                                                  8688 gets(buf, nbuf);
8639
                                                                                  8689
                                                                                        if(buf[0] == 0) // EOF
8640
       case LIST:
                                                                                  8690
                                                                                           return -1;
8641
        lcmd = (struct listcmd*)cmd;
                                                                                  8691 return 0;
8642
        if(fork1() == 0)
                                                                                  8692 }
8643
          runcmd(lcmd->left);
                                                                                  8693
8644
        wait():
                                                                                  8694
8645
        runcmd(lcmd->right);
                                                                                  8695
8646
        break;
                                                                                  8696
8647
                                                                                  8697
8648
                                                                                  8698
8649
                                                                                  8699
```

```
8700 int
8701 main(void)
8702 {
8703 static char buf[100];
8704 int fd;
8705
8706 // Ensure that three file descriptors are open.
8707
      while((fd = open("console", O_RDWR)) >= 0){
8708
       if(fd >= 3){
8709
          close(fd);
8710
          break;
8711
        }
8712 }
8713
8714 // Read and run input commands.
8715
      while(getcmd(buf, sizeof(buf)) >= 0){
8716
        if(buf[0] == 'c' && buf[1] == 'd' && buf[2] == ''){
8717
          // Chdir must be called by the parent, not the child.
8718
          buf[strlen(buf)-1] = 0; // chop \n
8719
          if(chdir(buf+3) < 0)</pre>
8720
            printf(2, "cannot cd %s\n", buf+3);
8721
          continue;
8722
8723
        if(fork1() == 0)
8724
          runcmd(parsecmd(buf));
8725
        wait();
8726 }
8727 exit();
8728 }
8729
8730 void
8731 panic(char *s)
8732 {
8733 printf(2, "%s\n", s);
8734 exit();
8735 }
8736
8737 int
8738 fork1(void)
8739 {
8740 int pid;
8741
8742 pid = fork();
8743 if(pid == -1)
        panic("fork");
8744
8745 return pid;
8746 }
8747
8748
8749
```

```
8750 // Constructors
8751
8752 struct cmd*
8753 execcmd(void)
8754 {
8755 struct execcmd *cmd;
8756
8757 cmd = malloc(sizeof(*cmd));
8758 memset(cmd, 0, sizeof(*cmd));
8759 cmd->type = EXEC;
8760 return (struct cmd*)cmd;
8761 }
8762
8763 struct cmd*
8764 redircmd(struct cmd *subcmd, char *file, char *efile, int mode, int fd)
8765 {
8766 struct redircmd *cmd;
8767
8768  cmd = malloc(sizeof(*cmd));
8769 memset(cmd, 0, sizeof(*cmd));
8770 cmd->type = REDIR:
8771 \quad cmd \rightarrow cmd = subcmd:
8772 cmd->file = file;
8773 cmd->efile = efile;
8774 cmd->mode = mode:
8775 \quad cmd \rightarrow fd = fd;
8776 return (struct cmd*)cmd;
8777 }
8778
8779 struct cmd*
8780 pipecmd(struct cmd *left, struct cmd *right)
8781 {
8782 struct pipecmd *cmd;
8783
8784 cmd = malloc(sizeof(*cmd));
8785 memset(cmd, 0, sizeof(*cmd));
8786 cmd->type = PIPE;
8787 cmd->left = left;
8788 cmd->right = right;
8789
      return (struct cmd*)cmd;
8790 }
8791
8792
8793
8794
8795
8796
8797
8798
8799
```

```
8800 struct cmd*
8801 listcmd(struct cmd *left, struct cmd *right)
8802 {
8803 struct listcmd *cmd;
8804
8805 cmd = malloc(sizeof(*cmd));
8806 memset(cmd, 0, sizeof(*cmd));
8807
      cmd->type = LIST;
8808 cmd->left = left;
8809 cmd->right = right;
8810 return (struct cmd*)cmd;
8811 }
8812
8813 struct cmd*
8814 backcmd(struct cmd *subcmd)
8815 {
8816 struct backcmd *cmd;
8817
8818 cmd = malloc(sizeof(*cmd));
8819
      memset(cmd, 0, sizeof(*cmd));
8820 cmd \rightarrow type = BACK;
8821 cmd \rightarrow cmd = subcmd;
8822 return (struct cmd*)cmd;
8823 }
8824
8825
8826
8827
8828
8829
8830
8831
8832
8833
8834
8835
8836
8837
8838
8839
8840
8841
8842
8843
8844
8845
8846
8847
8848
8849
```

```
8850 // Parsing
8851
8852 char whitespace[] = " \t\r\n\v";
8853 char symbols[] = "<|>&;()";
8854
8855 int
8856 gettoken(char **ps, char *es, char **q, char **eq)
8857 {
8858 char *s;
8859 int ret;
8860
8861 s = *ps;
8862 while(s < es && strchr(whitespace, *s))
8863
        S++;
8864 if(q)
8865
        *q = s;
     ret = *s;
8866
8867
     switch(*s){
8868 case 0:
8869
        break;
8870
     case '|':
8871
      case '(':
8872
      case ')':
      case ';':
8873
8874 case '&':
8875 case '<':
8876
        S++;
8877
        break;
8878 case '>':
8879
        S++;
8880
        if(*s == '>'){
8881
          ret = '+';
8882
          S++;
8883
        }
8884
        break;
8885
      default:
8886
        ret = 'a';
8887
        while(s < es && !strchr(whitespace, *s) && !strchr(symbols, *s))</pre>
8888
          S++;
8889
        break;
8890 }
8891 if(eq)
8892
        eq = s;
8893
8894 while(s < es && strchr(whitespace, *s))
8895
        S++;
8896
      *ps = s;
8897 return ret;
8898 }
8899
```

```
8900 int
8901 peek(char **ps, char *es, char *toks)
8902 {
8903 char *s;
8904
8905 s = *ps;
8906 while(s < es && strchr(whitespace, *s))
8907
       S++;
8908 *ps = s;
8909 return *s && strchr(toks, *s);
8910 }
8911
8912 struct cmd *parseline(char**, char*);
8913 struct cmd *parsepipe(char**, char*);
8914 struct cmd *parseexec(char**, char*);
8915 struct cmd *nulterminate(struct cmd*);
8916
8917 struct cmd*
8918 parsecmd(char *s)
8919 {
8920 char *es:
8921 struct cmd *cmd;
8922
8923 es = s + strlen(s);
8924 cmd = parseline(&s, es);
8925
      peek(&s, es, "");
8926 if(s != es){
        printf(2, "leftovers: %s\n", s);
8927
8928
        panic("syntax");
8929 }
8930 nulterminate(cmd);
8931 return cmd;
8932 }
8933
8934 struct cmd*
8935 parseline(char **ps, char *es)
8936 {
8937 struct cmd *cmd;
8938
8939 cmd = parsepipe(ps, es);
8940
      while(peek(ps, es, "&")){
        gettoken(ps, es, 0, 0);
8941
8942
        cmd = backcmd(cmd);
8943 }
8944 if(peek(ps, es, ";")){
8945
        gettoken(ps, es, 0, 0);
8946
        cmd = listcmd(cmd, parseline(ps, es));
8947 }
8948 return cmd;
8949 }
```

```
8950 struct cmd*
8951 parsepipe(char **ps, char *es)
8952 {
8953 struct cmd *cmd;
8954
8955 cmd = parseexec(ps, es);
8956 if(peek(ps, es, "|")){
8957
        gettoken(ps, es, 0, 0);
8958
        cmd = pipecmd(cmd, parsepipe(ps, es));
8959 }
8960 return cmd;
8961 }
8962
8963 struct cmd*
8964 parseredirs(struct cmd *cmd, char **ps, char *es)
8965 {
8966 int tok;
8967
     char *q, *eq;
8968
8969 while(peek(ps, es, "<>")){
8970
        tok = gettoken(ps, es, 0, 0);
8971
        if(gettoken(ps, es, &q, &eq) != 'a')
8972
          panic("missing file for redirection");
8973
        switch(tok){
8974
        case '<':
8975
          cmd = redircmd(cmd, q, eq, 0_RDONLY, 0);
8976
          break:
8977
        case '>':
8978
          cmd = redircmd(cmd, q, eq, O_WRONLY|O_CREATE, 1);
8979
          break:
8980
        case '+': // >>
8981
          cmd = redircmd(cmd, q, eq, O_WRONLY|O_CREATE, 1);
8982
          break:
8983
        }
8984 }
8985
     return cmd;
8986 }
8987
8988
8989
8990
8991
8992
8993
8994
8995
8996
8997
8998
8999
```

```
9000 struct cmd*
9001 parseblock(char **ps, char *es)
9002 {
9003 struct cmd *cmd;
9004
9005 if(!peek(ps, es, "("))
9006
        panic("parseblock");
9007
      gettoken(ps, es, 0, 0);
9008 cmd = parseline(ps, es);
9009 if(!peek(ps, es, ")"))
9010
       panic("syntax - missing )");
9011 gettoken(ps, es, 0, 0);
9012 cmd = parseredirs(cmd, ps, es);
9013
      return cmd;
9014 }
9015
9016 struct cmd*
9017 parseexec(char **ps, char *es)
9018 {
9019 char *q, *eq;
9020 int tok. argc:
9021 struct execcmd *cmd;
9022
      struct cmd *ret;
9023
9024 if(peek(ps, es, "("))
9025
        return parseblock(ps, es);
9026
9027
      ret = execcmd();
9028
      cmd = (struct execcmd*)ret;
9029
9030 argc = 0;
9031
      ret = parseredirs(ret, ps, es);
9032
      while(!peek(ps, es, "|)&;")){
9033
        if((tok=gettoken(ps, es, &q, &eq)) == 0)
9034
          break;
9035
        if(tok != 'a')
9036
          panic("syntax");
9037
        cmd->argv[argc] = q;
9038
        cmd->eargv[argc] = eq;
9039
        argc++;
9040
        if(argc >= MAXARGS)
9041
          panic("too many args");
9042
        ret = parseredirs(ret, ps, es);
9043 }
9044 cmd->argv[argc] = 0;
9045 cmd \rightarrow eargv[argc] = 0;
9046
      return ret;
9047 }
9048
9049
```

```
9050 // NUL-terminate all the counted strings.
9051 struct cmd*
9052 nulterminate(struct cmd *cmd)
9053 {
9054 int i;
9055 struct backcmd *bcmd;
9056 struct execcmd *ecmd;
9057 struct listcmd *lcmd;
9058 struct pipecmd *pcmd;
     struct redircmd *rcmd;
9059
9060
9061 	 if(cmd == 0)
9062
        return 0;
9063
9064 switch(cmd->type){
9065
      case EXEC:
9066
        ecmd = (struct execcmd*)cmd;
9067
        for(i=0; ecmd->argv[i]; i++)
9068
          *ecmd->eargv[i] = 0;
9069
        break;
9070
9071
      case REDIR:
9072
        rcmd = (struct redircmd*)cmd;
9073
        nulterminate(rcmd->cmd);
9074
        *rcmd->efile = 0:
9075
        break;
9076
9077
      case PIPE:
9078
        pcmd = (struct pipecmd*)cmd;
9079
        nulterminate(pcmd->left);
9080
        nulterminate(pcmd->right);
9081
        break;
9082
9083
      case LIST:
9084
        lcmd = (struct listcmd*)cmd;
9085
        nulterminate(lcmd->left);
9086
        nulterminate(lcmd->right);
9087
        break;
9088
9089
      case BACK:
9090
        bcmd = (struct backcmd*)cmd;
9091
        nulterminate(bcmd->cmd);
9092
        break;
9093 }
9094 return cmd;
9095 }
9096
9097
9098
9099
```

```
9100 #include "asm.h"
                                                                                         # Complete the transition to 32-bit protected mode by using a long jmp
9101 #include "memlayout.h"
                                                                                         # to reload %cs and %eip. The segment descriptors are set up with no
9102 #include "mmu.h"
                                                                                  9152
                                                                                         # translation, so that the mapping is still the identity mapping.
9103
                                                                                  9153
                                                                                                $(SEG_KCODE<<3), $start32
9104 # Start the first CPU: switch to 32-bit protected mode, jump into C.
                                                                                  9154
9105 # The BIOS loads this code from the first sector of the hard disk into
                                                                                  9155 .code32 # Tell assembler to generate 32-bit code now.
9106 # memory at physical address 0x7c00 and starts executing in real mode
                                                                                  9156 start32:
9107 # with %cs=0 %ip=7c00.
                                                                                  9157
                                                                                         # Set up the protected-mode data segment registers
9108
                                                                                  9158
                                                                                                 $(SEG_KDATA<<3), %ax
                                                                                                                         # Our data segment selector
                                                                                         movw
9109 .code16
                                   # Assemble for 16-bit mode
                                                                                  9159
                                                                                                 %ax, %ds
                                                                                                                         # -> DS: Data Segment
                                                                                         movw
9110 .globl start
                                                                                  9160
                                                                                                 %ax, %es
                                                                                                                         # -> ES: Extra Segment
                                                                                         movw
9111 start:
                                                                                  9161
                                                                                         movw
                                                                                                 %ax. %ss
                                                                                                                         # -> SS: Stack Segment
9112
                                                                                  9162
                                                                                                                         # Zero segments not ready for use
      cli
                                   # BIOS enabled interrupts; disable
                                                                                                 $0. %ax
                                                                                         movw
9113
                                                                                  9163
                                                                                                 %ax, %fs
                                                                                                                         # -> FS
                                                                                         movw
                                                                                                                         # -> GS
9114
      # Zero data segment registers DS. ES. and SS.
                                                                                  9164
                                                                                         movw
                                                                                                 %ax. %as
9115
               %ax.%ax
                                   # Set %ax to zero
                                                                                  9165
      xorw
9116
      movw
               %ax,%ds
                                   # -> Data Segment
                                                                                  9166
                                                                                         # Set up the stack pointer and call into C.
9117
      movw
              %ax.%es
                                   # -> Extra Segment
                                                                                  9167
                                                                                         mov1
                                                                                                 $start. %esp
9118
               %ax.%ss
                                   # -> Stack Segment
                                                                                  9168
                                                                                         call
                                                                                                 bootmain
      movw
9119
                                                                                  9169
9120
      # Physical address line A20 is tied to zero so that the first PCs
                                                                                  9170
                                                                                         # If bootmain returns (it shouldn't), trigger a Bochs
9121
      # with 2 MB would run software that assumed 1 MB. Undo that.
                                                                                  9171
                                                                                         # breakpoint if running under Bochs, then loop.
                                                                                                                         # 0x8a00 -> port 0x8a00
9122 seta20.1:
                                                                                  9172
                                                                                         movw
                                                                                                 $0x8a00, %ax
9123
      inb
               $0x64,%a1
                                       # Wait for not busy
                                                                                  9173
                                                                                         movw
                                                                                                 %ax, %dx
      testb
              $0x2.%al
                                                                                  9174
                                                                                                 %ax. %dx
9124
                                                                                         outw
9125
               seta20.1
                                                                                  9175
      jnz
                                                                                         movw
                                                                                                 $0x8ae0, %ax
                                                                                                                         # 0x8ae0 -> port 0x8a00
9126
                                                                                  9176
                                                                                         outw
                                                                                                 %ax, %dx
9127
      movb
               $0xd1,%a1
                                       # 0xd1 -> port 0x64
                                                                                  9177 spin:
9128
      outb
              %a1,$0x64
                                                                                         qmj
                                                                                  9178
                                                                                                 spin
9129
                                                                                  9179
9130 seta20.2:
                                                                                  9180 # Bootstrap GDT
9131
      inb
               $0x64,%a1
                                       # Wait for not busy
                                                                                  9181 .p2align 2
                                                                                                                                 # force 4 byte alignment
              $0x2.%al
9132
      testb
                                                                                  9182 gdt:
9133
      jnz
               seta20.2
                                                                                  9183 SEG_NULLASM
                                                                                                                                 # null seq
9134
                                                                                  9184
                                                                                         SEG_ASM(STA_X|STA_R, 0x0, 0xffffffff)
                                                                                                                                 # code seq
9135
               $0xdf.%al
                                                                                         SEG_ASM(STA_W, 0x0, 0xffffffff)
      movb
                                       # 0xdf -> port 0x60
                                                                                  9185
                                                                                                                                 # data seg
9136
      outb
              %al.$0x60
                                                                                  9186
9137
                                                                                  9187 gdtdesc:
9138
      # Switch from real to protected mode. Use a bootstrap GDT that makes
                                                                                  9188
                                                                                         .word
                                                                                                 (qdtdesc - qdt - 1)
                                                                                                                                 # sizeof(gdt) - 1
      # virtual addresses map directly to physical addresses so that the
                                                                                  9189
                                                                                         .long
                                                                                                                                 # address gdt
                                                                                                 qdt
9140
      # effective memory map doesn't change during the transition.
                                                                                  9190
9141
      ladt
               adtdesc
                                                                                  9191
9142
      mov1
               %cr0, %eax
                                                                                  9192
9143
      orl
               $CRO_PE, %eax
                                                                                  9193
9144
      mov1
               %eax, %cr0
                                                                                  9194
9145
                                                                                  9195
9146
                                                                                  9196
9147
                                                                                  9197
9148
                                                                                  9198
9149
                                                                                  9199
```

Sheet 91 Sheet 91

```
9200 // Boot loader.
                                                                                 9250 void
                                                                                 9251 waitdisk(void)
9201 //
9202 // Part of the boot block, along with bootasm.S, which calls bootmain().
                                                                                 9252 {
9203 // bootasm.S has put the processor into protected 32-bit mode.
                                                                                 9253 // Wait for disk ready.
9204 // bootmain() loads an ELF kernel image from the disk starting at
                                                                                 9254 while((inb(0x1F7) & 0xC0) != 0x40)
9205 // sector 1 and then jumps to the kernel entry routine.
                                                                                 9255
9206
                                                                                 9256 }
9207 #include "types.h"
                                                                                 9257
9208 #include "elf.h"
                                                                                 9258 // Read a single sector at offset into dst.
9209 #include "x86.h"
                                                                                 9259 void
9210 #include "memlayout.h"
                                                                                 9260 readsect(void *dst, uint offset)
9211
                                                                                 9261 {
9212 #define SECTSIZE 512
                                                                                 9262 // Issue command.
9213
                                                                                 9263
                                                                                       waitdisk();
9214 void readseg(uchar*, uint, uint);
                                                                                       outb(0x1F2, 1); // count = 1
9215
                                                                                 9265
                                                                                       outb(0x1F3, offset);
9216 void
                                                                                 9266 outb(0x1F4, offset >> 8);
9217 bootmain(void)
                                                                                 9267 outb(0x1F5, offset >> 16):
9218 {
                                                                                 9268
                                                                                       outb(0x1F6. (offset \gg 24) | 0xE0):
9219 struct elfhdr *elf;
                                                                                 9269
                                                                                       outb(0x1F7, 0x20); // cmd 0x20 - read sectors
9220 struct proghdr *ph. *eph:
                                                                                 9270
9221 void (*entry)(void);
                                                                                 9271 // Read data.
9222 uchar* pa;
                                                                                 9272 waitdisk();
9223
                                                                                 9273 insl(0x1F0, dst, SECTSIZE/4);
9224
      elf = (struct elfhdr*)0x10000; // scratch space
                                                                                 9274 }
9225
                                                                                 9275
9226
      // Read 1st page off disk
                                                                                 9276 // Read 'count' bytes at 'offset' from kernel into physical address 'pa'.
      readseg((uchar*)elf, 4096, 0);
9227
                                                                                 9277 // Might copy more than asked.
9228
                                                                                 9278 void
9229 // Is this an ELF executable?
                                                                                 9279 readseg(uchar* pa, uint count, uint offset)
9230 if(elf->magic != ELF_MAGIC)
                                                                                 9280 {
        return; // let bootasm.S handle error
9231
                                                                                 9281 uchar* epa;
9232
                                                                                 9282
9233 // Load each program segment (ignores ph flags).
                                                                                 9283
                                                                                       epa = pa + count;
9234
      ph = (struct proghdr*)((uchar*)elf + elf->phoff);
                                                                                 9284
9235
      eph = ph + elf -> phnum;
                                                                                 9285
                                                                                      // Round down to sector boundary.
9236
      for(; ph < eph; ph++){
                                                                                 9286
                                                                                       pa -= offset % SECTSIZE;
9237
        pa = (uchar*)ph->paddr;
                                                                                 9287
9238
        readseg(pa, ph->filesz, ph->off);
                                                                                 9288
                                                                                      // Translate from bytes to sectors; kernel starts at sector 1.
9239
        if(ph->memsz > ph->filesz)
                                                                                 9289
                                                                                       offset = (offset / SECTSIZE) + 1;
9240
           stosb(pa + ph->filesz, 0, ph->memsz - ph->filesz);
                                                                                 9290
9241 }
                                                                                 9291 // If this is too slow, we could read lots of sectors at a time.
9242
                                                                                 9292 // We'd write more to memory than asked, but it doesn't matter --
9243 // Call the entry point from the ELF header.
                                                                                 9293
                                                                                       // we load in increasing order.
9244 // Does not return!
                                                                                 9294
                                                                                        for(; pa < epa; pa += SECTSIZE, offset++)</pre>
9245 entry = (void(*)(void))(elf->entry);
                                                                                 9295
                                                                                         readsect(pa, offset);
9246 entry();
                                                                                 9296 }
9247 }
                                                                                 9297
9248
                                                                                 9298
9249
                                                                                 9299
```

Sheet 92 Sheet 92