

1.

```
.data
    f: .word
    g: .word  #待定义
    h: .word  #待定义
.text
    lw $t1,g
    lw $t2,h
    addi $t2,$t2,-5
    add $t1,$t1,$t2
    sw $t1,f
```

2.

```
.data
    A:.space  #待定义
    B:.space  #待定义
.text
    sub $t0,$s3,$s4
    sll $t0,$t0,2
    add $t1,$s6,$t0
    lw  $t2,0($t1)
    sw  $t2,32($s7)
```

3.

```
(1) for(int i=0;i<5;i++){
    for(int j=0;j<4-i;j++){
        if(Array[j]>Array[j+1]){
            int temp=Array[j];
            Array[j]=Array[j+1];
            Array[j+1]=temp;}
    }
}
```

(2)

```
.data
    Array: .space  #待定义
.text
    lw $t0,$s6
    lw $t1,(4)$s6
    lw $t3,(12)$s6
    lw $t4,(16)$s6

    sw $t4,$s6
    sw $t0,(4)$s6
    sw $t1,(12)$s6
```

sw \$t3,(16)\$s6

4. Type: R

Assembly language instruction: sub \$v1,\$v1,\$v0

Binary representation of instruction:00000000011000100001100000100010