

UXL Foundation Monthly Update May 2025



Upcoming UXL Events



UXL Foundation Mini Summit at OSS

Denver, Colorado

June 26, 2025 | 9:00am–12:30pm MDT



UXL Foundation Mini Summit

DATE June 26, 2025 TIME 9am–12:30pm MDT

Colorado Convention Center

Registration Cost: \$5

The Unified Acceleration (UXL) Foundation governs a set of C++ projects that can be used to accelerate a range of use cases, from math operations like SPARSE and BLAS, through to AI operations on CPUs, GPUs and other processors. Join this summit to find out how you can take advantage of the UXL Foundation libraries, and how you can get involved in both shaping the projects as well as making meaningful contributions.

AGENDA:

- Welcome Address (30 min)
- How the UXL Foundation Projects are Powering AI (30 min)
- Getting Started: Using & Contributing (30 min)
- Developing Stable Open Source Releases (30 min)
- High Performance for oneAPI Libraries (30 min)
- UXL Foundation Roadmap (30 min)
- Community Session (30 min)



Defining Open Standards Programming for AI and HPC

Hosted by the Unified Acceleration (UXL) Foundation

ISC High Performance 2025 | Hamburg, Germany

Wednesday, June 11, 2025 | 2:15 PM to 3:15 PM

GUEST SPEAKERS INCLUDING:



Rod Burns
Ecosystem,
Codeplay Software



ISC High Performance
The HPC Event.



UXL
Unified Acceleration Foundation

Working Groups + SIGs

Open Source Working Group

Notes available [in GitHub](#)

[Security processes and infrastructure](#) work package

- Projects continue to implement new processes
- OpenSSF scorecard will be measured and monitored

Project Best Practices Dashboard

- Initial measures presented to the Steering Committee
- Presentation planned for next Working Group meeting
- Aiming to have published information in August

[Public Infrastructure](#) work package

- Projects have published public CI infrastructure requirements
- Next steps to bring more resources to projects to meet these

UXL Project Public Roadmaps and Documentation

- Some projects now have public milestones
- Working towards greater visibility of planning

SIGs

Notes available [in GitHub](#)

Recordings in [openprofile.dev](#)

Language SIG Meeting

One of the engineers from the Blender project presented their work incorporating SYCL including the use of new extensions for image processing performance.

Math SIG Meeting

New Arm CI integration and an Intel presentation on kernel auto-tuning using MKLAPS.

Hardware SIG Meeting

Skyler from Micron presented their “Memory Lake” concept, how this can be used to improve performance for AI workloads and the types of architecture needed to do this with software.

Upcoming

See the [public calendar](#) for upcoming meetings and join the relevant [mailing lists](#) to receive invitations

- AI SIG meets during May
- Working Group meets as usual