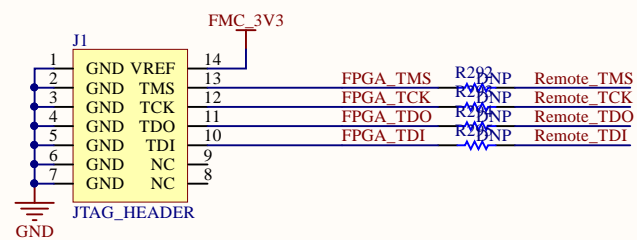
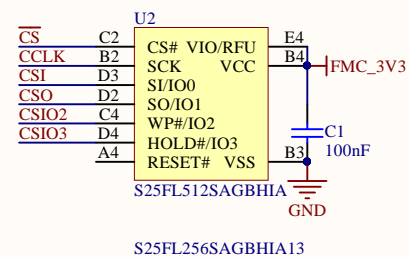
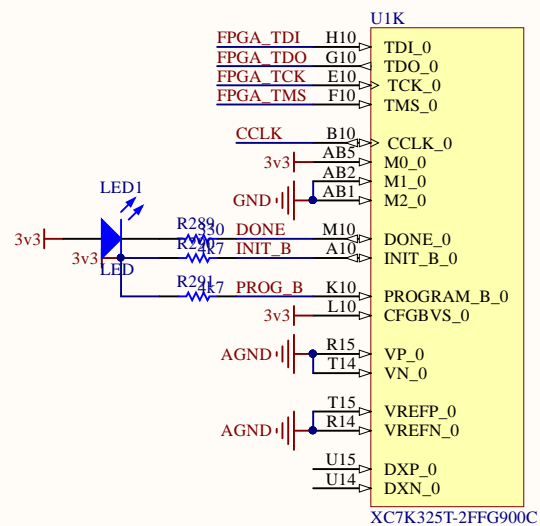
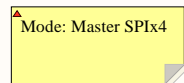


1		2		3		4												
A	1	DP1_M1_P DP1_M1_N	B16_10_P B16_10_N	19	DP1_C0_P DP1_C0_N	B18_17_P B18_17_N	39	HA1_01_P HA1_01_N	B18_6_P B18_6_N	1	DP2_M1_P DP2_M1_N	B13_10_P B13_10_N	19	DP2_C0_P DP2_C0_N	B13_8_P B13_8_N	39	HA2_01_P HA2_01_N	B12_1_P B12_1_N
	2	DP1_M2_P DP1_M2_N	B16_9_N B16_9_P	20	DP1_M0_P DP1_M0_N	B18_24_P B18_24_N	40	HA1_05_P HA1_05_N	B18_3_P B18_3_N	2	DP2_M2_P DP2_M2_N	B13_18_P B13_18_N	20	DP2_M0_P DP2_M0_N	B13_16_P B13_16_N	40	HA2_05_P HA2_05_N	B12_4_N B12_4_P
	3	DP1_M3_P DP1_M3_N	B16_18_P B16_18_N	21	LA1_06_P LA1_06_N	B18_9_P B18_9_N	41	HA1_09_P HA1_09_N	B18_5_P B18_5_P	3	DP2_M3_P DP2_M3_N	B12_2_P B12_2_N	21	LA2_06_P LA2_06_N	B12_24_N B12_24_P	41	HA2_09_P HA2_09_N	B12_9_P B12_9_N
	4	DP1_M4_P DP1_M4_N	B16_22_P B16_22_P	22	LA1_10_P LA1_10_N	B17_22_N B17_22_P	42	HA1_13_P HA1_13_N	B18_1_P B18_1_N	4	DP2_M4_P DP2_M4_N	B13_15_N B13_15_P	22	LA2_10_P LA2_10_N	B32_23_P B32_23_N	42	HA2_13_P HA2_13_N	B12_18_P B12_18_N
	5	DP1_M5_P DP1_M5_N	B16_24_N B16_24_P	23	LA1_14_P LA1_14_N	B16_1_P B16_1_N	43	HA1_16_P HA1_16_N	B17_1_P B17_1_N	5	DP2_M5_P DP2_M5_N	B33_1_P B33_1_N	23	LA2_14_P LA2_14_N	B33_6_P B33_6_N	43	HA2_16_P HA2_16_N	B12_10_P B12_10_N
	6	DP1_C1_P DP1_C1_N	B17_5_P B17_5_N	24	LA1_18_P LA1_18_N	B15_5_P B15_5_N	44	HA1_20_P HA1_20_N	B17_6_P B17_6_N	6	DP2_C1_P DP2_C1_N	B33_7_N B33_7_P	24	LA2_18_P LA2_18_N	B33_3_N B33_3_P	44	HA2_20_P HA2_20_N	B32_10_P B32_10_N
	7	DP1_C2_P DP1_C2_N	B15_15_P B15_15_N	25	LA1_27_P LA1_27_N	B15_24_P B15_24_N	45	HB1_03_P HB1_03_N	B17_7_P B17_7_N	7	DP2_C2_P DP2_C2_N	B13_24_P B13_24_N	25	LA2_27_P LA2_27_N	B34_4_P B34_4_N	45	HB2_03_P HB2_03_N	B32_18_P B32_18_N
	8	DP1_C3_P DP1_C3_N	B15_17_P B15_17_N	26	LA1_28_P LA1_28_N	B15_7_N B15_7_P	46	HB1_05_P HB1_05_N	B15_1_P B15_1_N	8	DP2_C3_P DP2_C3_N	B34_7_P B34_7_N	26	LA2_28_P LA2_28_N	B33_19_P B33_19_N	46	HB2_05_P HB2_05_N	B32_21_P B32_21_N
	9	DP1_CCLK1_P DP1_CCLK1_N	B18_15_P B18_15_N	27	LA1_30_P LA1_30_N	B15_12_N B15_12_P	47	HB1_09_P HB1_09_N	B15_10_N B15_10_P	9	DP2_CCLK1_P DP2_CCLK1_N	B13_5_P B13_5_N	27	LA2_30_P LA2_30_N	B33_14_P B33_14_N	47	HB2_09_P HB2_09_N	B32_22_N B32_22_P
	10	DP1_M9_P DP1_M9_N	B16_7_N B16_7_P	28	LA1_32_P LA1_32_N	B15_23_N B15_23_P	48	HB1_13_P HB1_13_N	B15_2_N B15_2_P	10	DP2_M9_P DP2_M9_N	B13_11_P B13_11_N	28	LA2_32_P LA2_32_N	B34_11_P B34_11_N	48	HB2_13_P HB2_13_N	B33_11_N B33_11_P
B	11	DP1_M8_P DP1_M8_N	B16_17_N B16_17_P	29	DP1_GTCLK0_P DP1_GTCLK0_N	B18_11_P B18_11_N				11	DP2_M8_P DP2_M8_N	B12_13_N B12_13_P	29	DP2_GTCLK0_P DP2_GTCLK0_N	B13_7_P B13_7_N			
	12	DP1_M7_P DP1_M7_N	B17_16_P B17_16_N	30	LA1_01_P LA1_01_N	B18_19_P B18_19_N				12	DP2_M7_P DP2_M7_N	B32_17_N B32_17_P	30	LA2_01_P LA2_01_N	B12_17_N B12_17_P			
	13	DP1_M6_P DP1_M6_N	B17_21_P B17_21_N	31	LA1_05_P LA1_05_N	B17_20_P B17_20_N				13	DP2_M6_P DP2_M6_N	B32_24_P B32_24_N	31	LA2_05_P LA2_05_N	B12_6_N B12_6_P			
	14	DP1_GTCLK1_P DP1_GTCLK1_N	B15_9_N B15_9_P	32	LA1_09_P LA1_09_N	B17_19_P B17_19_N				14	DP2_GTCLK1_P DP2_GTCLK1_N	B33_5_P B33_5_N	32	LA2_09_P LA2_09_N	B32_16_N B32_16_P			
	15	DP1_C9_P DP1_C9_N	B15_4_N B15_4_P	33	LA1_13_P LA1_13_N	B17_23_P B17_23_N				15	DP2_C9_P DP2_C9_N	B34_20_N B34_20_P	33	LA2_13_P LA2_13_N	B32_20_N B32_20_P			
	16	DP1_C8_P DP1_C8_N	B15_21_N B15_21_P	34	LA1_17_P LA1_17_N	B16_8_P B16_8_N				16	DP2_C8_P DP2_C8_N	B34_1_P B34_1_N	34	LA2_17_P LA2_17_N	B33_9_P B33_9_N			
	17	DP1_C7_P DP1_C7_N	B15_19_N B15_19_P	35	LA1_23_P LA1_23_N	B15_6_N B15_6_P				17	DP2_C7_P DP2_C7_N	B34_3_P B34_3_N	35	LA2_23_P LA2_23_N	B33_8_N B33_8_P			
	18	DP1_C6_P DP1_C6_N	B15_22_N B15_22_P	36	LA1_26_P LA1_26_N	B15_20_P B15_20_N				18	DP2_C6_P DP2_C6_N	B34_6_P B34_6_N	36	LA2_26_P LA2_26_N	B34_5_N B34_5_P			
				37	LA1_29_P LA1_29_N	B16_6_N B16_6_P							37	LA2_29_P LA2_29_N	B32_19_P B32_19_N			
				38	LA1_33_P LA1_33_N	B15_14_N B15_14_P							38	LA2_33_P LA2_33_N	B34_19_P B34_19_N			
C		DP1_C4_P DP1_C4_N	SFP2_RX_P SFP2_RX_N								DP2_C4_P DP2_C4_N	SFP3_RX_P SFP3_RX_N						
		DP1_C5_P DP1_C5_N	SFP2_TX_P SFP2_TX_N								DP2_C5_P DP2_C5_N	SFP3_TX_P SFP3_TX_N						
D		HB1_06_P HB1_06_N									HB2_06_P HB2_06_N							
		HB1_10_P HB1_10_N									HB2_10_P HB2_10_N							
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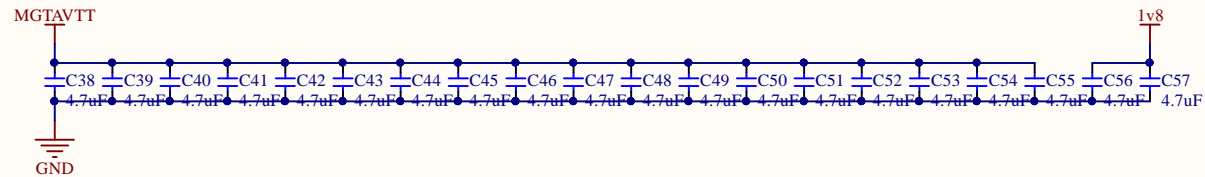
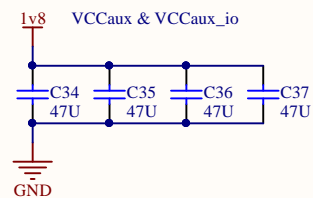
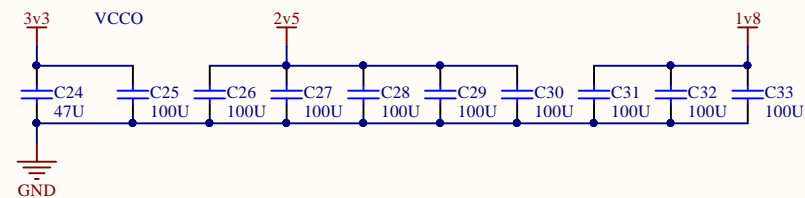
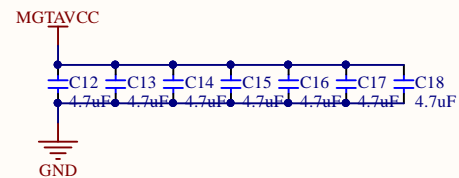
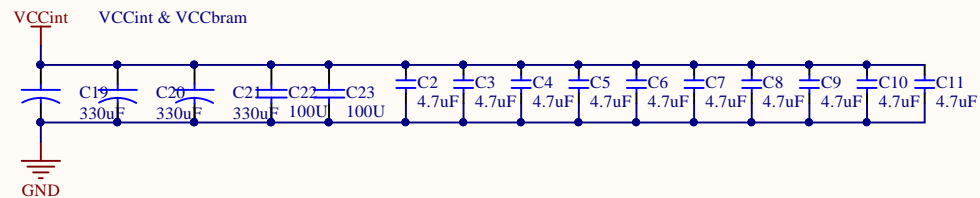
Title			FPGA configure		
Size	Number				Revision
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File:	E:\ADproject\...\FPGA_config.SchDoc			Drawn By:	

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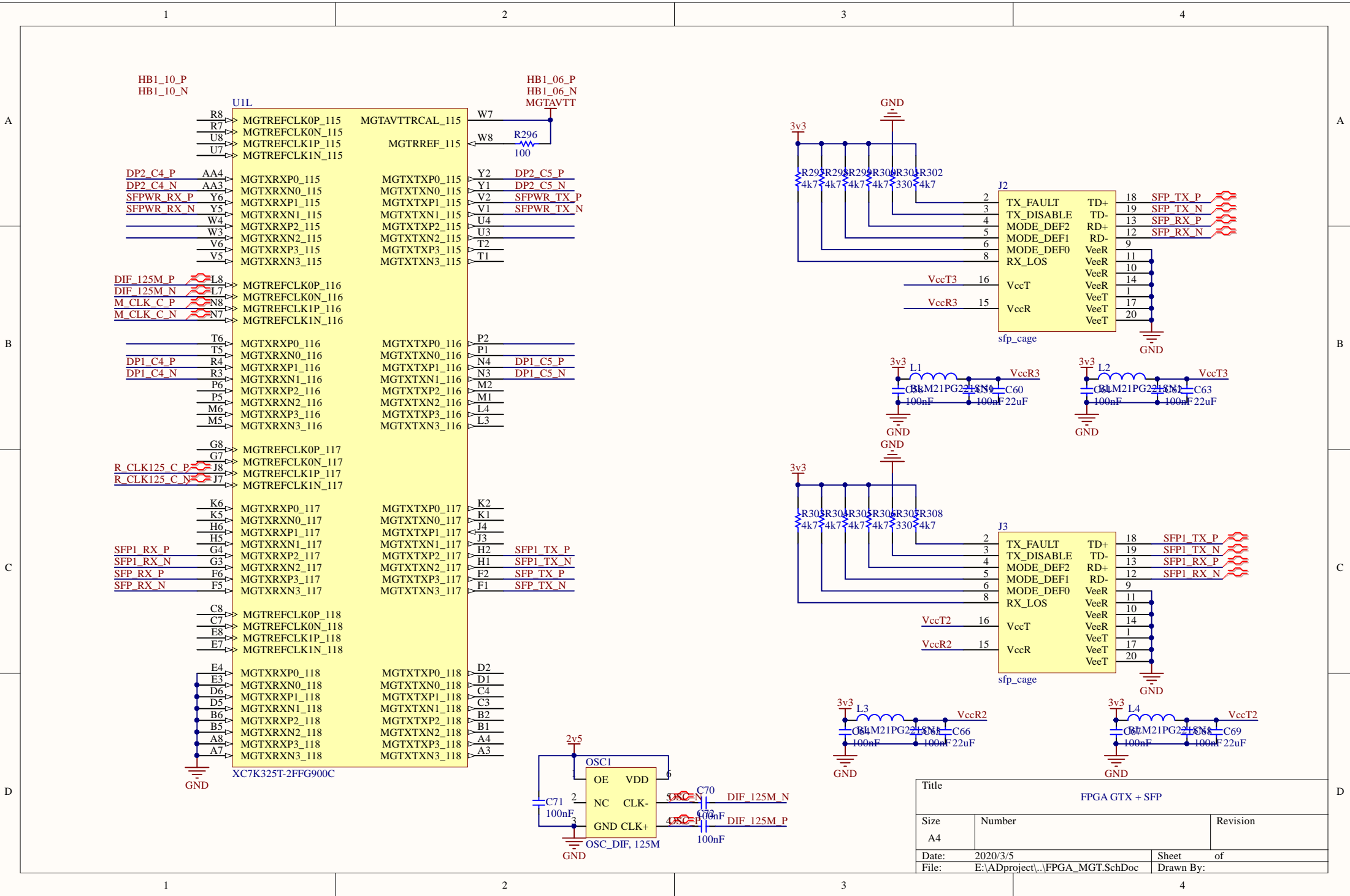
Title		
Decoupling capacitors		
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A4		
Date:	2020/3/5	Sheet of
File:	E:\ADproject\...\FPGA_de_cap.SchDoc	Drawn By:

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Title		
FPGA GTX + SFP		
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File:	E:\ADproject\...\FPGA_MGT.SchDoc	Drawn By:

▲XC7K325T bank12 to bank18 are HR banks.
I/O voltage range from 1.2V to 3.3V.

U1A

BANK 12

IO_0_12	Y20	SFPWR_MOD0
IO_L1P_T0_12	Y23	B12_1 P
IO_L1N_T0_12	Y24	B12_1 N
IO_L2P_T0_12	Y21	B12_2 P
IO_L2N_T0_12	AA21	B12_2 N
IO_L3P_T0_DQS_12	AB22	B12_3 P
IO_L3N_T0_DQS_12	AB23	B12_3 N
IO_L4P_T0_12	AA22	B12_4 P
IO_L4N_T0_12	AA23	B12_4 N
IO_L5P_T0_12	AC20	B12_5 P
IO_L5N_T0_12	AC21	B12_5 N
IO_L6P_T0_12	AA20	B12_6 P
IO_L6N_T0_VREF_12	AB20	B12_6 N
IO_L7P_T1_12	AB24	B12_7 P
IO_L7N_T1_12	AC25	B12_7 N
IO_L8P_T1_12	AC22	B12_8 P
IO_L8N_T1_12	AD22	B12_8 N
IO_L9P_T1_DQS_12	AC24	B12_9 P
IO_L9N_T1_DQS_12	AD24	B12_9 N
IO_L10P_T1_12	AD21	B12_10 P
IO_L10N_T1_12	AE21	B12_10 N
IO_L11P_T1_SRCC_12	AE23	B12_11 P
IO_L11N_T1_SRCC_12	AF23	B12_11 N
IO_L12P_T1_MRCC_12	AD23	B12_12 P
IO_L12N_T1_MRCC_12	AE24	B12_12 N
IO_L13P_T2_MRCC_12	AF22	B12_13 P
IO_L13N_T2_MRCC_12	AG23	B12_13 N
IO_L14P_T2_SRCC_12	AG24	B12_14 P
IO_L14N_T2_SRCC_12	AH24	B12_14 N
IO_L15P_T2_DQS_12	AJ24	SFPWR_TX_FAULT
IO_L15N_T2_DQS_12	AK25	SFPWR_TX_DIS
IO_L16P_T2_12	AE25	B12_16 P
IO_L16N_T2_12	AF25	B12_16 N
IO_L17P_T2_12	AK23	B12_17 P
IO_L17N_T2_12	AK24	B12_17 N
IO_L18P_T2_12	AG25	B12_18 P
IO_L18N_T2_12	AH25	B12_18 N
IO_L19P_T3_12	AF20	B12_19 P
IO_L19N_T3_VREF_12	AF21	B12_19 N
IO_L20P_T3_12	AG22	B12_20 P
IO_L20N_T3_12	AH22	B12_20 N
IO_L21P_T3_DQS_12	AJ22	B12_21 P
IO_L21N_T3_DQS_12	AJ23	B12_21 N
IO_L22P_T3_12	AG20	B12_22 P
IO_L22N_T3_12	AH20	B12_22 N
IO_L23P_T3_12	AH21	B12_23 P
IO_L23N_T3_12	AJ21	B12_23 N
IO_L24P_T3_12	AK20	B12_24 P
IO_L24N_T3_12	AK21	B12_24 N
IO_25_12	AE20	SFPWR_MOD1

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U1B

BANK 13

IO_0_13	Y25	
IO_L1P_T0_13	Y26	B13_1 P
IO_L1N_T0_13	AA26	B13_1 N
IO_L2P_T0_13	W27	B13_2 P
IO_L2N_T0_13	W28	B13_2 N
IO_L3P_T0_DQS_13	Y28	B13_3 P
IO_L3N_T0_DQS_13	AA28	B13_3 N
IO_L4P_T0_13	W29	B13_4 P
IO_L4N_T0_13	Y29	B13_4 N
IO_L5P_T0_13	AA27	B13_5 P
IO_L5N_T0_13	AB28	B13_5 N
IO_L6P_T0_13	AA25	B13_6 P
IO_L6N_T0_VREF_13	AB25	B13_6 N
IO_L7P_T1_13	AC29	B13_7 P
IO_L7N_T1_13	AC30	B13_7 N
IO_L8P_T1_13	Y30	B13_8 P
IO_L8N_T1_13	AA30	B13_8 N
IO_L9P_T1_DQS_13	AD29	B13_9 P
IO_L9N_T1_DQS_13	AE29	B13_9 N
IO_L10P_T1_13	AB29	B13_10 P
IO_L10N_T1_13	AB30	B13_10 N
IO_L11P_T1_SRCC_13	AD27	B13_11 P
IO_L11N_T1_SRCC_13	AD28	B13_11 N
IO_L12P_T1_MRCC_13	AB27	B13_12 P
IO_L12N_T1_MRCC_13	AC27	B13_12 N
IO_L13P_T2_MRCC_13	AG29	B13_13 P
IO_L13N_T2_MRCC_13	AH29	B13_13 N
IO_L14P_T2_SRCC_13	AE28	B13_14 P
IO_L14N_T2_SRCC_13	AF28	B13_14 N
IO_L15P_T2_DQS_13	AK29	B13_15 P
IO_L15N_T2_DQS_13	AK30	B13_15 N
IO_L16P_T2_13	AE30	B13_16 P
IO_L16N_T2_13	AF30	B13_16 N
IO_L17P_T2_13	AJ28	B13_17 P
IO_L17N_T2_13	AG30	B13_18 P
IO_L18P_T2_13	AH30	B13_18 N
IO_L18N_T2_13	AC26	B13_19 P
IO_L19P_T3_13	AD26	B13_19 N
IO_L19N_T3_VREF_13	AJ27	B13_20 P
IO_L20P_T3_13	AK28	B13_20 N
IO_L20N_T3_13	AG27	B13_21 P
IO_L21P_T3_DQS_13	AG28	B13_21 N
IO_L21N_T3_DQS_13	AH26	B13_22 P
IO_L22P_T3_13	AH27	B13_22 N
IO_L22N_T3_13	AF26	B13_23 P
IO_L23P_T3_13	AF27	B13_23 N
IO_L23N_T3_13	AJ26	B13_24 P
IO_L24P_T3_13	AK26	B13_24 N
IO_L24N_T3_13	AE26	SFPWR_MOD2
IO_25_13		

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U1C

BANK 14

IO_0_14	R19	B14_0
IO_L1P_T0_D00_MOSI_14	P24	CSI
IO_L1N_T0_D01_DIN_14	R25	CSO
IO_L2P_T0_D02_14	R20	CSIO2
IO_L2N_T0_D03_14	R21	CSIO3
IO_L3P_T0_DQS_PUDC_B_14	R23	GND
IO_L3N_T0_DQS_EMCCLK_14	R24	B14_3 N
IO_L4P_T0_D04_14	T20	B14_4 P
IO_L4N_T0_D05_14	T21	B14_4 N
IO_L5P_T0_D06_14	T22	B14_5 P
IO_L5N_T0_D07_14	T23	B14_5 N
IO_L6P_T0_FCS_B_14	U19	CS
IO_L6N_T0_D08_VREF_14	U20	B14_6 N
IO_L7P_T1_D09_14	P29	B14_7 P
IO_L7N_T1_D10_14	R29	B14_7 N
IO_L8P_T1_D11_14	P27	B14_8 P
IO_L8N_T1_D12_14	P28	B14_8 N
IO_L9P_T1_DQS_14	R30	B14_9 P
IO_L9N_T1_DQS_D13_14	T30	B14_9 N
IO_L10P_T1_D14_14	P26	B14_10 P
IO_L10N_T1_D15_14	R26	B14_10 N
IO_L11P_T1_SRCC_14	R28	B14_11 P
IO_L11N_T1_SRCC_14	T28	B14_11 N
IO_L12P_T1_MRCC_14	T26	B14_12 P
IO_L12N_T1_MRCC_14	T27	B14_12 N
IO_L13P_T2_MRCC_14	U27	B14_13 P
IO_L13N_T2_MRCC_14	U28	B14_13 N
IO_L14P_T2_SRCC_14	T25	B14_14 P
IO_L14N_T2_SRCC_14	U25	B14_14 N
IO_L15P_T2_DQS_RDWR_B_14	U29	B14_15 P
IO_L15N_T2_DQS_DOUT_CSO_B_14	U30	B14_15 N
IO_L16P_T2_CSI_B_14	V26	B14_16 P
IO_L16N_T2_A15_D31_14	V27	B14_16 N
IO_L17P_T2_A14_D30_14	V29	B14_17 P
IO_L17N_T2_A13_D29_14	V30	B14_17 N
IO_L18P_T2_A12_D28_14	V25	B14_18 P
IO_L18N_T2_A11_D27_14	W26	B14_18 N
IO_L19P_T3_A10_D26_14	V19	B14_19 P
IO_L19N_T3_A09_D25_VREF_14	V20	B14_19 N
IO_L20P_T3_A08_D24_14	W23	B14_20 P
IO_L20N_T3_A07_D23_14	W24	B14_20 N
IO_L21P_T3_DQS_14	U22	B14_21 P
IO_L21N_T3_DQS_A06_D22_14	U23	B14_21 N
IO_L22P_T3_A05_D21_14	V21	B14_22 P
IO_L22N_T3_A04_D20_14	V22	B14_22 N
IO_L23P_T3_A03_D19_14	U24	B14_23 P
IO_L23N_T3_A02_D18_14	V24	B14_23 N
IO_L24P_T3_A01_D17_14	W21	B14_24 P
IO_L24N_T3_A00_D16_14	W22	B14_24 N
IO_25_14	W19	SFPWR_LOS

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U1D

BANK 15

IO_0_15	M19	B15_1 P
IO_L1P_T0_ADOP_15	J23	B15_1 N
IO_L1N_T0_ADON_15	J24	B15_2 P
IO_L2P_T0_AD8P_15	L22	B15_2 N
IO_L2N_T0_AD8N_15	L23	B15_3 P
IO_L3P_T0_DQS_AD1P_15	K23	B15_3 N
IO_L3N_T0_DQS_AD1N_15	K24	B15_4 P
IO_L4P_T0_AD9P_15	L21	B15_4 N
IO_L4N_T0_AD9N_15	K21	B15_5 P
IO_L5P_T0_AD2P_15	J21	B15_5 N
IO_L5N_T0_AD2N_15	J22	B15_6 P
IO_L6P_T0_15	M20	B15_6 N
IO_L6N_T0_VREF_15	L20	B15_7 P
IO_L7P_T1_AD10P_15	J29	B15_7 N
IO_L7N_T1_AD10N_15	H29	B15_8 P
IO_L8P_T1_AD3P_15	J27	B15_8 N
IO_L8N_T1_AD3N_15	J28	B15_9 P
IO_L9P_T1_DQS_AD11P_15	L30	B15_9 N
IO_L9N_T1_DQS_AD11N_15	K30	B15_10 P
IO_L10P_T1_AD4P_15	K26	B15_10 N
IO_L10N_T1_AD4N_15	L26	B15_11 P
IO_L11P_T1_SRCC_AD12P_15	L27	B15_11 N
IO_L11N_T1_SRCC_AD12N_15	L25	B15_12 P
IO_L12P_T1_MRCC_AD5P_15	K25	B15_12 N
IO_L12N_T1_MRCC_AD5N_15	K28	B15_13 P
IO_L13P_T2_MRCC_15	K29	B15_13 N
IO_L13N_T2_MRCC_15	M28	B15_14 P
IO_L14P_T2_SRCC_15	L28	B15_14 N
IO_L14N_T2_SRCC_15	M29	B15_15 P
IO_L15P_T2_DQS_15	M30	B15_15 N
IO_L15N_T2_DQS_ADV_B_15	N27	B15_16 P
IO_L16P_T2_A28_15	M27	B15_16 N
IO_L16N_T2_A27_15	N29	B15_17 P
IO_L17P_T2_A26_15	N30	B15_17 N
IO_L17N_T2_A25_15	N25	B15_18 P
IO_L18P_T2_A24_15	N26	B15_18 N
IO_L18N_T2_A23_15	N19	B15_19 P
IO_L19P_T3_A22_15	N20	B15_19 N
IO_L19N_T3_A21_VREF_15	N21	B15_20 P
IO_L20P_T3_A20_15	N22	B15_20 N
IO_L20N_T3_A19_15	P23	B15_21 P
IO_L21P_T3_DQS_15	N24	B15_21 N
IO_L21N_T3_DQS_A18_15	P21	B15_22 P
IO_L22P_T3_A17_15	P22	B15_22 N
IO_L22N_T3_A16_15	M24	B15_23 P
IO_L23P_T3_FOE_B_15	M25	B15_23 N
IO_L23N_T3_FWE_B_15	M22	B15_24 P
IO_L24P_T3_RS1_15	M23	B15_24 N
IO_L24N_T3_RS0_15	P19	
IO_25_15		

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Title			
FPGA bank 12 - 15			
Size	Number		Revision
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▲ XC7K325T bank12 to bank18 are HR banks.
I/O voltage range from 1.2V to 3.3V.

U1E

BANK 16

IO_0_16	F23	B16_1_P
IO_L1P_T0_16	B23	B16_1_N
IO_L1N_T0_16	A23	B16_2_P
IO_L2P_T0_16	E23	B16_2_N
IO_L2N_T0_16	D23	B16_3_P
IO_L3P_T0_DQS_16	F25	B16_3_N
IO_L3N_T0_DQS_16	E25	B16_4_P
IO_L4P_T0_16	E24	B16_4_N
IO_L4N_T0_16	D24	B16_5_P
IO_L5P_T0_16	F26	B16_5_N
IO_L5N_T0_16	E26	B16_6_P
IO_L6P_T0_16	G23	B16_6_N
IO_L6N_T0_VREF_16	G24	B16_7_P
IO_L7P_T1_16	B27	B16_7_N
IO_L7N_T1_16	A27	B16_8_P
IO_L8P_T1_16	C24	B16_8_N
IO_L8N_T1_16	B24	B16_9_P
IO_L9P_T1_DQS_16	B28	B16_9_N
IO_L9N_T1_DQS_16	A28	B16_10_P
IO_L10P_T1_16	A25	B16_10_N
IO_L10N_T1_16	A26	B16_11_P
IO_L11P_T1_SRCC_16	D26	B16_11_N
IO_L11N_T1_SRCC_16	C26	B16_12_P
IO_L12P_T1_MRCC_16	C25	B16_12_N
IO_L12N_T1_MRCC_16	B25	B16_13_P
IO_L13P_T2_MRCC_16	D27	B16_13_N
IO_L13N_T2_MRCC_16	C27	B16_14_P
IO_L14P_T2_SRCC_16	E28	B16_14_N
IO_L14N_T2_SRCC_16	C29	B16_15_P
IO_L15P_T2_DQS_16	B29	B16_15_N
IO_L15N_T2_DQS_16	D29	B16_16_P
IO_L16P_T2_16	C30	B16_16_N
IO_L16N_T2_16	B30	B16_17_P
IO_L17P_T2_16	A30	B16_17_N
IO_L17N_T2_16	E29	B16_18_P
IO_L18P_T2_16	E30	B16_18_N
IO_L18N_T2_16	H24	B16_19_P
IO_L19P_T3_16	H25	B16_19_N
IO_L19N_T3_VREF_16	G28	B16_20_P
IO_L20P_T3_16	F28	B16_20_N
IO_L20N_T3_16	G27	B16_21_P
IO_L21P_T3_DQS_16	F27	B16_21_N
IO_L21N_T3_DQS_16	G29	B16_22_P
IO_L22P_T3_16	F30	B16_22_N
IO_L22N_T3_16	H26	B16_23_P
IO_L23P_T3_16	H27	B16_23_N
IO_L23N_T3_16	H30	B16_24_P
IO_L24P_T3_16	G30	B16_24_N
IO_L24N_T3_16	G25	
IO_25_16		

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U1F

BANK 17

IO_0_17	G19	B17_1_P
IO_L1P_T0_17	K18	B17_1_N
IO_L1N_T0_17	L18	B17_2_P
IO_L2P_T0_17	H20	B17_2_N
IO_L2N_T0_17	G20	B17_3_P
IO_L3P_T0_DQS_17	L17	B17_3_N
IO_L3N_T0_DQS_17	H17	B17_4_P
IO_L4P_T0_17	L19	B17_4_N
IO_L4N_T0_17	L17	B17_5_P
IO_L5P_T0_17	L18	B17_5_N
IO_L5N_T0_17	K19	B17_6_P
IO_L6P_T0_17	K20	B17_6_N
IO_L6N_T0_VREF_17	H21	B17_7_P
IO_L7P_T1_17	H22	B17_7_N
IO_L7N_T1_17	D21	B17_8_P
IO_L8P_T1_17	C21	B17_8_N
IO_L8N_T1_17	G22	B17_9_P
IO_L9P_T1_DQS_17	F22	B17_9_N
IO_L9N_T1_DQS_17	D22	B17_10_P
IO_L10P_T1_17	C22	B17_10_N
IO_L10N_T1_17	F21	B17_11_P
IO_L11P_T1_SRCC_17	E21	B17_11_N
IO_L11N_T1_SRCC_17	F20	B17_12_P
IO_L12P_T1_MRCC_17	E20	B17_12_N
IO_L12N_T1_MRCC_17	D17	B17_13_P
IO_L13P_T2_MRCC_17	D18	B17_13_N
IO_L13N_T2_MRCC_17	E19	B17_14_P
IO_L14P_T2_SRCC_17	D19	B17_14_N
IO_L14N_T2_SRCC_17	D16	B17_15_P
IO_L15P_T2_DQS_17	C16	B17_15_N
IO_L15N_T2_DQS_17	G18	B17_16_P
IO_L16P_T2_17	F18	B17_16_N
IO_L16N_T2_17	C17	B17_17_P
IO_L17P_T2_17	B17	B17_17_N
IO_L17N_T2_17	G17	B17_18_P
IO_L18P_T2_17	F17	B17_18_N
IO_L18N_T2_17	C20	B17_19_P
IO_L19P_T3_17	B20	B17_19_N
IO_L19N_T3_VREF_17	A16	B17_20_P
IO_L20P_T3_17	A17	B17_20_N
IO_L20N_T3_17	A20	B17_21_P
IO_L21P_T3_DQS_17	A21	B17_21_N
IO_L21N_T3_DQS_17	B18	B17_22_P
IO_L22P_T3_17	A18	B17_22_N
IO_L22N_T3_17	B22	B17_23_P
IO_L23P_T3_17	A22	B17_23_N
IO_L23N_T3_17	C19	B17_24_P
IO_L24P_T3_17	B19	B17_24_N
IO_L24N_T3_17	E18	
IO_25_17		

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U1G

BANK 18

IO_0_18	G12	B18_1_P
IO_L1P_T0_18	L16	B18_1_N
IO_L1N_T0_18	K16	B18_2_P
IO_L2P_T0_18	L15	B18_2_N
IO_L2N_T0_18	K15	B18_3_P
IO_L3P_T0_DQS_18	L12	B18_3_N
IO_L3N_T0_DQS_18	L13	B18_4_P
IO_L4P_T0_18	K13	B18_4_N
IO_L4N_T0_18	L13	B18_5_P
IO_L5P_T0_18	K14	B18_5_N
IO_L5N_T0_18	L14	B18_6_P
IO_L6P_T0_18	L11	B18_6_N
IO_L6N_T0_VREF_18	K11	B18_7_P
IO_L7P_T1_18	H15	B18_7_N
IO_L7N_T1_18	G15	B18_8_P R_TOD_O_P
IO_L8P_T1_18	L11	B18_8_P R_TOD_O_N
IO_L8N_T1_18	L12	B18_9_P
IO_L9P_T1_DQS_18	L16	B18_9_N
IO_L9N_T1_DQS_18	H16	B18_10_P
IO_L10P_T1_18	H11	B18_10_N
IO_L10N_T1_18	H12	B18_11_P
IO_L11P_T1_SRCC_18	H14	B18_11_N
IO_L11N_T1_SRCC_18	G14	B18_12_P
IO_L12P_T1_MRCC_18	G13	B18_12_N
IO_L12N_T1_MRCC_18	F13	B18_13_P
IO_L13P_T2_MRCC_18	D12	B18_13_N
IO_L13N_T2_MRCC_18	D13	B18_14_P
IO_L14P_T2_SRCC_18	F12	B18_14_N
IO_L14N_T2_SRCC_18	E13	B18_15_P
IO_L15P_T2_DQS_18	C12	B18_15_N
IO_L15N_T2_DQS_18	B12	B18_16_P
IO_L16P_T2_18	F11	B18_16_N
IO_L16N_T2_18	E11	B18_17_P
IO_L17P_T2_18	A11	B18_17_N
IO_L17N_T2_18	A12	B18_18_P
IO_L18P_T2_18	D11	B18_18_N
IO_L18N_T2_18	C11	B18_19_P
IO_L19P_T3_18	F15	B18_19_N
IO_L19N_T3_VREF_18	E16	B18_20_P
IO_L20P_T3_18	E14	B18_20_N
IO_L20N_T3_18	E15	B18_21_P
IO_L21P_T3_DQS_18	D14	B18_21_N
IO_L21N_T3_DQS_18	C14	B18_22_P
IO_L22P_T3_18	B13	B18_22_N
IO_L22N_T3_18	A13	B18_23_P
IO_L23P_T3_18	C15	B18_23_N
IO_L23N_T3_18	B15	B18_24_P
IO_L24P_T3_18	B14	B18_24_N
IO_L24N_T3_18	A15	
IO_25_18	F16	

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Title			FPGA bank 16 - 18	
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▲ XC7K325T bank32 to bank34 are HP banks.
I/O voltage range from 1.2V to 1.8V.

U1H

BANK 32

IO_0_VRN_32 Y14
AK16 B32_1_P
AK15 B32_1_N
AG15 B32_2_P
AH15 B32_2_N
AH16 B32_3_P
AJ16 B32_3_N
AF15 B32_4_P
AG14 B32_4_N
AH17 B32_5_P
AJ17 B32_5_N
AE16 B32_6_P
AF16 B32_6_N
AJ19 B32_7_P
AK19 B32_7_N
AG19 B32_8_P
AH19 B32_8_N
AJ18 B32_9_P
AK18 B32_9_N TPI
AD19 B32_10_P
AE19 B32_10_N
AF18 B32_11_P
AG18 B32_11_N
AF17 B32_12_P
AG17 B32_12_N
AD18 B32_13_P
AE18 B32_13_N
AD17 B32_14_P
AD16 B32_14_N
Y19 B32_15_P
Y18 B32_15_N
AA18 B32_16_P
AB18 B32_16_N
AC19 B32_17_P
AB19 B32_17_N
AB17 B32_18_P
AC17 B32_18_N
AE15 B32_19_P
AE14 B32_19_N
AA15 B32_20_P
AB15 B32_20_N
AC16 B32_21_P
AC15 B32_21_N
AC14 B32_22_P
AD14 B32_22_N
AA17 B32_23_P
AA16 B32_23_N
Y16 B32_24_P
Y15 B32_24_N
AB14

U1I

BANK 33

IO_0_VRN_33 Y13
AA12 B33_1_P
AB12 B33_1_N
AA8 B33_2_P
AB8 B33_2_N
AB9 B33_3_P
AC9 B33_3_N
Y11 B33_4_P
Y10 B33_4_N
AA11 B33_5_P
AA10 B33_5_N
AA13 B33_6_P
AB13 B33_6_N
AB10 B33_7_P
AC10 B33_7_N
AD8 B33_8_P
AE8 B33_8_N
AC12 B33_9_P
AC11 B33_9_N
AD9 B33_10_P
AE9 B33_10_N
AE11 B33_11_P
AF11 B33_11_N
AD12 B33_12_P
AD11 B33_12_N
AG10 B33_13_P
AH10 B33_13_N
AE10 B33_14_P
AF10 B33_14_N
AJ9 B33_15_P
AK9 B33_15_N
AG9 B33_16_P
AH9 B33_16_N
AK11 B33_17_P
AK10 B33_17_N
AH11 B33_18_P
AJ11 B33_18_N
AE13 B33_19_P
AF13 B33_19_N
AK14 B33_20_P
AK13 B33_20_N
AH14 B33_21_P
AJ14 B33_21_N
AJ13 B33_22_P
AJ12 B33_22_N
AF12 B33_23_P
AG12 B33_23_N
AG13 B33_24_P
AH12 B33_24_N
AD13

U1J

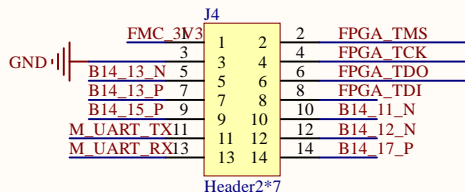
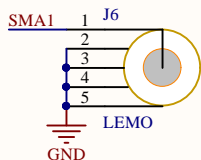
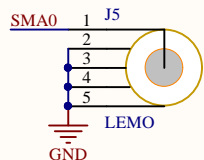
BANK 34

IO_0_VRN_34 AC6
AD4 B34_1_P
AD3 B34_1_N
IO_L1N_T0_34 AD3
IO_L2P_T0_34 AC2
IO_L2N_T0_34 AC1
AD2 B34_3_P
AD1 B34_3_N
IO_L3P_T0_DQS_34 AC5
IO_L3N_T0_DQS_34 AC4
IO_L4P_T0_34 AC5
IO_L4N_T0_34 AC4
IO_L5P_T0_34 AD6
IO_L5N_T0_34 AE6
IO_L6P_T0_34 AC7
IO_L6N_T0_VREF_34 AD7
AF3 B34_7_P
AF2 B34_7_N
IO_L7P_T1_34 AE1
IO_L7N_T1_34 AF1
IO_L8P_T1_34 AF1
IO_L8N_T1_34 AG4
IO_L9P_T1_DQS_34 AG3
IO_L9N_T1_DQS_34 AE4
IO_L10P_T1_34 AE3
IO_L10N_T1_34 AE5
IO_L11P_T1_SRCC_34 AF5
IO_L11N_T1_SRCC_34 AF6
IO_L12P_T1_MRCC_34 AG5
IO_L12N_T1_MRCC_34 AH4
IO_L13P_T2_MRCC_34 AJ4
IO_L13N_T2_MRCC_34 AH6
IO_L14P_T2_SRCC_34 AH5
IO_L14N_T2_SRCC_34 AG2
IO_L15P_T2_DQS_34 AH1
IO_L15N_T2_DQS_34 AH2
IO_L16P_T2_34 AJ2
IO_L16N_T2_34 AJ1
IO_L17P_T2_34 AK1
IO_L17N_T2_34 AJ3
IO_L18P_T2_34 AK3
IO_L18N_T2_34 AF8
IO_L19P_T3_34 AG8
IO_L19N_T3_VREF_34 AF7
IO_L20P_T3_34 AG7
IO_L20N_T3_34 AH7
IO_L21P_T3_DQS_34 AJ7
IO_L21N_T3_DQS_34 AJ6
IO_L22P_T3_34 AK6
IO_L22N_T3_34 AJ8
IO_L23P_T3_34 AK8
IO_L23N_T3_34 AK5
IO_L24P_T3_34 AK4
IO_L24N_T3_34 AB7
IO_25_VRP_34

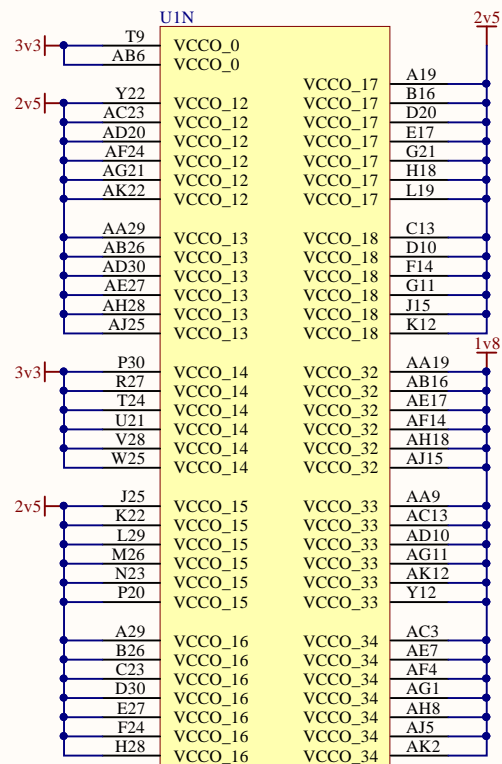
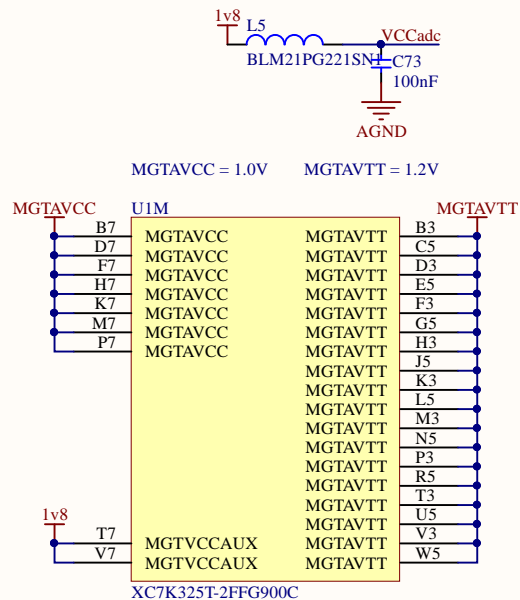
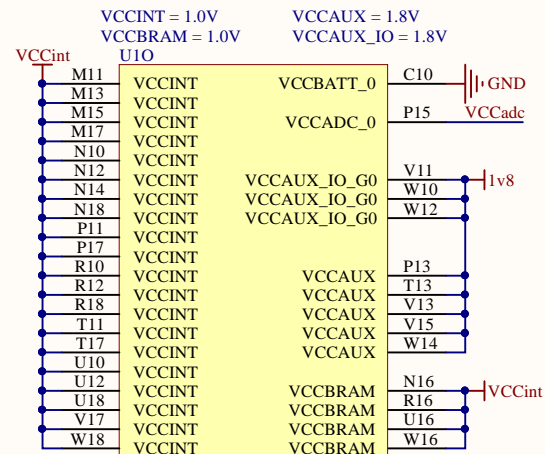
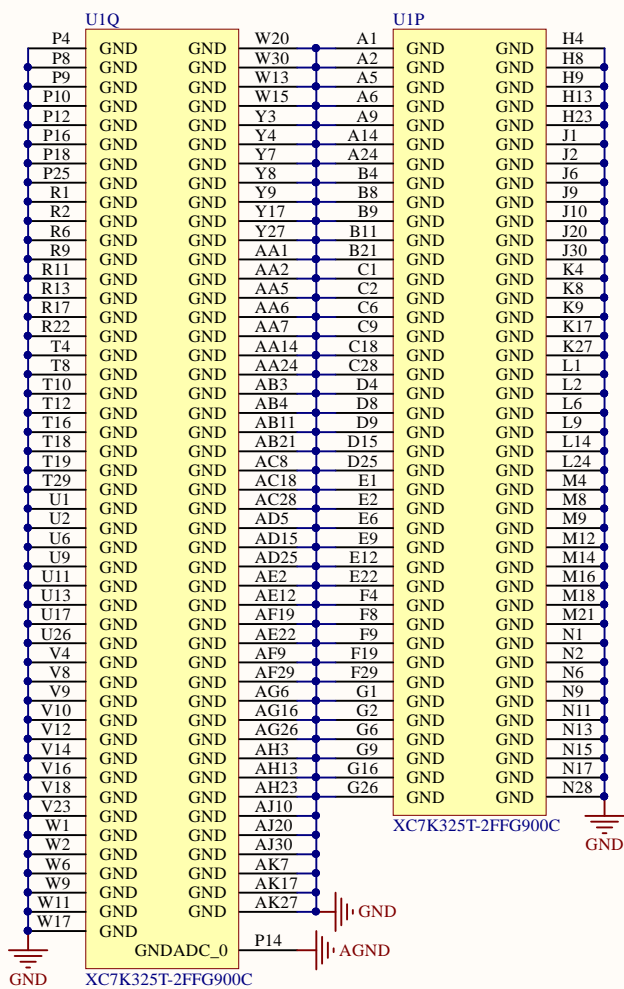
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Title		
FPGA bank 32 - 34		
Size	Number	Revision
A4		
Date:	2020/3/5	Sheet of
File:	E:\ADproject\...\FPGA_part3.SchDoc	Drawn By:



Title		
FPGA VCCO settings		
Size	Number	Revision
A4		
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File:	E:\ADproject\...\FPGA_power.SchDoc	Drawn By:

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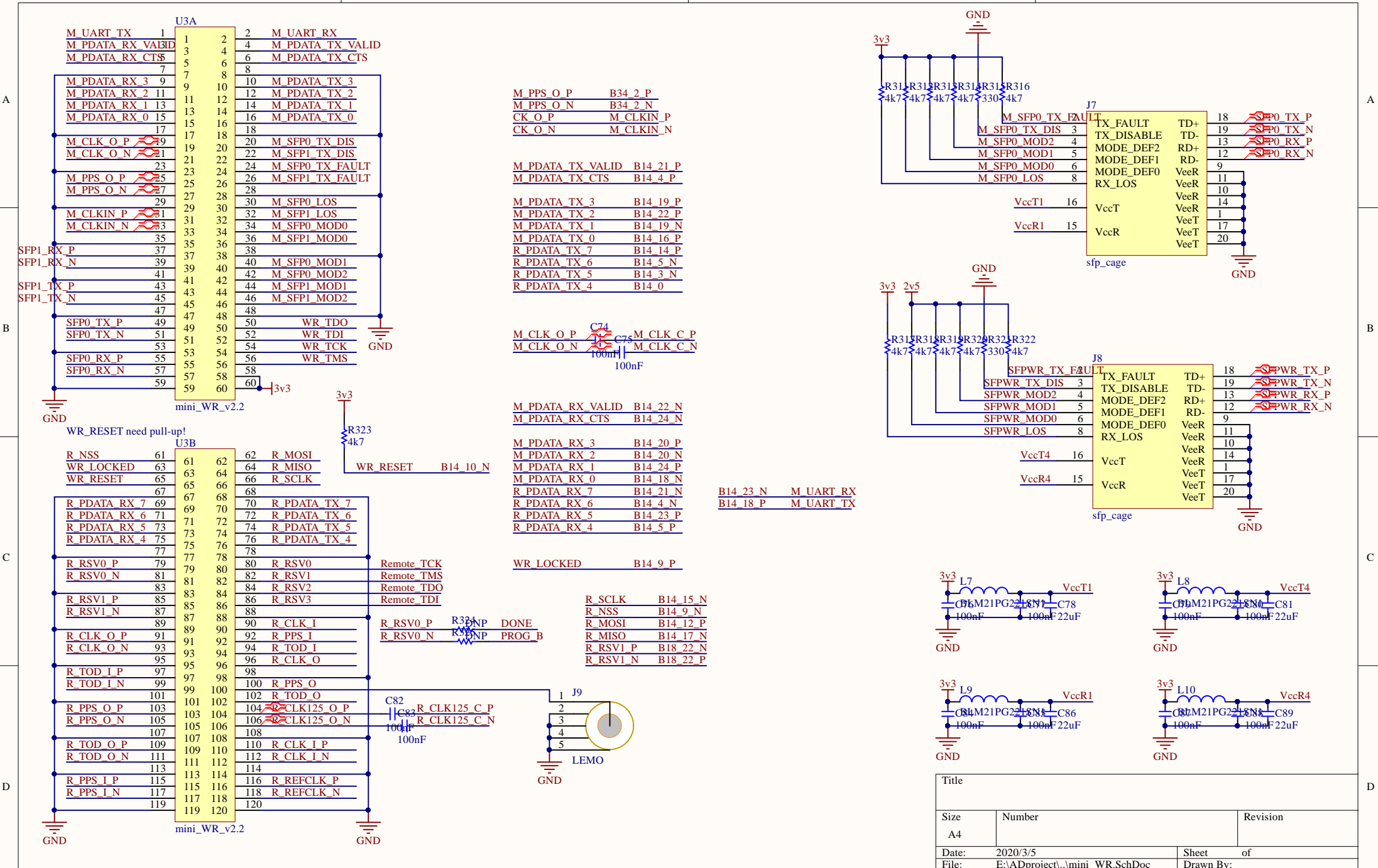
D

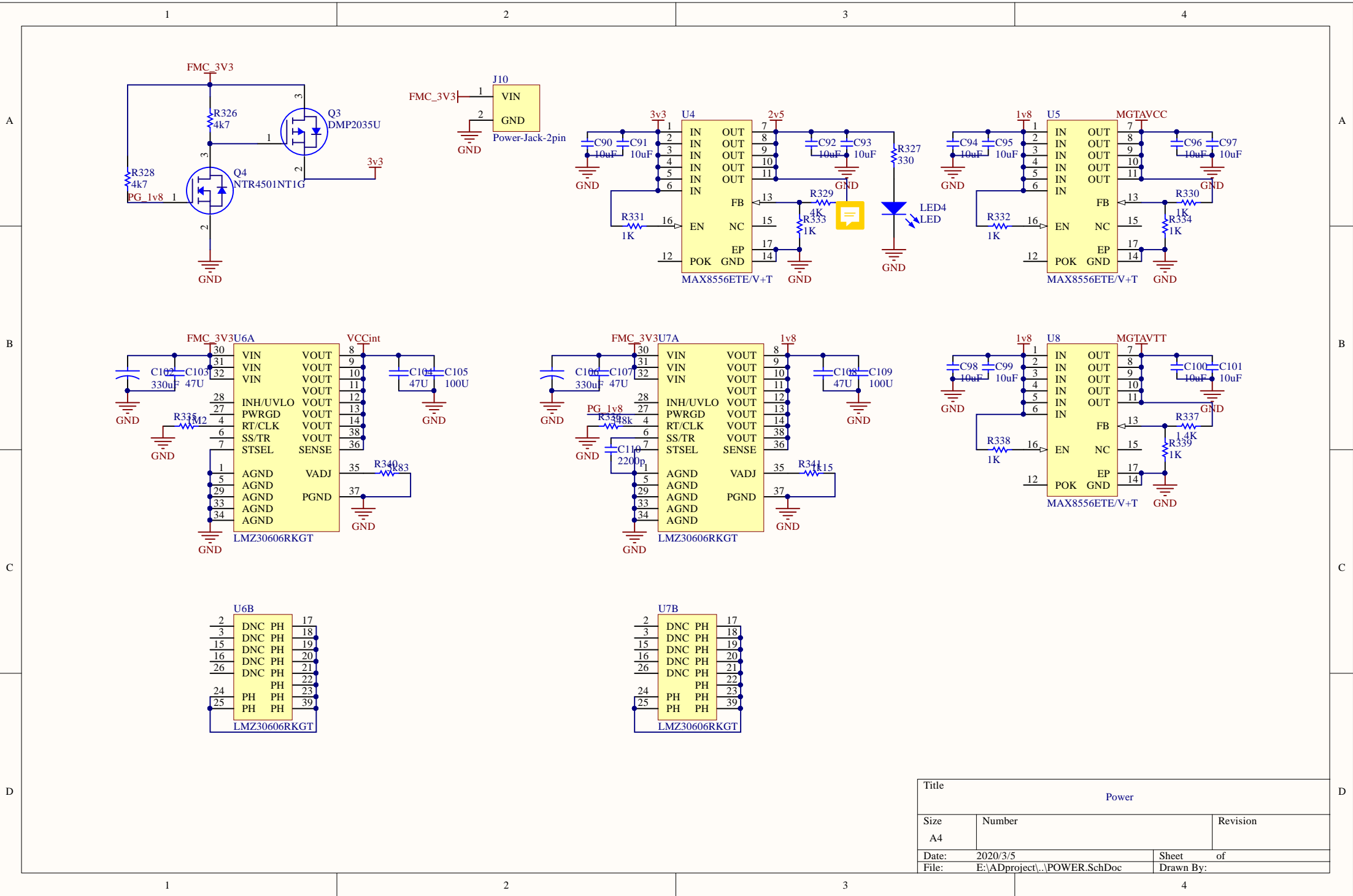
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Title		
Power		
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A4		
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