

JianPing(Xlous) Zeng

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RESEARCH My reseaching interests focus on compiler construction, and I am hooked by
INTERESTS various of puzzles in compiler, such as how to find optimal or approximately optimal method to improve efficiency of machine code generated by compiler in different criteria, especially in machine-independently optimization and code generation related to specified target machine.

EDUCATION **HuaZhong university of Science and Technology**, Wuhan, Hubei.

M.S., Computer science, 9/2014 to 6/2017, Major GPA 3.7

- Thesis Topic: *Wireless Fingerprinting Localization based On Partition of Region and Gaussian process regression*
- Advisor: KeShi, Ph.D

JiaXing university, Jiaxing, Zhejiang.

B.S., Computer science, 9/2010 to 6/2014, Major GPA 3.7

- Thesis Topic: *Design and implementation of Parallel algorithm on multi-cores processor*
- Advisor: Guangxue Yue, Ph.D

Central South university, Changsha,Hunan.

Exchange student., Computer science, 9/2012/ to 7/2013

AWARDS Student Awards — HuaZhong university of Science and Technology
• Academic excellent Award 2014-2017

Student Awards — JiaXing university
• Academic excellent Award 2010-2013
• Outstanding Graduates Award May 2014
• Second Class prize of Zhe Jiang Province Challenge Cup May 2013

WORK **Compiler Engineer** Dec 2017 to present
EXPERIENCE ToolChain group, Hangzhou C-sky system corporation,
At C-sky system corporation, I am engaged in improving GCC backend, developing LLVM backend for C-sky CPU and taking responsible for many valuable works, for example, how to implement the semantic of input language (C/C++) with specified machine instruction. In the other hand, I also accomplish some innovative works about figure out properly methods to solve bugs found in GCC backend, lastly, migrating C-sky fork of GCC from reload pass to LRA by default.

Since Joining the corporation, I also take charge of the implementation of LLVM backend for C-sky CPU because LLVM have various of advantages, such as license, great scalability and relatively excellent performance. Currently, Our team are making process of this project.

Software Engineer June 2017 to Dec 2017

Taobao BU, [Alibaba group](#),

I have finished a static analyzer for Objective-C, which used for checking formal correctness of Mobile Taobao APP on iOS platform. This software tool have capacity of finding some bugs in source file before delivery as consequence of reducing the probability of crash.

Software Engineer intern June 2016 to Aug 2016

YunOS BU, [Alibaba group](#),

I have tuned the fore-end of Google V8 virtual machine to add some functions for supporting static type annotation of JavaScript language, we named it as static-javascript. My main contributions to it including initial literature survey, making program and finally function test.

TEACHING **Teaching Assistant of Compiler course** Sept 2014 to June 2015

EXPERIENCE

HuaZhong university of Science and Technology,

I have taken responsible for reviewing student's assignment and solve some problem proposed by student about compiler.

OPEN SOURCE ***XCC C Compiler*** Summer 2015 to present

SOFTWARE

This is a C language compiler named *XCC* for researching and studying compiler construction, e.g. constant propagation, sparse conditional constant propagation, constant folding, global value numbering, loop invariant code motion, induction variable simplification, dead code elimination, pointer analysis, and many analysis & transforms operated on natural loop. In the other hand, it also includes a little of machine-dependent optimization, such as peephole opt on x86 platform.

The fore-end of *XCC* named *Jlang* has been already completed which can be used to lex, parse input source code. Then semantic action would be performed on abstract syntax tree(AST) to check the correctness, more important thing is to report precisely diagnostics information at the given source location where error or warning occur.

In order to view the internal data structure of *Jlang*, *XCC* provide various of command line options to control the output result, e.g. dumping tokens with *-E*, dumping AST corresponding to source code with option *-ast-dump*, emitting LLVM 2.6-compatible IR with option *-emit-llvm* etc.

In terms of middle-end, the *XCC* is able to performance some important analysis and transformation passes, such as basic alias analysis, induction

variable recognition, loop recognition, construction of dominator tree, dead code elimination, global value numbering, induction variable simplification, lcssa, loop invariant code motion, unused loop deletion, loop inversion, sparse conditional constant propagation, scalar replacement of aggregate, tail call elimination, unreachable basic block elimination etc.

Apart from fore-end and middle optimizer, effectively implementing the same semantic of IR code with machine-specific instructions is also greatly important part of complete compiler. In *XCC*, all of those work was implemented in directory backend of source tree, it consists of instruction selection, register allocation and no instruction scheduling. Currently, a naive instruction selection based on macro expansion has been completed. *XCC* supports two kind of register allocator, local register allocator with reuse of local register with local liveness analysis and a global allocator based on linear scan refined by Alkis Evlogimenos, wimmer.

Unfortunately, only x86 target is supported currently.

RESEARCH EXPERIENCE	Researching Assistant Department of computer science, HuaZhong university of science and technology Supervisor: Keshi, Ph.D	Sept 2015 to June 2017
BOOKS IN PREPARATION	1. Jianping Zeng . "Survey of Register allocation."	
REFERENCES	Keshi Professor School of computer science HuaZhong university of science and technology Guangxue Yue Professor College of Mathematics, Physics and Information Engineering guangxueyue@163.com Jiaxing university	Tel: 86-27-87541924 E-mail: keshi@hust.edu.cn Tel: 86-573-83647465 E-mail:
SOFTWARE SKILLS	Computer Programming: <ul style="list-style-type: none"> • C, C++, Java, C#, X86 assembly, Python, and Latex. Compiler: <ul style="list-style-type: none"> • familiar with common target-independently optimization upon IR in static single assignment form, data-flow/control flow analysis methods, instruction selection and register allocation, especially in register allocation. 	

Also familiar with LLVM compiler infrastructure. Currently studying GCC.