

Joint Offloading and Computing Optimization in Wireless Powered Mobile-Edge Computing Systems

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Abstract—Mobile-edge computing (MEC) and wireless power transfer (WPT) have been recognized as promising techniques in the Internet of Things (IoT) era to provide massive low-power wireless devices with enhanced computation capability and sustainable energy supply. In this paper, we propose a unified MEC-WPT design by considering a wireless powered multiuser MEC system, where a multi-antenna access point (AP) (integrated with an MEC server) broadcasts wireless power to charge multiple users and each user node relies on the harvested energy to execute computation tasks. With MEC, these users can execute their respective tasks locally by themselves or offload all or part of them to the AP based on a time division multiple access (TDMA) protocol. Building on the proposed model, we develop an innovative framework to improve the MEC performance, by jointly optimizing the energy transmit beamforming at the AP, the central processing unit (CPU) frequencies and the numbers of offloaded bits at the users, as well as the time allocation among users. Under this framework, we address a practical scenario where latency-limited computation is required. In this case, we develop an optimal resource allocation scheme that minimizes the AP's total energy consumption subject to the users' individual computation latency constraints. Leveraging the state-of-the-art optimization techniques, we derive the optimal solution in a semi-closed form. Numerical results demonstrate the merits of the proposed design over alternative benchmark schemes.

Index Terms—Mobile-edge computing, wireless power transfer, computation offloading, energy beamforming, convex optimization.

I. INTRODUCTION

The recent advancement of Internet of Things (IoT) has motivated various new applications (e.g., autonomous driving, virtual reality, augmented reality, and tele-surgery) to

provide real-time machine-to-machine and machine-to-human interactions [2]. These emerging latency-sensitive applications critically rely on the real-time communication and computation of massive wireless devices. For example, smart wireless sensors in IoT networks may need to perceive the physical environment and then use the built-in computation resources to preprocess the sensed data in real time before sending it to the access point (AP) [2]. As extensive existing works focus on improving their communication performance, how to provide these devices with enhanced computation capability is a crucial yet challenging task to be tackled, especially when they are of small size and low power. To resolve this issue, mobile-edge computing (MEC) has emerged as a promising technique by providing cloud-like computing at the edge of mobile networks via integrated MEC servers at wireless APs and base stations (BSs) [3], [4]. Leveraging MEC, resource-limited wireless devices can offload their computation tasks to APs/BSs; then the integrated MEC servers can compute these tasks remotely. In general, the computation offloading can be implemented in two ways, namely *binary* and *partial* offloading [3]. In the binary offloading case, the computation task is not partitionable and should be offloaded as a whole. In the partial offloading case, the task can be partitioned into two parts, and only one of them is offloaded. The MEC technique facilitates the real-time implementation of computation-extensive tasks at massive low-power devices, and thus has attracted growing research interests in both academia and industry [3]–[7].

On the other hand, how to provide sustainable and cost-effective energy supply to massive computation-heavy devices is another challenge facing IoT. Radio-frequency (RF) signal based wireless power transfer (WPT) provides a viable solution by deploying dedicated energy transmitters to broadcast energy wirelessly [18]. Recently, emerging wireless powered communication networks (WPCNs) and simultaneous wireless information and power transfer (SWIPT) paradigms have been proposed to achieve ubiquitous wireless communications in a self-sustainable way [12]–[15], where WPT and wireless communications are combined into a joint design. In order to improve the WPT efficiency from the energy transmitter to one or more energy receivers, *transmit energy beamforming* has been proposed as a promising solution by deploying multiple antennas at energy transmitters [9]. By properly adjusting the transmit beamforming vectors, energy transmitters can concentrate the radiative energy towards the intended receivers for efficient WPT. Motivated by these approaches, it is expected that the transmit energy beamforming-enabled WPT can also play an important role in facilitating self-sustainable

问题1:

问题2:

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computing for a large number of IoT devices.

To explore benefits of both MEC and WPT in ubiquitous computing, this paper develops a joint MEC-WPT design by considering a wireless powered *multiuser* MEC system that consists of a multi-antenna AP and multiple single-antenna users. The AP employs energy transmit beamforming to simultaneously charge the users, and each user relies on its harvested energy to execute the respective computation task. Suppose that partial offloading is allowed such that each user can arbitrarily partition the computation task into two *independent* parts for local computing and offloading, respectively. Furthermore, we assume that the downlink WPT and the uplink wireless communication (for computation offloading) are operated simultaneously over orthogonal frequency bands.¹ In addition, a time division multiple access (TDMA) protocol is employed to coordinate computation offloading, where different users offload their respective tasks to the AP over orthogonal time slots. The main results of this paper are summarized as follows.

- To improve the performance of such a wireless powered multiuser MEC system, we develop an innovative design framework by jointly optimizing the energy transmit beamforming at the AP, the central processing unit (CPU) frequencies² and the numbers of offloaded bits at the users, as well as the offloading time allocation among users. Note that the number of offloaded bits at each user corresponds to the multiplication of the offloading rate and allocated offloading time in this block.
- Targeting an energy-efficient wireless-powered MEC design, we minimize the AP's total energy consumption subject to the users' individual computation latency constraints. Leveraging the state-of-the-art optimization techniques, we obtain the optimal solution in a semi-closed form. It is revealed that at the optimal solution, the number of locally computed bits at each user should be strictly positive; i.e., it is always beneficial for each user to leave certain bits for local computing. It is also shown that the optimal offloading rate (and equivalent transmit power) at each user critically depends on the channel power gain and the circuit power.
- Extensive numerical results are provided to gauge the performance of the proposed designs with joint WPT, local computing, and offloading (i.e., task partition per user and offloading time allocation among the users) optimization, over benchmark schemes without such a joint consideration. It is shown that the proposed design can significantly reduce the energy consumption of the wireless powered MEC systems.

¹The wireless energy harvesting in the downlink and the information transmission (or offloading) in the uplink can be performed simultaneously over orthogonal frequency bands in one single antenna with a duplexer, as commonly used in conventional frequency-division-duplexing (FDD) wireless communication transceivers.

²The term *CPU* generally refers to the processing unit and control unit at each user that takes charge of the local computing of computation tasks. The CPU frequency, i.e., the frequency of the CPU's clock pulses, determines the rate at which a CPU executes instructions.

A. Related Works

Transmit energy beamforming enabled WPT has been extensively studied in the literature (see, e.g., [9]–[24] and references therein). By considering a linear energy harvesting (EH) model, various prior works have investigated the optimal design of energy beamforming under different setups with SWIPT, e.g., in two-user multi-input multi-output (MIMO) systems [9], secrecy communications systems [10], multi-input single-output (MISO) interference channels [11], and multiuser MISO downlink channels [14]–[16]. Furthermore, some recent works investigated the transmit power allocation [23] and the transmit waveform optimization [24] for WPT by taking into account the nonlinear nature of the rectifier in EH [20], [21]. In addition, the benefit of energy beamforming crucially relies on the channel state information (CSI) known at the transmitter. The reverse-link channel training [17] and the energy measurement and feedback methods [18], [19] were proposed in WPT systems for the energy transmitter to practically learn the CSI to users. Furthermore, [22] developed a distributed energy beamforming system for multiple energy transmitters to charge multiple energy receivers simultaneously, with the help of the energy measurement and feedback.

On the other hand, several existing works [25]–[33] investigated the energy-efficient multiuser MEC design, where each user is powered by fixed energy sources such as battery, and the objective is to minimize the energy consumption at the users via joint computing and offloading optimization at the demand side. For example, [25] provided an overview on the applications and challenges of computation offloading. [26] and [27] investigated the dynamic offloading for MEC systems based on the techniques of Markov decision process and Lyapunov optimization, respectively. [28], [29] considered the joint computation and communication resource allocation in single-user MEC systems, and such designs were extended to multiuser MEC systems in [30]–[33]. Different from these prior works that studied WPT and MEC separately, this paper pursues a joint MEC-WPT design in a wireless powered multiuser MEC system, by jointly optimizing the WPT supply at the AP, as well as the local computing and offloading demands at the users.

It is worth noting that a prior work [34] considered the wireless powered *single-user* MEC system with binary offloading, where the user aims to maximize the probability of successful computation, by deciding whether a task should be fully offloaded or not, subject to the computation latency constraint. By contrast, this paper considers a more general case with more than one user, and allows for more flexible partial offloading to improve the system performance in terms of the energy efficiency (i.e., minimizing the total energy consumption at the AP including the radiated energy for WPT and the energy for computing the offloaded tasks).

The remainder of the paper is organized as follows. Section II presents the system model. Section III formulates the computation latency constrained energy consumption minimization problem, and develops an efficient algorithm to obtain a well-structured optimal solution. Section IV provides numerical results to demonstrate the merits of the proposed design.

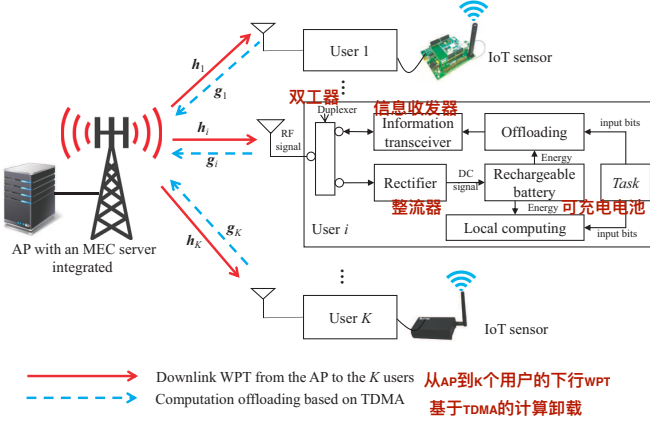


Fig. 1. A wireless powered multiuser MEC system with WPT in the downlink and computation offloading in the uplink.

Finally, Section V concludes this paper.

Notations: Boldface letters refer to vectors (lower case) or matrices (upper case). For a square matrix S , $\text{tr}(S)$ denotes its trace, while $S \geq 0$ means that S is positive semidefinite. For an arbitrary-size matrix M , $\text{rank}(M)$, M^+ , and M^H denote its rank, transpose, and conjugate transpose, respectively. I and 0 denote an identity matrix and an all-zero vector/matrix, respectively, with appropriate dimensions. $\mathbb{C}^{x \times y}$ denotes the space of $x \times y$ complex matrices; \mathbb{R} denotes the set of real numbers. $\mathbb{E}[\cdot]$ denotes the statistical expectation. $\|x\|$ denotes the Euclidean norm of a vector x , $|z|$ denotes the magnitude of a complex number z , and $[x]^+ \triangleq \max(x, 0)$.

II. SYSTEM MODEL

As shown in Fig. 1, we consider a wireless powered multiuser MEC system consisting of an N -antenna AP (integrated with an MEC server) and a set $\mathcal{K} \triangleq \{1, \dots, K\}$ of single-antenna users. In this system, the AP employs RF signal based energy transmit beamforming to charge the K users. Each user $i \in \mathcal{K}$ utilizes the harvested energy to execute its computation task through local computing and offloading. Suppose that the downlink WPT from the AP to the users and the uplink computation offloading are operated simultaneously over orthogonal frequency bands, and the uplink for computation offloading and the downlink for computation result downloading are operated over the same frequency band. Assume a block-based model, and we focus on one particular block with length T . Here, T is chosen to be no larger than the latency of the MEC application and also no larger than the channel coherence time, such that the wireless channels remain unchanged during this block. For simplifying the analysis and better capturing the AP's transmission energy for computation offloading, we assume that the AP perfectly knows the CSI from/to the K users,³ as well as their computation requirements. In accordance with such information, the AP coordinates the downlink

³When the CSI at the AP is not perfect (e.g., subject to some CSI estimation errors), the WPT and MEC performance may degrade. In this case, robust optimization techniques (see, e.g., [10], [15]) may be applied to obtain the energy beamforming vectors. However, the imperfect CSI scenario is out of scope of this paper.

WPT, the computation offloading, and the local computing for the K users.

A. Energy Transmit Beamforming from AP to Users

Let $s \in \mathbb{C}^{N \times 1}$ denote the energy-bearing transmit signal by the AP, which is assumed to be a random signal with its power spectral density satisfying certain regulations on RF radiation [18]. Let $Q \triangleq \mathbb{E}[ss^H] \geq 0$ denote the energy transmit covariance matrix and $\mathbb{E}[\|s\|^2] = \text{tr}(Q)$ the transmit power at the AP. In general, the AP can use multiple energy beams to deliver the wireless energy, i.e., Q can be of any rank. Supposing $r = \text{rank}(Q) \leq N$, then a total of r energy beams can be obtained via the eigenvalue decomposition (EVD) of Q [18]. Let $h_i \in \mathbb{C}^{N \times 1}$ denote the channel vector from the AP to user $i \in \mathcal{K}$, and define $H_i \triangleq h_i h_i^H$, $\forall i \in \mathcal{K}$. Accordingly, the received RF power at each user $i \in \mathcal{K}$ is given by $|h_i^H s|^2$. In order to harvest such energy, each user i first converts the received RF signal into a direct current (DC) signal by a rectifier and then stores the energy of the DC signal in its chargeable battery (cf. Fig. 1). Note that the harvested DC power is generally nonlinear with respect to the received RF power [20], due to the nonlinear devices such as the diodes and diode-connected transistors. Moreover, the nonlinear RF-to-DC conversion greatly depends on the input power level and the transmit waveform. In the literature, there have been a handful of recent works on analytic nonlinear EH models, which characterize such nonlinear relations between the harvested DC power and the input RF power [23] or transmit waveform [24]. However, there still lacks a generic EH model that captures all practical issues [21]. Therefore, for simplicity, we assume that the input RF power is within the linear regime of the rectifier, and consider a linear EH model which has been commonly adopted in the WPT literature [9]–[12], [14]–[22]. Accordingly, the harvested energy amount by user i over this time block is

$$E_i = T\zeta \mathbb{E} \left[|h_i^H s|^2 \right] = T\zeta \text{tr}(QH_i), \quad (1)$$

where $0 < \zeta \leq 1$ denotes the constant EH efficiency per user. The harvested energy E_i is used by user i for both computation offloading and local computing.

B. Energy Consumption at Users for Computation

For each user $i \in \mathcal{K}$, the computation task with $R_i > 0$ computation input bits is partitioned into two parts with $\ell_i \geq 0$ and $q_i \geq 0$ bits, which are offloaded to the MEC server at the AP or locally computed, respectively.⁴ We assume that such a partition does not incur additional computation input bits, i.e., $R_i = \ell_i + q_i$, $\forall i \in \mathcal{K}$.

1) **Computation Offloading from Users to the AP:** In order for the K users to offload their respective bits to the AP for computation, we adopt a TDMA protocol without interference as shown in Fig. 2, where the block is divided into $2K$ time slots each with a length of t_i , $\forall i \in \{1, \dots, 2K\}$. In the first K time slots, the K users offload their computation bits to the AP

⁴Each input bit can be treated as the smallest task unit, which includes the needed program codes and input parameters.

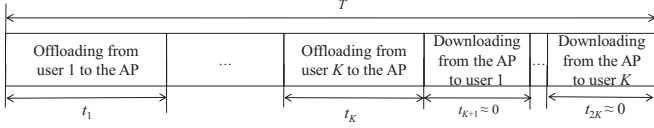


Fig. 2. The TDMA protocol for multiuser computation offloading.

one by one. After the MEC server executes the computation tasks on behalf of these users, **the AP sends the computation results to the K users in the last K time slots.** Due to the sufficient CPU capability at the MEC server, the computation time consumed at the MEC server are relatively small and negligible. Therefore, we assume that the users can download the computation results immediately after the first K offloading time slots. Furthermore, as the AP is usually with high transmit power and the computed results are usually of small size, we ignore the downloading time, i.e., $t_i \approx 0$, $\forall i \in \{K+1, \dots, 2K\}$, and also ignore the energy consumption for transmitting and receiving the computation results in this paper.

For computation offloading in time slot i , let $\mathbf{g}_i \in \mathbb{C}^{N \times 1}$ denote the uplink channel vector from user i to the AP and p_i the transmit power for offloading. Assume further that the AP employs the maximum ratio combining (MRC) receiver to decode the information. The achievable offloading rate (in bits/sec) for user i is given by

$$r_i = B \log_2 \left(1 + \frac{p_i \tilde{g}_i}{\Gamma \sigma^2} \right), \quad \forall i \in \mathcal{K}, \quad (2)$$

where B denotes the spectrum bandwidth, $\tilde{g}_i \triangleq \|\mathbf{g}_i\|^2$ denotes the effective channel power gain from user i to the AP, σ^2 is the noise power at the receiver of the AP, and $\Gamma \geq 1$ is a constant accounting for the gap from the channel capacity due to a practical coding and modulation scheme. For simplicity, $\Gamma = 1$ is assumed throughout this paper. As a result, the number of offloaded bits ℓ_i by user i to the AP can be expressed as

$$\ell_i = r_i t_i, \quad \forall i \in \mathcal{K}. \quad (3)$$

Computation offloading incurs energy consumption at both the K users and the AP. Per user $i \in \mathcal{K}$, in addition to the transmit power p_i , a constant circuit power $p_{c,i}$ (by the digital-to-analog converter (DAC), filter, etc.) is consumed. The offloading energy consumption at user i is then $E_{\text{off},i} = (p_i + p_{c,i})t_i$. With simple manipulations based on (2) and (3), the transmit power p_i can be expressed as $p_i = \frac{1}{\beta} \beta \left(\frac{\ell_i}{t_i} \right)$, where $\beta(x) \triangleq \sigma^2 (2^{\frac{x}{B}} - 1)$ is a monotonically increasing and convex function with respect to x .⁵ Hence, the offloading energy consumption at user i is

$$E_{\text{off},i} = \frac{t_i}{\beta} \beta \left(\frac{\ell_i}{t_i} \right) + p_{c,i} t_i. \quad (4)$$

Remark 2.1: Note that in practice, in order for the AP to acquire the CSI (to the K users) for the energy beamforming design, each user needs to consume a certain amount of energy

⁵Note that to avoid the issue of dividing by zero, we define $\beta \left(\frac{\ell_i}{t_i} \right) = 0$ when either $\ell_i = 0$ or $t_i = 0$ holds.

(e.g., for CSI feedback), and there generally exists a tradeoff between such energy consumption at the users versus the CSI accuracy at the AP. However, with the technical advancements, the user's feedback overhead for CSI acquisition could be made very small. Specifically, there are generally three types of CSI acquisition methods in the literature, namely the channel estimation and feedback [8], reverse-link training based on the channel reciprocity [17], and energy measurement and feedback [18], [19]. In the energy measurement and feedback method [18], each user only needs to measure its harvested energy level over each block and send one feedback bit to the AP per block; based on the feedback bits, the AP can sequentially improve the accuracy of CSI estimation; such a one-bit feedback is negligible when compared to the user reverse-link traffic for task offloading. Thus it is practically reasonable to ignore the feedback overhead and energy consumption at each user.

As for the AP, the energy is mainly consumed for executing the offloaded computation tasks and transmitting the computation results back to the users [4]. As the AP and its integrated MEC server generally have sufficient communication and computation capacities⁶, it can adopt a large transmit power (accordingly high communication rate) and a high constant CPU frequency to minimize the latency. In this case, the AP's energy consumption is generally proportional to the totally offloaded bits $\sum_{i=1}^K \ell_i$ from the K users. Therefore, we adopt a simplified linear energy consumption model for the computation at the AP as

$$E_{\text{MEC}} = \alpha \sum_{i=1}^K \ell_i, \quad (5)$$

where α denotes the energy consumption per offloaded bit at the AP. In practice, α depends on the transceiver structure of the AP, the chip structure of the MEC server, and its operated CPU frequencies, etc. [4].

2) Local Computing at Users: Consider next the local computing for executing q_i input bits at each user $i \in \mathcal{K}$. Let C_i denote the number of CPU cycles required for computing one input bit at user i . Then the total number of CPU cycles required for the q_i bits is $C_i q_i$. By applying dynamic voltage and frequency scaling (DVFS) techniques [3], [4], user i can control the energy consumption for local task execution by adjusting the CPU frequency $f_{i,n}$ for each cycle n , where $f_{i,n} \in (0, f_i^{\max}]$, $n \in \{1, \dots, C_i q_i\}$, and f_i^{\max} denotes user i 's maximum CPU frequency.⁷ With $f_{i,n}$'s, the execution time for local computing at user i is $\sum_{n=1}^{C_i q_i} \frac{1}{f_{i,n}}$. As each user $i \in \mathcal{K}$

⁶In the case when the MEC server's computing capacity is limited, the computation offloading protocol needs to be redesigned, by taking into account the computation time at the MEC server as well as the computation resource sharing among these different users. Under such a scenario, how to jointly design the WPT and MEC optimally is out of the scope of this paper. It is an interesting direction to pursue in the future work.

⁷Note that in practice, each CPU frequency $f_{i,n}$ can only be an integer chosen from a finite set. However, such an integer constraint may make the design problem a mixed-integer one that is NP-hard in general. To avoid this, we model $f_{i,n}$ as continuous variables to provide a performance upper-bound for the practical cases with discrete CPU frequencies.

needs to accomplish the task execution within a block, the execution time cannot exceed the block length T , i.e.,

$$\sum_{n=1}^{C_i q_i} \frac{1}{f_{i,n}} \leq T, \quad \forall i \in \mathcal{K}. \quad (6)$$

Under the assumption of a low CPU voltage that normally holds for low-power devices, the consumed energy for local computing at user $i \in \mathcal{K}$ could be expressed as [35]

$$E_{\text{loc},i} = \sum_{n=1}^{C_i q_i} \kappa_i f_{i,n}^2, \quad (7)$$

where κ_i is the effective capacitance coefficient that depends on the chip architecture at user i .

C. Energy Harvesting Constraints at Users

As each user $i \in \mathcal{K}$ is powered by the WPT from the AP to achieve self-sustainable operation, the so-called energy harvesting constraint needs to be imposed such that the totally consumed energy at the user cannot exceed the harvested energy E_i in (1) per block. By combining the computation offloading energy in (4) and the local computation energy in (7), the total energy consumed by user i within the block is $E_{\text{off},i} + E_{\text{loc},i}$. Therefore, we must have per user $i \in \mathcal{K}$:⁸

$$E_{\text{loc},i} + E_{\text{off},i} \leq E_i. \quad (8)$$

III. COMPUTATION LATENCY CONSTRAINED ENERGY MINIMIZATION

A. Problem Formulation

Under the above setup, we pursue an energy-efficient MEC-WPT design by considering a computation latency constrained energy minimization problem. Suppose that each user $i \in \mathcal{K}$ has a computation task with $R_i > 0$ input bits, which needs to be successfully executed before the end of the block. In this case, the sum of the number of offloaded bits ℓ_i and the number of locally computed bits q_i should be equal to R_i , i.e., we have $q_i = R_i - \ell_i$, $\forall i \in \mathcal{K}$.

We aim to minimize the energy consumption at the AP (including the energy consumption $\sum_{i=1}^K \alpha \ell_i$ in (5) for computation and $T \text{tr}(\mathbf{Q})$ for WPT) while ensuring the successful execution of the K users' computation tasks per time block. To this end, we jointly optimize the energy transmit covariance matrix \mathbf{Q} at the AP, the local CPU frequencies $\{f_{i,1}, \dots, f_{i,C_i(R_i-\ell_i)}\}$, and the numbers of offloaded bits ℓ_i 's at the users, as well as the time allocation t_i 's among different users. Let $\mathbf{t} \triangleq [t_1, \dots, t_K]^\top$, $\boldsymbol{\ell} \triangleq [\ell_1, \dots, \ell_K]^\top$, and

$\mathbf{f} \triangleq [f_{1,1}, \dots, f_{K,C_K(R_K-\ell_K)}]^\top$. Mathematically, the latency-constrained energy minimization problem is formulated as

$$(\mathcal{P}1): \min_{\mathbf{Q} \geq 0, \mathbf{t}, \boldsymbol{\ell}, \mathbf{f}} T \text{tr}(\mathbf{Q}) + \sum_{i=1}^K \alpha \ell_i \quad (9a)$$

$$\text{s.t.} \quad \sum_{n=1}^{C_i(R_i-\ell_i)} \frac{1}{f_{i,n}} \leq T, \quad \forall i \in \mathcal{K} \quad (9b)$$

$$\sum_{n=1}^{C_i(R_i-\ell_i)} \kappa_i f_{i,n}^2 + \frac{t_i}{g_i} \beta\left(\frac{\ell_i}{t_i}\right) + p_{c,i} t_i - T \zeta \text{tr}(\mathbf{Q} \mathbf{H}_i) \leq 0, \quad \forall i \in \mathcal{K} \quad (9c)$$

$$\sum_{i=1}^K t_i \leq T, \quad t_i \geq 0, \quad 0 \leq \ell_i \leq R_i, \quad \forall i \in \mathcal{K} \quad (9d)$$

$$0 \leq f_{i,n} \leq f_i^{\max}, \quad \forall n, \quad \forall i \in \mathcal{K}. \quad (9e)$$

Here, the constraints in (9b) and (9c) represent the K users' individual local computing latency and energy harvesting constraints, respectively. Note that due to the non-convex nature of (9b) and (9c), problem $(\mathcal{P}1)$ is non-convex in the current form. However, we can transform it into a convex form and find the well-structured optimal solution, as will be shown in the next subsection.

B. Optimal Solution to Problem $(\mathcal{P}1)$

In this subsection, we provide the optimal solution to the computation latency constrained energy minimization problem $(\mathcal{P}1)$. To cope with the non-convex constraints in (9b) and (9c), we first establish the following lemma.

Lemma 3.1: Given the number of offloaded bits ℓ , the optimal solution of the local CPU frequencies $f_{i,n}$'s to problem $(\mathcal{P}1)$ should satisfy

$$f_{i,1} = \dots = f_{i,C_i(R_i-\ell_i)} = C_i(R_i - \ell_i)/T, \quad \forall i \in \mathcal{K}. \quad (10)$$

Proof: See Appendix A. ■

Lemma 3.1 indicates that at each user $i \in \mathcal{K}$, the local CPU frequencies for different CPU cycles are identical at the optimality. Hence, problem $(\mathcal{P}1)$ can be equivalently reformulated as

$$(\mathcal{P}1.1): \min_{\mathbf{Q} \geq 0, \mathbf{t}, \boldsymbol{\ell}} T \text{tr}(\mathbf{Q}) + \sum_{i=1}^K \alpha \ell_i \quad (11a)$$

$$\text{s.t.} \quad \sum_{i=1}^K t_i \leq T \quad (11b)$$

$$\frac{\kappa_i C_i^3 (R_i - \ell_i)^3}{T^2} + \frac{t_i}{g_i} \beta\left(\frac{\ell_i}{t_i}\right) + p_{c,i} t_i - T \zeta \text{tr}(\mathbf{Q} \mathbf{H}_i) \leq 0 \quad \forall i \in \mathcal{K} \quad (11c)$$

$$0 \leq \ell_i \leq R_i, \quad t_i \geq 0, \quad \forall i \in \mathcal{K}. \quad (11d)$$

As $\beta(x)$ is convex as a function of $x \geq 0$, its perspective function $\frac{t_i}{g_i} \beta\left(\frac{\ell_i}{t_i}\right)$ is jointly convex with respect to $t_i \geq 0$ and $\ell_i \geq 0$ [37]. As a result, the energy harvesting constraints in (11c) become convex. Furthermore, since the objective function in (11a) is affine and the other constraints are all convex, problem $(\mathcal{P}1.1)$ is convex and can thus be optimally solved

⁸Note that in (8) we consider that the totally consumed energy should not exceed the totally harvested one, instead of the "energy causality" in conventional energy harvesting communications (see, e.g., [13]). This consideration implies that at the beginning of the block each user has sufficiently large energy storage, such that the stored energy will never be used up at any time within each block and the energy storage level will be refilled via energy harvesting by the end of each block.

by standard convex optimization techniques. Nevertheless, to gain engineering insights, we derive its optimal solution in a semi-closed form by leveraging the Lagrange duality method [37].

Let $\mu \geq 0$ and $\lambda_i \geq 0$ denote the dual variables associated with the time-allocation constraint in (11b) and the i -th energy harvesting constraint in (11c), $\forall i \in \mathcal{K}$, respectively. Then the partial Lagrangian of (P1.1) is expressed as

$$\begin{aligned} \mathcal{L}_1(\mathbf{Q}, \mathbf{t}, \ell, \boldsymbol{\lambda}, \mu) = & T \text{tr} \left(\left(\mathbf{I} - \sum_{i=1}^K \zeta \lambda_i \mathbf{H}_i \right) \mathbf{Q} \right) - \mu T \\ & + \sum_{i=1}^K \left(\alpha \ell_i + \frac{\lambda_i \kappa_i C_i^3 (R_i - \ell_i)^3}{T^2} \right. \\ & \left. + \frac{\lambda_i t_i}{\tilde{g}_i} \beta \left(\frac{\ell_i}{t_i} \right) + \lambda_i p_{c,i} t_i + \mu t_i \right), \end{aligned} \quad (12)$$

where $\boldsymbol{\lambda} \triangleq [\lambda_1, \dots, \lambda_K]^\dagger$. Accordingly, the dual function is given by

$$\begin{aligned} \Phi(\boldsymbol{\lambda}, \mu) = & \min_{\mathbf{Q} \geq \mathbf{0}, \mathbf{t}, \ell} \mathcal{L}(\mathbf{Q}, \mathbf{t}, \ell, \boldsymbol{\lambda}, \mu) \\ \text{s.t. } & 0 \leq \ell_i \leq R_i, \quad t_i \geq 0, \quad \forall i \in \mathcal{K}. \end{aligned} \quad (13)$$

Consequently, the dual problem of (P1.1) is

$$(\mathcal{D}1.1): \quad \max_{\boldsymbol{\lambda}, \mu} \quad \Phi(\boldsymbol{\lambda}, \mu) \quad (14a)$$

$$\text{s.t. } \mathbf{F}(\boldsymbol{\lambda}) \triangleq \mathbf{I} - \sum_{i=1}^K \zeta \lambda_i \mathbf{H}_i \geq \mathbf{0} \quad (14b)$$

$$\mu \geq 0, \quad \lambda_i \geq 0, \quad \forall i \in \mathcal{K}. \quad (14c)$$

Note that the constraint $\mathbf{F}(\boldsymbol{\lambda}) \geq \mathbf{0}$ is necessary to ensure the dual function $\Phi(\boldsymbol{\lambda}, \mu)$ to be bounded from below (as proved in Appendix B). We denote the feasible set of $(\boldsymbol{\lambda}, \mu)$ characterized by (14b) and (14c) as \mathcal{S} .

Since problem (P1.1) is convex and satisfies the Slater's condition, strong duality holds between (P1.1) and its dual problem (D1.1) [37]. As a result, we can solve (P1.1) by equivalently solving (D1.1). In the following, we first obtain the dual function $\Phi(\boldsymbol{\lambda}, \mu)$ for any given $(\boldsymbol{\lambda}, \mu) \in \mathcal{S}$, and then find the optimal dual variables $\boldsymbol{\lambda}$ and μ to maximize $\Phi(\boldsymbol{\lambda}, \mu)$ using the ellipsoid method [38]. For convenience of presentation, let $(\mathbf{Q}^*, \mathbf{t}^*, \ell^*)$ denote the optimal solution to problem (13) for given $\boldsymbol{\lambda}$ and μ , $(\mathbf{Q}^{\text{opt}}, \mathbf{t}^{\text{opt}}, \ell^{\text{opt}})$ denote the optimal primary solution to (P1.1), and $(\boldsymbol{\lambda}^{\text{opt}}, \mu^{\text{opt}})$ denote the optimal dual solution to (D1.1).

1) *Evaluating the Dual Function $\Phi(\boldsymbol{\lambda}, \mu)$* : First, we obtain the dual function $\Phi(\boldsymbol{\lambda}, \mu)$ in (13) for any given $(\boldsymbol{\lambda}, \mu) \in \mathcal{S}$. To this end, problem (13) can be decomposed into $(K + 1)$ subproblems as follows, one for optimizing \mathbf{Q} and the other K for jointly optimizing t_i 's and ℓ_i 's.

$$\min_{\mathbf{Q}} \text{tr}(\mathbf{Q} \mathbf{F}(\boldsymbol{\lambda})) \quad \text{s.t. } \mathbf{Q} \geq \mathbf{0}. \quad (15)$$

$$\min_{t_i, \ell_i} \alpha \ell_i + \frac{\lambda_i \kappa_i C_i^3 (R_i - \ell_i)^3}{T^2} + \frac{\lambda_i t_i}{\tilde{g}_i} \beta \left(\frac{\ell_i}{t_i} \right) + \lambda_i p_{c,i} t_i + \mu t_i \quad (16a)$$

$$\text{s.t. } 0 \leq \ell_i \leq R_i, \quad t_i \geq 0, \quad (16b)$$

where each subproblem i in (16) is for the user $i \in \mathcal{K}$. Under the condition of $\mathbf{F}(\boldsymbol{\lambda}) \geq \mathbf{0}$, it is evident that the optimal value of problem (15) is zero and its optimal solution \mathbf{Q}^* can be any positive semidefinite matrix in the null space of $\mathbf{F}(\boldsymbol{\lambda})$. Without loss of optimality, we simply set $\mathbf{Q}^* = \mathbf{0}$ for the purpose of obtaining the dual function $\Phi(\boldsymbol{\lambda}, \mu)$ and computing the optimal dual solution.⁹ Note that $\mathbf{Q}^* = \mathbf{0}$ is generally not the optimal primary solution to (P1.1). As a result, after finding the optimal dual solution $(\boldsymbol{\lambda}^{\text{opt}}, \mu^{\text{opt}})$, we need to use an additional step to retrieve the optimal primary solution of \mathbf{Q}^{opt} to (P1.1), as will be shown in Section IV-C.

For the i -th subproblem in (16), it is convex and satisfies the Slater's condition. Based on the Karush-Kuhn-Tucker (KKT) conditions [37], one can obtain the optimal solution (t_i^*, ℓ_i^*) to (16) in a semi-closed form, as stated in the following lemma.

Lemma 3.2: For any given $(\boldsymbol{\lambda}, \mu) \in \mathcal{S}$, the optimal solution (t_i^*, ℓ_i^*) to problem (16) can be obtained as follows.

- If $\lambda_i = 0$, we have $\ell_i^* = 0$ and $t_i^* = 0$;
- If $\lambda_i > 0$, we have

$$\ell_i^* = \left[R_i - \sqrt{\frac{T^2}{3 \kappa_i C_i^3} \left(\frac{\alpha}{\lambda_i} + \frac{\sigma^2 \ln 2}{B \tilde{g}_i} 2^{\frac{r_i^*}{B}} \right)} \right]^+ \quad (17)$$

$$t_i^* = \ell_i^* / r_i^*, \quad (18)$$

where $r_i^* \triangleq \frac{B}{\ln 2} \left(W_0 \left(\frac{\tilde{g}_i}{\sigma^2 e} \left(\frac{\mu}{\lambda_i} + p_{c,i} \right) - \frac{1}{e} \right) + 1 \right)$ denotes the offloading rate of user i , $W_0(x)$ is the principal branch of the Lambert W function defined as the solution for $W_0(x)e^{W_0(x)} = x$ [36], and e is the base of the natural logarithm.

Proof: See Appendix C. ■

By combining Lemma 3.2 and $\mathbf{Q}^* = \mathbf{0}$, the dual function $\Phi(\boldsymbol{\lambda}, \mu)$ can be evaluated for any given $(\boldsymbol{\lambda}, \mu) \in \mathcal{S}$.

2) *Obtaining the Optimal $\boldsymbol{\lambda}^{\text{opt}}$ and μ^{opt} to Maximize $\Phi(\boldsymbol{\lambda}, \mu)$* : Having obtained $(\mathbf{Q}^*, \ell^*, \mathbf{t}^*)$ for given $\boldsymbol{\lambda}$ and μ , we can next solve the dual problem (D1.1) to maximize $\Phi(\boldsymbol{\lambda}, \mu)$. Note that the dual function $\Phi(\boldsymbol{\lambda}, \mu)$ is concave but non-differentiable in general [37]. Hence, we use subgradient based methods, e.g., the ellipsoid method [38], to obtain the optimal $\boldsymbol{\lambda}^{\text{opt}}$ and μ^{opt} for problem (D1.1). The basic idea of the ellipsoid method is to find a series of ellipsoids to localize the optimal dual solution $\boldsymbol{\lambda}^{\text{opt}}$ and μ^{opt} [38]. To start with, we choose a given $(\boldsymbol{\lambda}, \mu) \in \mathcal{S}$ as the center of the initial ellipsoid and set its volume to be sufficiently large to contain $(\boldsymbol{\lambda}^{\text{opt}}, \mu^{\text{opt}})$. Then, at each iteration, we update the dual variables $(\boldsymbol{\lambda}, \mu)$ based on the subgradients of both the objective function and the constraint functions, and accordingly construct a new ellipsoid with reduced volume. When the volume of the ellipsoid is reduced below a certain threshold, the iteration will terminate and the center of the ellipsoid is chosen to be the optimal dual solution $(\boldsymbol{\lambda}^{\text{opt}}, \mu^{\text{opt}})$. More details can be referred to in [38].

To implement the ellipsoid method, it remains to determine the subgradients of both the objective function in (14a) and

⁹Note that $\mathbf{Q}^* = \mathbf{0}$ is not a unique optimal solution to problem (15) when $\mathbf{F}(\boldsymbol{\lambda})$ is rank-deficient, i.e., $\text{rank}(\mathbf{F}(\boldsymbol{\lambda})) < N$.

the constraint functions in (14b) and (14c). For the objective function $\Phi(\lambda, \mu)$ in (14a), one subgradient is given by [38]

$$\left[\frac{\kappa_1 C_1^3 (R_1 - \ell_1^*)^3}{T^2} + \frac{t_1^*}{\tilde{g}_1} \beta \left(\frac{\ell_1^*}{t_1^*} \right) + p_{c,1} t_1^*, \dots, \frac{\kappa_K C_K^3 (R_K - \ell_K^*)^3}{T^2} + \frac{t_K^*}{\tilde{g}_K} \beta \left(\frac{\ell_K^*}{t_K^*} \right) + p_{c,K} t_K^*, \sum_{i=1}^K t_i^* - T \right]^\dagger. \quad (19a)$$

As for the constraint $F(\lambda) \geq 0$ in (14b), we have the following lemma.

Lemma 3.3: Let $v \in \mathbb{C}^{N \times 1}$ be the eigenvector corresponding to the smallest eigenvalue of $F(\lambda)$, i.e., $v = \arg \min_{\|x\|=1} x^H F(\lambda) x$. Then the constraint $F(\lambda) \geq 0$ is equivalent to the constraint of $v^H F(\lambda) v \geq 0$, and the subgradient of $v^H F(\lambda) v$ at the given λ and μ is

$$[\zeta v^H H_1 v, \dots, \zeta v^H H_K v, 0]^\dagger. \quad (20)$$

Proof: See Appendix D. ■

Furthermore, the subgradient of $\lambda_i \geq 0$ in (14c) is given by the elementary vector $e_i \in \mathbb{R}^{(K+1) \times 1}$ (i.e., e_i is of all zero entries except for the i -th entry being one), $\forall i \in \mathcal{K}$, while that of $\mu \geq 0$ is e_{K+1} . By using this together with (19) and (20), the ellipsoid method can be applied to efficiently update λ and μ towards the optimal λ^{opt} and μ^{opt} for (D1.1).

3) *Finding the Optimal Primary Solution to (P1):* With λ^{opt} and μ^{opt} obtained, it remains to determine the optimal primary solution to (P1.1) (or equivalently (P1)). Specifically, by replacing λ and μ with λ^{opt} and μ^{opt} in Lemma 3.2, one can obtain the optimal $(t^{\text{opt}}, \ell^{\text{opt}})$ for (P1) in a semi-closed form. Furthermore, by substituting ℓ^{opt} in Lemma 3.1, one can then obtain the optimal local CPU frequencies $\{f_{i,n}^{\text{opt}}\}$ for the K users. However, we cannot directly obtain the optimal energy transmit covariance matrix Q^{opt} for (P1) from the solution to problem (15), since its solution is non-unique in general. Therefore, we adopt an additional step to obtain Q^{opt} by solving a semidefinite program (SDP), which corresponds to solving problem (P1.1) for Q under the given $(t^{\text{opt}}, \ell^{\text{opt}})$.

We can then readily establish the following proposition.

Proposition 3.1: The optimal solution $(\{f_{i,n}^{\text{opt}}\}, Q^{\text{opt}}, t^{\text{opt}}, \ell^{\text{opt}})$ for problem (P1) is given by

$$\ell_i^{\text{opt}} = \begin{cases} \left[R_i - \sqrt{\frac{T^2}{3\kappa_i C_i^3} \left(\frac{\alpha}{\lambda_i^{\text{opt}}} + \frac{\sigma^2 \ln 2}{B \tilde{g}_i} 2^{\frac{r_i^{\text{opt}}}{B}} \right)} \right]^+, & \text{if } \lambda_i^{\text{opt}} > 0, \\ 0, & \text{if } \lambda_i^{\text{opt}} = 0, \end{cases} \quad (21)$$

$$t_i^{\text{opt}} = \begin{cases} \ell_i^{\text{opt}} / r_i^{\text{opt}}, & \text{if } \lambda_i^{\text{opt}} > 0, \\ 0, & \text{if } \lambda_i^{\text{opt}} = 0, \end{cases} \quad (22)$$

$$f_{i,1}^{\text{opt}} = \dots = f_{i,C_i(R_i - \ell_i^{\text{opt}})}^{\text{opt}} = C_i(R_i - \ell_i^{\text{opt}})/T, \quad \forall i \in \mathcal{K}, \quad (23)$$

and

$$Q^{\text{opt}} = \arg \min_{Q \geq 0} T \text{tr}(Q) \\ \text{s.t.} \quad \frac{\kappa_i C_i^3 (R_i - \ell_i^{\text{opt}})^3}{T^2} + \frac{t_i^{\text{opt}}}{\tilde{g}_i} \beta \left(r_i^{\text{opt}} \right) + p_{c,i} t_i^{\text{opt}} - T \zeta \text{tr}(Q H_i) \leq 0, \quad \forall i \in \mathcal{K}, \quad (24)$$

where

$$r_i^{\text{opt}} \triangleq \frac{B}{\ln 2} \left(W_0 \left(\frac{\tilde{g}_i}{\sigma^2 e} \left(\frac{\mu^{\text{opt}}}{\lambda_i^{\text{opt}}} + p_{c,i} \right) - \frac{1}{e} \right) + 1 \right) \quad (25)$$

corresponds to the optimal offloading rate for user i , $\forall i \in \mathcal{K}$.

Proposition 3.1 can be verified by simply combining Lemmas 3.1 and 3.2; hence, we omit its detailed proof for conciseness. Note that (24) is an instance of SDP, which can thus be efficiently solved by off-the-shelf solvers, e.g., CVX [39].

Summarizing, we present Algorithm 1 to solve the computation latency constrained energy minimization problem (P1).

Algorithm 1 for Solving the Energy Minimization Problem (P1)

- 1: **Initialization:** Given an ellipsoid $\mathcal{E}((\lambda, \mu), \mathbf{A})$ containing $(\lambda^{\text{opt}}, \mu^{\text{opt}})$, where (λ, μ) is the center of \mathcal{E} and $\mathbf{A} > 0$ characterizes the volume of \mathcal{E} .
- 2: **Repeat:**
 - For each user $i \in \mathcal{K}$, obtain (t_i^*, ℓ_i^*) by Lemma 3.2 under given λ_i and μ ;
 - Compute the subgradients of the objective function and the constraints of (D1.1) as in Section III-B.2;
 - Update λ and μ using the ellipsoid method [38];
- 3: **Until** λ and μ converge within a prescribed accuracy.
- 4: **Set** $(\lambda^{\text{opt}}, \mu^{\text{opt}}) \leftarrow (\lambda, \mu)$.
- 5: **Output:** Obtain $(t^{\text{opt}}, \ell^{\text{opt}})$, $\{f_{i,n}^{\text{opt}}\}$, and compute Q^{opt} by (24).

Remark 3.1: Proposition 3.1 shows that the optimal joint computing and offloading design has the following interesting properties to minimize the energy consumption at the AP.

- 1) First, if the energy harvesting constraint is not tight for user i (i.e., user i harvests sufficient wireless energy), then no computation offloading is required and user i should compute all the tasks locally (i.e., $\ell_i^{\text{opt}} = 0$). This can be explained based on the complementary slackness condition [37], i.e.,

$$\lambda_i^{\text{opt}} \left(\frac{\kappa_i C_i^3 (R_i - \ell_i^{\text{opt}})^3}{T^2} + \frac{t_i^{\text{opt}}}{\tilde{g}_i} \beta \left(r_i^{\text{opt}} \right) + p_{c,i} t_i^{\text{opt}} - T \zeta \text{tr}(Q^{\text{opt}} H_i) \right) = 0, \quad \forall i \in \mathcal{K}. \quad (26)$$

In this case, if the energy harvesting constraint is not tight for user i , then based on (26) we have $\lambda_i^{\text{opt}} = 0$, and accordingly $\ell_i^{\text{opt}} = 0$ holds from (21). This property is intuitive: when the user has sufficient energy to accomplish the tasks locally, there is no need to employ computation offloading that incurs additional energy consumption for the MEC server's computation at the AP.

- 2) Next, it is always beneficial to leave some bits for local computing at each user $i \in \mathcal{K}$, i.e., $\ell_i^{\text{opt}} < R_i$ always holds (see (21)). In other words, offloading all the bits to the AP is always suboptimal. This is because when $\ell_i^{\text{opt}} \rightarrow R_i$, the marginal energy consumption of local

computing is almost zero, and thus it is beneficial to leave some bits for local computing in this case.

- 3) Furthermore, it is observed that for each user i , more stringent the energy harvesting constraint is (or the associated dual variable λ_i^{opt} is larger), more bits should be offloaded to the AP with a smaller offloading rate r_i^{opt} . This property follows based on (21) and (25), in which a larger λ_i^{opt} admits a larger ℓ_i^{opt} and a smaller r_i^{opt} .
- 4) Finally, the number of offloaded bits ℓ_i^{opt} and the offloading rate r_i^{opt} for each user i are affected by the channel gain \tilde{g}_i , the block length T , the circuit power $p_{c,i}$, and the MEC energy consumption α per offloaded bit in the following way: 1) when the channel condition becomes better (i.e., \tilde{g}_i becomes larger), both ℓ_i^{opt} and r_i^{opt} increase, and thus user i is likely to offload more bits with a higher offloading rate; 2) a higher circuit power $p_{c,i}$ at the user leads to a higher offloading rate r_i^{opt} ; 3) when T or α increases, ℓ_i^{opt} reduces and thus fewer bits are offloaded to the AP.

IV. NUMERICAL RESULTS

In this section, numerical results are provided to gauge the performance of the proposed design with **joint WPT, offloading, and computing optimization**, as compared to the following four benchmark schemes.

- 1) *Local computing only*: each user $i \in \mathcal{K}$ accomplishes its computation task by only local computing. This scheme corresponds to solving problem (P1) by setting $\ell_i = 0$, $\forall i \in \mathcal{K}$.
- 2) *Computation offloading only*: each user $i \in \mathcal{K}$ accomplishes its computation task by fully offloading the computation bits to the AP. This scheme corresponds to solving (P1) by setting $f_{i,n} = 0$, $\forall n$, $\forall i \in \mathcal{K}$, as well as $\ell_i = R_i$ for (P1), $\forall i \in \mathcal{K}$.
- 3) *Joint design with isotropic WPT*: the N -antenna AP radiates the RF energy isotropically or omni-directionally over all directions by setting $\mathbf{Q} = p\mathbf{I}$, where $p \geq 0$ denotes the transmit power at each antenna. This scheme corresponds to solving problem (P1) by replacing \mathbf{Q} as $p\mathbf{I}$ with p being another optimization variable.
- 4) *Separate MEC-WPT design*: this scheme separately designs the computation offloading for MEC and the energy beamforming for WPT [21], [32]. First, the K users minimize their sum-energy consumption subject to the users' individual computation latency constraints [32]. Then, under the constraints of energy demand at the K users, the AP designs the transmit energy beamforming with minimum energy consumption [21].

In the simulations, the EH efficiency is set as $\zeta = 0.3$. The system parameters are set as (unless stated otherwise): the number of the AP antennas $N = 4$, $C_i = 10^3$ cycles/bit, $\kappa_i = 10^{-28}$, $\forall i \in \mathcal{K}$ [32], the circuit power $p_{c,i} = 10^{-4}$ Watt (W), the energy consumption per offloaded bit by the MEC server $\alpha = 10^{-4}$ Joule/bit, the receiver noise power $\sigma^2 = 10^{-9}$ W, and the spectrum bandwidth for offloading $B = 2$

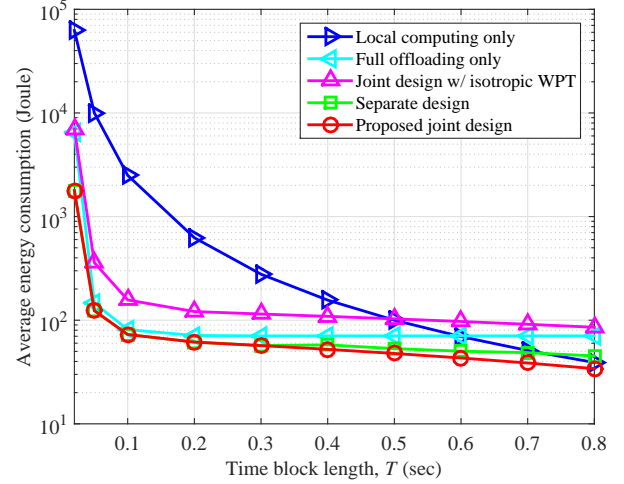


Fig. 3. The average energy consumption at the AP versus the block length T .

MHz. By considering a Rayleigh fading channel model, the wireless channel from the AP to each user $i \in \mathcal{K}$ is set as

$$\mathbf{h}_i = \theta_0 d_i^{-3} \bar{\mathbf{h}}_i, \quad \mathbf{g}_i = \theta_0 d_i^{-3} \bar{\mathbf{g}}_i, \quad (27)$$

where $\bar{\mathbf{h}}_i \sim \mathcal{CN}(0, \mathbf{I})$ and $\bar{\mathbf{g}}_i \sim \mathcal{CN}(0, \mathbf{I})$, $\forall i \in \mathcal{K}$, is an independent and identically distributed (i.i.d.) circularly symmetric complex Gaussian (CSCG) random vector with zero mean and covariance \mathbf{I} ; $\theta_0 = 6.25 \times 10^{-4}$ (i.e., -32 dB) corresponds to the channel power gain at a reference distance of one meter; d_i denotes the distance from the AP to user $i \in \mathcal{K}$; and the path-loss exponent is assumed to be 3. The numerical results are obtained by averaging over 500 randomized channel realizations. Note that the simulation parameters are specifically chosen, but our approaches can be also applied to other system setups.

A. Case with Homogeneous Users

First, we consider the case with homogeneous users, where the distances from the AP to all the users are identical with $d_i = 5$ meters, $\forall i \in \mathcal{K}$. The corresponding average power loss is set to be 5×10^{-6} (i.e., -53 dB). Additionally, the numbers of computation bits at all users are set to be identical, i.e., $R = R_i$, $\forall i \in \mathcal{K}$. Figs. 3–6 show the average energy consumption at the AP under different system parameters. It is observed that the proposed joint design achieves the lowest average energy consumption at the AP among all the five schemes. The joint design with isotropic WPT achieves a suboptimal performance due to the loss of multi-antenna energy beamforming gain. The suboptimal performance of the separate-design scheme implies the necessity of unified demand-supply optimization in wireless powered MEC systems.

Fig. 3 shows the average energy consumption at the AP versus the time block length T , where $R = 10$ kbits and $K = 10$. First, with a small value of T (e.g., $T = 0.05$ sec), the benchmark schemes but the local-computing-only scheme are observed to achieve a near optimal performance close to that with the proposed joint design, while as T increases, the energy

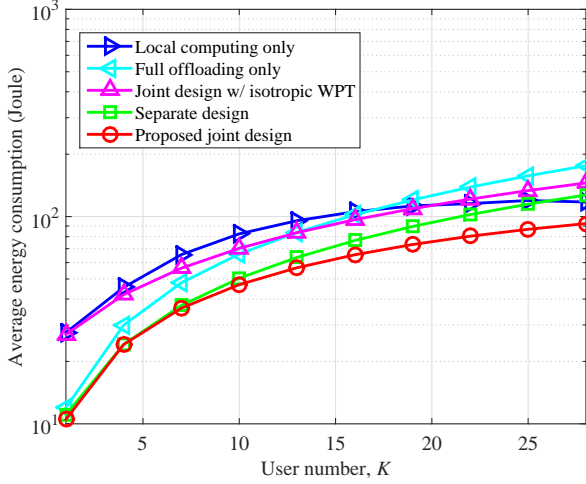


Fig. 4. The average energy consumption at the AP versus the user number K .

consumption with the local-computing-only scheme significantly decreases, approaching that with the proposed joint design. It is also observed that the energy consumption with the full-offloading-only scheme remains almost unchanged when $T \geq 0.1$ sec. This is due to the fact that in this case, the optimal offloading time for all users is fixed to be around 0.1 sec for saving the circuit energy consumption; hence, increasing T cannot further improve the energy efficiency in this case. By contrast, the energy consumption with the local-computing-only scheme decreases monotonically as T increases. This is because as T increases, one can always lower down the CPU frequency to save energy for local computing. Finally, it is seen in Fig. 3 both the separate-design and the equal-offloading-time-allocation schemes achieve a very similar performance in the interested time block regime.

Fig. 4 depicts the average energy consumption versus the user number K , where $R = 10$ kbits and $T = 0.5$ sec. It is shown that the gain achieved by the proposed joint design becomes more significant as the user number K becomes large. The full-loading-only scheme outperforms the local-computing-only scheme, but with a decreasing gain as K increases. This is because in the full-offloading-only scheme, all users share the finite time block and the offloading energy consumption would increase drastically when K becomes large. It is also observed that the performance of the equal-offloading-time-allocation scheme becomes closer to that of the proposed joint design with larger $K \geq 15$. This indicates that an equal offloading time is desirable for a large number of the users in order to minimize the energy consumption at the AP.

Fig. 5 shows the average energy consumption at the AP versus the number of computation bits R at each user, where $K = 2$ and $T = 0.05$ sec. It is shown that the average energy consumption by all the six schemes increases as R becomes large, and the full-offloading-only scheme outperforms the local-computing-only one, especially when R becomes large. This indicates that with large R values, it is desirable to offload more computation bits to the AP in order to reduce the energy

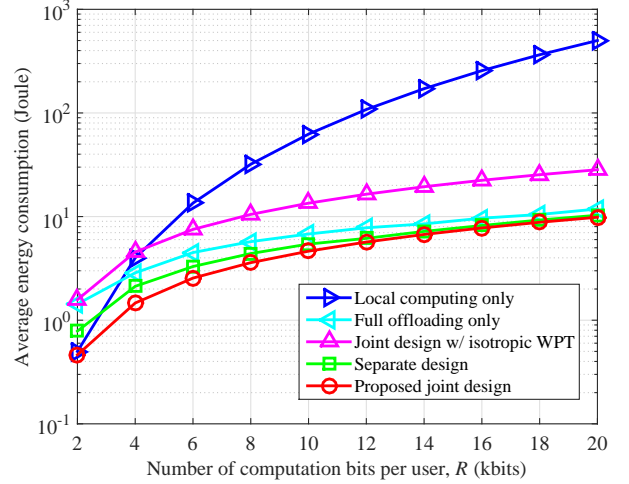


Fig. 5. The average energy consumption at the AP versus the number of computation bits R at each user.

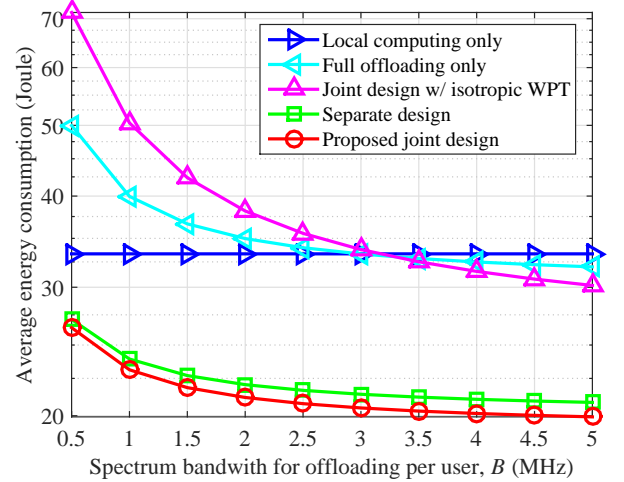


Fig. 6. The average energy consumption at the AP versus the spectrum bandwidth B for offloading.

consumption. Furthermore, the full-offloading-only scheme is observed to achieve a near optimal performance close to that with the proposed joint design when R becomes large. This is because the energy consumption per bit for offloading is significantly smaller than that for local computing in the large R case. It is also observed that the separate-design scheme outperforms all the other benchmark schemes in this setup.

Fig. 6 shows the average energy consumption versus the spectrum bandwidth B for offloading, where $K = 6$, $T = 0.5$ sec, and $R = 50$ kbits. As expected, Fig. 6 shows that the energy consumption by the four schemes with offloading decreases as B increases, and the one by the local-computing-only scheme remains unchanged. This indicates that a large value of B not only implies a high offloading rate, but also helps save the energy consumption in computation offloading. It is also observed that at small B values (e.g., $B \leq 3$ MHz), the local-computing-only scheme outperforms the full-offloading-only scheme, but it does not hold for large B cases. This

TABLE I
OFFLOADED BITS AND RESIDUAL ENERGY AT USERS FOR THE PROPOSED OPTIMAL JOINT DESIGN UNDER DIFFERENT d_2 .

d_2 (meters)	2	3	4	5	6	7	8
Near user's offloaded bits ℓ_1^{opt} (kbits)	0.165	0.142	0.124	0.092	0.068	0.028	0.006
Far user's offloaded bits ℓ_2^{opt} (kbits)	1.798	6.974	11.817	13.682	13.585	12.972	12.162
Near user's residual energy ($\times 10^{-5}$ Joule)	0.007	0.026	0.062	0.531	3.276	9.218	21.105
Far user's residual energy ($\times 10^{-5}$ Joule)	0.003	0	0	0	0	0	0

TABLE II
OFFLOADED BITS AND RESIDUAL ENERGY AT USERS FOR THE PROPOSED OPTIMAL JOINT DESIGN UNDER DIFFERENT R_2 .

R_2 (kbits)	10	20	30	40
Near user's offloaded bits ℓ_1^{opt} (kbits)	0.825	0.432	0.239	0.282
Far user's offloaded bits ℓ_2^{opt} (kbits)	3.586	13.62	23.791	33.264
Near user's residual energy ($\times 10^{-5}$ Joule)	0.426	3.317	6.42	9.545
Far user's residual energy ($\times 10^{-5}$ Joule)	0	0	0	0

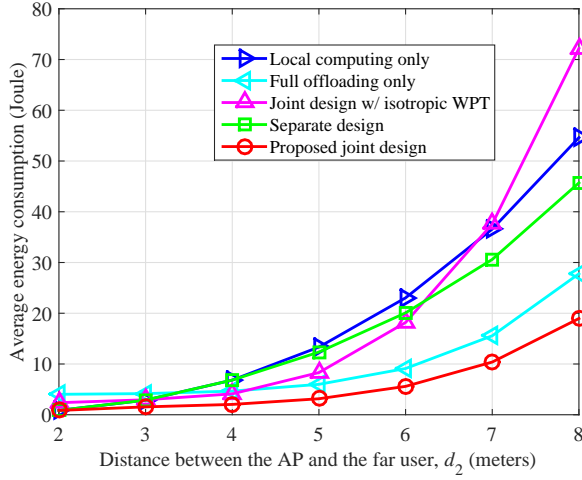


Fig. 7. The average energy consumption at the AP versus the distance d_2 from the AP to the far user.

indicates that offloading becomes a better option than local-computing as B increases.

B. Case with Heterogeneous Users

Next, we evaluate the performance of the wireless powered MEC system in the case with heterogeneous users. For the purpose of illustration, we focus on the scenario with only $K = 2$ users. It is assumed that the distances from the AP to the two users (namely near and far users) are $d_1 = 2$ meters and $d_2 \geq 2$ meters, respectively. The time block length is set as $T = 0.2$ sec.

Fig. 7 shows the average energy consumption versus the distance d_2 from the AP to the far user, where the computation task sizes for both users are set as $R_1 = R_2 = 20$ kbits. It is observed that as d_2 increases, the energy consumption by the six schemes increases significantly, and the proposed joint design achieves the lowest energy consumption among them. The local-computing-only scheme is observed to outperform the full-offloading-only scheme when $d_2 < 3$ meters, but performs inferior to the full-offloading-only scheme when

$d_2 > 3$ meters. Furthermore, the proposed joint design is observed to achieve a significant performance gain over the separate-design one when $d_2 > 4$ meters.

To provide more insights, Table I demonstrates the numbers of offloaded bits ℓ_i^{opt} s at both users and their residual energy (i.e., $E_i - E_{\text{loc},i} - E_{\text{off},i}$) under different values of d_2 for the proposed joint design. It is observed that the far user prefers offloading significantly more bits than the near user, especially at a larger d_2 . As d_2 increases, the number of offloaded bits ℓ_1^{opt} by the near user decreases significantly, while that by the far user (i.e., ℓ_2^{opt}) increases. This result is generally consistent with the first property in Remark 3.1. Furthermore, it is observed that the residual energy at the near user increases dramatically as d_2 increases, while the far user always uses up all its energy when $d_2 > d_1$. This shows that as d_2 increases, the energy consumption increase at the AP in Fig. 7 is mainly to satisfy the energy requirement at the far user. In this case, the near user will harvest a lot of energy.

Furthermore, we consider the case when the near and far users have distinct computation task sizes. Fig. 8 depicts the average energy consumption versus the computation task size R_2 at the far user, where $R_1 = 20$ kbits and $d_2 = 6$ meters. It is observed that the energy consumption by the six schemes increases as R_2 increases, and both the local-computing-only and the separate-design schemes lead to much higher energy consumption than the other four schemes when $R_2 > 20$ kbits. This is due to the fact that in the local-computing-only and the separate-design schemes, the far user cannot explore the benefit of task offloading for energy saving. Among the five benchmark schemes, the full-offloading-only scheme achieves the best performance close to the optimal proposed one. Table II presents the numbers of offloaded bits at both users and their residual energy for the proposed joint design. It is observed that as R_2 increases, the number of offloaded bits ℓ_1^{opt} by the near user decreases, while ℓ_2^{opt} by the far user increases significantly. Similarly as in Table I, ℓ_2^{opt} is observed to be significantly larger than ℓ_1^{opt} . It is also observed that with R_2 increasing, the residual energy at the near user becomes more significant, while that at the far user is zero.

Tables I and II show that when the users are heterogeneous

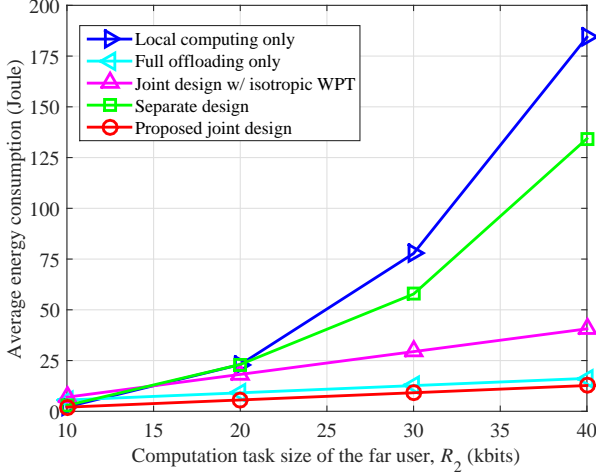


Fig. 8. The average energy consumption at the AP versus the computation task size R_2 kbits.

in locations and/or task sizes, even the optimal joint design still leads to unbalanced energy demand and supply at these users. In particular, the AP needs to use a large transmit power to satisfy the high energy demand of users that are far apart and/or have heavy computation tasks. At the same time, the nearby users with light computation tasks can accordingly harvest more energy and are likely to have energy surplus. To better balance the energy demand and surplus, it can be viable to enable user cooperation between near and far users, which is an interesting research direction worth pursuing in future work.

V. CONCLUSION

We developed a unified MEC-WPT design framework with joint energy beamforming, offloading, and computing optimization in emerging wireless powered multiuser MEC systems. In particular, we proposed an efficient wireless powered multiuser MEC design by considering the latency-constrained computation, for which the AP minimizes the total energy consumption subject to the users' individual computation latency constraints. Leveraging the Lagrange duality method, we obtained the optimal solution in a semi-closed form. Numerical results demonstrated the merits of the proposed joint design over alternative benchmark schemes. The proposed unified MEC-WPT design can pave the way to facilitate ubiquitous computing for IoT devices in a self-sustainable way.

APPENDICES

A. Proof of Lemma 3.1

First, consider the case when there exists some user i with $\ell_i = R_i$, i.e., user i offloads all of its computation task bits to the AP. As user i does not perform local computing in this case, the local CPU frequency of user i is evidently zero.

We next consider the nontrivial case of $0 \leq \ell_i < R_i$, $\forall i \in \mathcal{K}$. Define $f_i \triangleq \frac{\sum_{n=1}^{C_i(R_i-\ell_i)} f_{i,n}}{C_i(R_i-\ell_i)}$, $\forall i \in \mathcal{K}$. Since that both $1/x$ and x^2

are convex functions with respect to $x > 0$, based on Jensen's inequality [37], it follows that

$$C_i(R_i - \ell_i)/f_i \leq \sum_{n=1}^{C_i(R_i-\ell_i)} 1/f_{i,n} \quad (28a)$$

$$C_i(R_i - \ell_i)\kappa_i f_i^2 \leq \sum_{n=1}^{C_i(R_i-\ell_i)} \kappa_i f_{i,n}^2, \quad (28b)$$

where both the equalities hold if and only if

$$f_{i,1} = \dots = f_{i,C_i(R_i-\ell_i)}, \quad \forall i \in \mathcal{K}. \quad (29)$$

As a result, the optimality of problem (P1) is achieved when (29) holds. Therefore, by replacing $f_i \triangleq f_{i,n}$, $\forall n$, problem (P1) is equivalently expressed as

$$\min_{Q \geq 0, \mathbf{t}, \ell, \{f_i\}} T \text{tr}(\mathbf{Q}) + \sum_{i=1}^K \alpha \ell_i \quad (30a)$$

$$\text{s.t.} \quad C_i(R_i - \ell_i)/f_i \leq T, \quad \forall i \in \mathcal{K} \quad (30b)$$

$$\begin{aligned} \kappa_i C_i(R_i - \ell_i) f_i^2 + \frac{t_i}{g_i} \beta \left(\frac{\ell_i}{t_i} \right) + p_{c,i} t_i \\ - T \zeta \text{tr}(\mathbf{Q} \mathbf{H}_i) \leq 0, \quad \forall i \in \mathcal{K} \end{aligned} \quad (30c)$$

$$\sum_{i=1}^K t_i \leq T, \quad t_i \geq 0, \quad 0 \leq \ell_i \leq R_i, \quad \forall i \in \mathcal{K}. \quad (30d)$$

For a given (\mathbf{t}, ℓ) , it is evident that the optimal f_i 's for (30) (equivalent (P1)) should be as small as possible by (30c). Since f_i is bounded below by $C_i(R_i - \ell_i)/T$ in (30b), it follows that the optimal f_i 's are

$$f_i = C_i(R_i - \ell_i)/T, \quad \forall i \in \mathcal{K}. \quad (31)$$

It then readily follows that, at optimum of (P1), $f_{i,1} = \dots = f_{i,C_i(R_i-\ell_i)} = C_i(R_i - \ell_i)/T$, $\forall i \in \mathcal{K}$.

B. Proof of $\mathbf{F}(\lambda) \geq \mathbf{0}$

$\mathbf{F}(\lambda) \geq \mathbf{0}$ can be verified by contradiction. Assume that $\mathbf{F}(\lambda)$ is not positive semidefinite. Denote by ξ one eigenvector corresponding to the negative eigenvalue of $\mathbf{F}(\lambda)$. By setting $\mathbf{Q} = \tau \xi \xi^H \geq \mathbf{0}$ with τ going to infinity (which is feasible for (13)), it follows that

$$\lim_{\tau \rightarrow +\infty} \text{tr}(\mathbf{Q} \mathbf{F}(\lambda)) = \lim_{\tau \rightarrow +\infty} \tau \xi^H \mathbf{F}(\lambda) \xi = -\infty, \quad (32)$$

which in turn implies that the objective value in (13) is unbounded below over $\mathbf{Q} \geq \mathbf{0}$. Therefore, in order for the dual function value $\Phi(\lambda, \mu)$ to be bounded below, we need $\mathbf{F}(\lambda) \geq \mathbf{0}$.

C. Proof of Lemma 3.2

Given $(\lambda, \mu) \in \mathcal{S}$, we solve problem (16) for each user $i \in \mathcal{K}$. When $\lambda_i = 0$, the objective function in (16) becomes $\alpha \ell_i + \mu t_i$. It is evident that $t_i^* = 0$ and $\ell_i^* = 0$ are optimal for (16).

For $\lambda_i > 0$, the Lagrangian of (16) is given by

$$\begin{aligned} \mathcal{L}_i = & \alpha \ell_i + \frac{\lambda_i \kappa_i C_i^3(R_i - \ell_i)^3}{T^2} + \frac{\lambda_i t_i}{g_i} \beta \left(\frac{\ell_i}{t_i} \right) + \lambda_i p_{c,i} t_i \\ & + \mu t_i + \gamma_i(\ell_i - R_i) - \nu_i \ell_i - \eta_i t_i, \end{aligned} \quad (33)$$

where γ_i , ν_i , and η_i are the non-negative Lagrangian multipliers associated with $\ell_i \leq R_i$, $\ell_i \geq 0$, and $t_i \geq 0$, respectively. Based on the KKT conditions [37], the necessary and sufficient conditions for the optimal primal-dual point $(t_i^*, \ell_i^*, \gamma_i^*, \nu_i^*, \eta_i^*)$ are

$$t_i^* \geq 0, \quad 0 \leq \ell_i^* \leq R_i \quad (34a)$$

$$\gamma_i^* \geq 0, \quad \nu_i^* \geq 0, \quad \eta_i^* \geq 0 \quad (34b)$$

$$\gamma_i^*(\ell_i^* - R_i) = 0, \quad \nu_i^*\ell_i^* = 0, \quad \eta_i^*t_i^* = 0 \quad (34c)$$

$$\frac{\lambda_i}{\tilde{g}_i} \left(\beta \left(\frac{\ell_i^*}{t_i^*} \right) - \frac{\ell_i^*}{t_i^*} \beta' \left(\frac{\ell_i^*}{t_i^*} \right) \right) + \lambda_i p_{c,i} + \mu - \eta_i^* = 0 \quad (34d)$$

$$\alpha - \frac{3\lambda_i \kappa_i C_i^3 (R_i - \ell_i^*)^2}{T^2} + \frac{\lambda_i}{\tilde{g}_i} \beta' \left(\frac{\ell_i^*}{t_i^*} \right) + \gamma_i^* - \nu_i^* = 0, \quad (34e)$$

where $\beta'(x) \triangleq \frac{\sigma^2 \ln 2}{B} 2^{\frac{x}{B}}$ is the first-order derivative of $\beta(x)$ with respect to x . Note that (34c) denotes the complementary slackness condition, while the left-hand-side terms of (34d) and (34e) are the first-order derivatives of \mathcal{L}_i with respect to t_i^* and ℓ_i^* , respectively. For the function $y = \beta(x) - x\beta'(x)$ of $x > 0$, its inverse function can be shown to be [36]

$$x = \frac{B}{\ln 2} \left(W_0 \left(-\frac{y}{\sigma^2 e} - \frac{1}{e} \right) + 1 \right). \quad (35)$$

Let $r_i^* \triangleq \ell_i^*/t_i^*$. From (34b) and (34d), we have $\beta(r_i^*) - r_i^* \beta'(r_i^*) = -\tilde{g}_i \left(\frac{\mu}{\lambda_i} + p_{c,i} \right)$. Based on (35), it follows that

$$r_i^* = \frac{B}{\ln 2} \left(W_0 \left(\frac{\tilde{g}_i}{\sigma^2 e} \left(\frac{\mu}{\lambda_i} + p_{c,i} \right) - \frac{1}{e} \right) + 1 \right). \quad (36)$$

Since $W_0(x)$ is a monotonically increasing function of $x \geq -\frac{1}{e}$ and $W_0(-\frac{1}{e}) = -1$ [36], it follows that $r_i^* > 0$ with non-zero $p_{c,i}$. From (34c) and (34e), it is immediate that

$$\ell_i^* = \left[R_i - \sqrt{\frac{1}{3\kappa_i C_i^3} \left(\frac{\alpha}{\lambda_i} + \frac{\sigma^2 \ln 2}{B \tilde{g}_i} 2^{\frac{r_i^*}{B}} \right)} \right]^+. \quad (37)$$

With (36) and (37), the optimal t_i^* is then obtained as $t_i^* = \ell_i^*/r_i^*$.

D. Proof of Lemma 3.3

The positive semidefinite constraint $\mathbf{F}(\boldsymbol{\lambda}) \geq \mathbf{0}$ can be equivalently expressed as a scalar inequality constraint as [37]

$$\pi(\boldsymbol{\lambda}) \triangleq \min_{\|\boldsymbol{\xi}\|=1} \boldsymbol{\xi}^H \mathbf{F}(\boldsymbol{\lambda}) \boldsymbol{\xi} \geq 0. \quad (38)$$

Given a query point $\boldsymbol{\lambda}_1 \triangleq [\lambda_{1,1}, \dots, \lambda_{1,K}]^\top$, one can find the normalized eigenvector \mathbf{v}_1 of $\mathbf{F}(\boldsymbol{\lambda}_1)$ corresponding to the smallest eigenvalue of $\mathbf{F}(\boldsymbol{\lambda}_1)$ (i.e., $\pi(\boldsymbol{\lambda}_1)$). Consequently, we can determine the value of the scalar constraint at a query point as $\pi(\boldsymbol{\lambda}_1) = \mathbf{v}_1^H \mathbf{F}(\boldsymbol{\lambda}_1) \mathbf{v}_1$. To obtain a subgradient, we have

$$\begin{aligned} \pi(\boldsymbol{\lambda}) - \pi(\boldsymbol{\lambda}_1) &= \min_{\|\boldsymbol{\xi}\|=1} \boldsymbol{\xi}^H \mathbf{F}(\boldsymbol{\lambda}) \boldsymbol{\xi} - \mathbf{v}_1^H \mathbf{F}(\boldsymbol{\lambda}_1) \mathbf{v}_1 \\ &\leq \mathbf{v}_1^H (\mathbf{F}(\boldsymbol{\lambda}) - \mathbf{F}(\boldsymbol{\lambda}_1)) \mathbf{v}_1 \end{aligned} \quad (39a)$$

$$= \sum_{i=1}^K (\lambda_{1,i} - \lambda_i) \zeta \mathbf{v}_1^H \mathbf{H}_i \mathbf{v}_1, \quad (39b)$$

where the last equality follows from the affine structure of $\mathbf{F}(\cdot)$ in (14b). By the weak subgradient calculus [37], the subgradient of $\mathbf{F}(\boldsymbol{\lambda})$ at the given $\boldsymbol{\lambda}$ and μ is then

$$[\zeta \mathbf{v}^H \mathbf{H}_1 \mathbf{v}, \dots, \zeta \mathbf{v}^H \mathbf{H}_K \mathbf{v}, 0]^\top, \quad (40)$$

where \mathbf{v} is the eigenvector corresponding to the smallest eigenvalue of $\mathbf{F}(\boldsymbol{\lambda})$, and the last zero entry follows from the fact that $\pi(\boldsymbol{\lambda})$ is independent of μ .

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