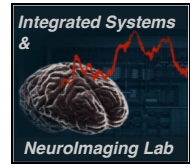


# Jiazhen Hong

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## EDUCATION

<b>Rutgers University, New Brunswick, NJ, USA</b> <i>Ph.D. candidate</i>	<i>01/2020 – Present</i>
Computer Engineering, G.P.A.: 3.9/4.0 <b>Thesis Topic:</b> Artificial Intelligence for Time-Series Signal Processing in Brain-Computer Interfaces (BCIs)	
<b>Stevens Institute of Technology, Hoboken, NJ, USA</b> <i>M.Sc.</i>	<i>09/2017 – 01/2019</i>
Electrical Engineering, G.P.A.: 3.9/4.0	
<b>Jimei &amp; Chung Yuan Christian University, China</b> <i>B.Sc. (Double Degree)</i>	<i>09/2012 – 06/2016</i>
Communication Engineering, G.P.A.: 3.5/4.0	

## WORKING EXPERIENCE

<b>Integrated Systems &amp; Neuroimaging Lab, Rutgers University</b> <i>Teaching/Research Assistant</i>	<i>01/2020 – Present</i>
<ul style="list-style-type: none"><li>Developed ChatBCI, a mind-controlled speller system for BCI integrating <b>LLMs</b>.</li><li>Designed a <b>Transformer</b>-based model to enhance character recognition rates in the P300 speller system.</li><li>Developed topographic image representation for video-like electroencephalography (EEG) signals using <b>TimeSformer</b>.</li><li>Created a channel selection method to improve the speed and efficiency of BCI systems for motor imagery tasks.</li><li>Developed a geometric approach to optimize the k-means algorithm, addressing issues with local minima.</li></ul>	
<b>EMOTIV, San Francisco, CA, USA</b> <i>Research Intern</i>	<i>10/2024 – Present</i>
<ul style="list-style-type: none"><li>Developed a <b>foundation model</b> for processing time-series signals in brain-computer interfaces.</li></ul>	
<b>Conference Reviewer</b>	
<ul style="list-style-type: none"><li>International Conference on Artificial Intelligence and Statistics (AISTATS)</li><li>IEEE International Symposium on Biomedical Imaging (ISBI)</li></ul>	<i>2023 – 2024</i> <i>2024</i>

## TECHNICAL SKILLS

- Python (PyTorch, TensorFlow, scikit-learn, Qt5, NumPy, Pandas)
- MATLAB® (EEGLAB, Digital Signal Processing toolbox, object-oriented programming)
- Hardware (Brain Products, actiCAP, Raspberry Pi, Arduino, VEX-brain)

## PUBLICATIONS

### (Accepted/Published)

- J. Hong**, F. Shamsi, and L. Najafizadeh, “A **Deep Learning** Framework Based on Dynamic Channel Selection for Early Classification of Left and Right Hand Motor Imagery Tasks,” Proc. of 44th Annual International Conference of the IEEE Engineering in Medicine and Biology Society (EMBC’22), Glasgow, Scotland, July 2022, pp. 3550-3553
- J. Hong**, L. Najafizadeh, “P3T: A **Transformer** Model for Enhancing Character Recognition Rates in P300 Speller Systems,” 58th Annual Asilomar Conference on Signals, Systems, and Computers
- J. Hong**, W. Wang and L. Najafizadeh, “ChatBCI: A Fast P300 Speller Brain-Computer Interface Incorporating **Generative AI**-Based Word Prediction,” 2024 IEEE Brain Discovery and Neurotechnology Workshop. (Spotlight) – Machine Learning and Computer Paradigms for Brain Discovery

### (Under review)

- J. Hong**, W. Qian, Y. Chen, and Y. Zhang, “A geometric approach to **k-means**,” submitted
- J. Hong** and L. Najafizadeh, “TopoEEG: a **TimeSformer**-Based Topographic **Image Representation** Method for Early Single-Trial Detection of P300,” submitted

### (In Preparation)

- J. Hong**, W. Wang, S. Haghani, and L. Najafizadeh, “Subject-specific Channel Selection Based on Davies- Bouldin Index for EEG Motor Imagery Classification,” in preparation
- J. Hong**, W. Wang, and L. Najafizadeh, “A Mind-Controlled Speller System Incorporating **LLMs** and P300-Based Brain-Computer Interfaces,” in preparation

## AWARDS

- Travel Award of 2024 IEEE Brain Discovery & Neurotechnology Workshop *09/2024*
- Best Teaching Assistant Award for Fall 2023, Rutgers ECE *05/2024*