

SoCLab Final Project Report

FPGA-based Instant Image Recognition on Convolutional Neural Network

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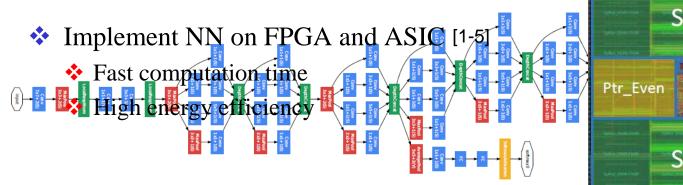
Advisor: Prof. An-Yeu Wu

Date: 2018/1/15

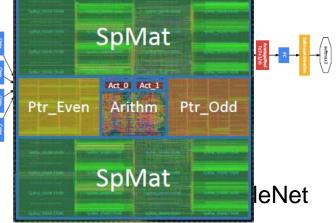


Background

- Neural networks have many breakthroughs in recent years
 - * Computer vision: image recognition, object detection
 - ❖ Speech domain: machine translation, chatbot
 - AlphaGo
- Bottleneck of deep neural network
 - **❖** Massive matrix multiplication
 - Long computation time
 - High energy consumption

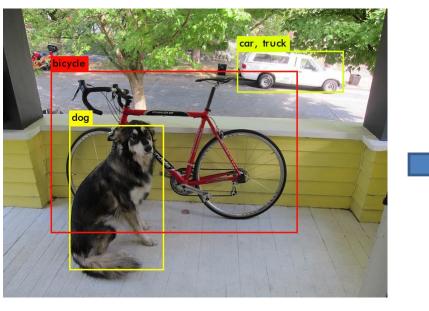




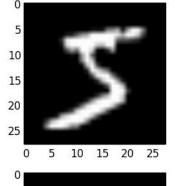


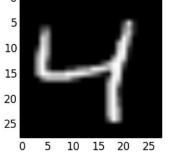


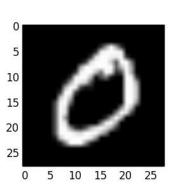
Our Target

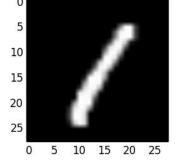










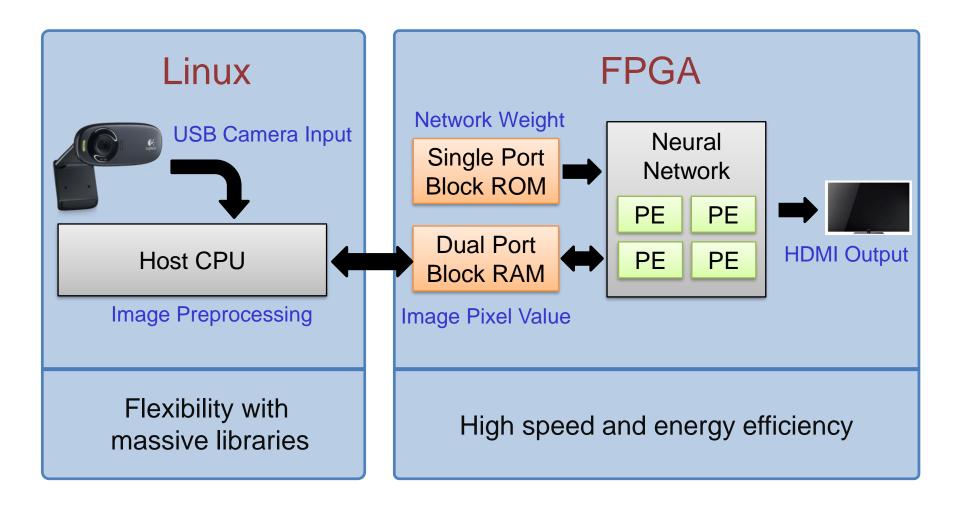


- Real-time object classification
 - One of the main techniques for self-driving cars

- Handwritten digits recognition
 - **Easier to implement**
 - Can be extended to more complex applications



Software/Hardware Co-design Overview [5]





HW: Neural Network

- Parameterized module
- Convolutional layer

•	Maxpooling	layer

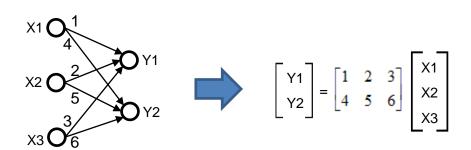
- Dense layer
- * Relu layer

$$f(x) = \max(0, x)$$

- Softmax layer
 - Find biggest value

22	15	1	3	60
42	5	38	39	7
28	9	4	66	79
0	2	25	12	17
9	14	2	51	3

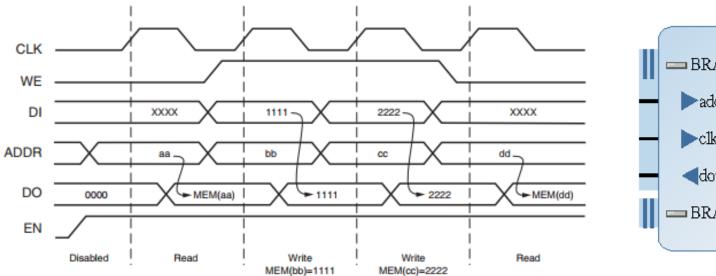
1	1	2	4		I
5	6	7	8	6	
3	2	1	0	3	İ
1	2	3	4		

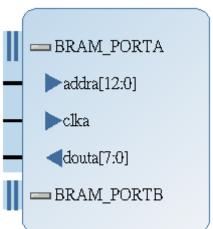




HW: Single Port Block ROM [6-7]

- Store pre-trained MNIST model weight
 - Quantize to eight bit
 - Multiply 4: maintain accuracy and avoid overflow
 - Save to .coe and load into BRAM.



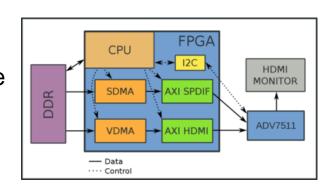


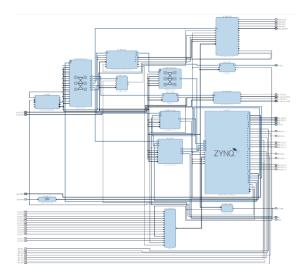
WRITE_FIRST Mode



HW: HDMI Output [8-10]

- FPGA
 - Insufficient time to make out how HDMI hardware works
- Embedded linux
 - Need HDMI driver
 - Hard to make HDMI hardware controllable
- Applicable tutorials and examples are few
 - Most are implemented in VGA
 - Many examples are no longer maintained
 - Bugs exist or version mismatch
- Replaced with seven-segment display and LED







SW: USB Camera Input [11-12]

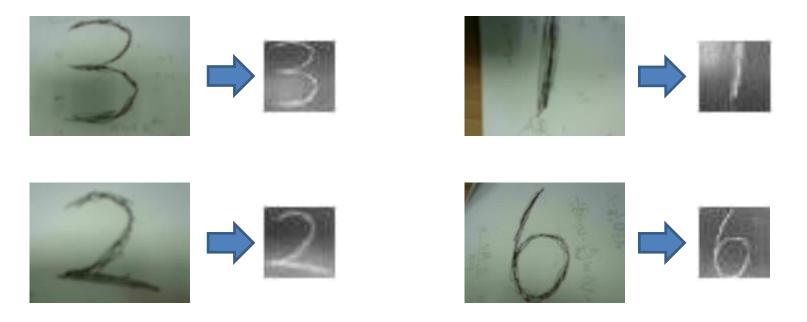
- USB camera uses H.264 as video compression standard
- Enable USB camera drivers @ kernel configuration for Linaro
- Video4Linux (V4L) is a collection of device drivers and an API for supporting real-time video capture on Linux systems
 - Suitable for USB webcam
- Open /dev/video0
- VIDIOC QUERYCAP
- VIDIOC_REQBUFS
- V4L2_MEMORY_MMAP
- VIDIOC_STREAMON
- VIDIOC_DQBUF
- VIDIOC_STREAMOFF





SW: Image Preprocessing

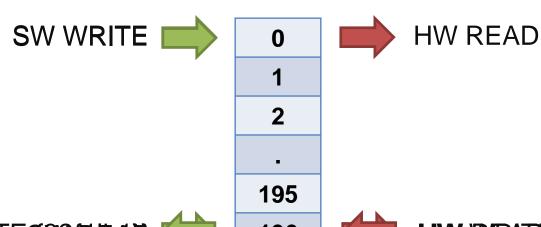
- The image for MNIST is 28x28 pixel and value from 0 to 1
- The image from webcam is 120x160 pixel and color
 - Cut picture to 120x120
 - Down-sample to 28x28
 - Color map to 0~1





HW: Dual Port Block RAM (1/3) [13-15]

- The bridge for HW and SW
 - SW write images into BRAM
 - HW read images from BRAM
- Image size: 28x28 pixels, 8 bits for each pixel
- → Need 32 bits (Width) x 196 (Depth) dual port BRAM
- Protocol between HW and SW



BRAM PORTA ▶addra[31:0] **⊳**clka ▶dina[31:0] douta[31:0] ena rsta wea[3:0] BRAM PORTB ▶addrb[31:0] **▶**clkb ▶dinb[31:0] doutb[31:0] **enh** rstb web[3:0]



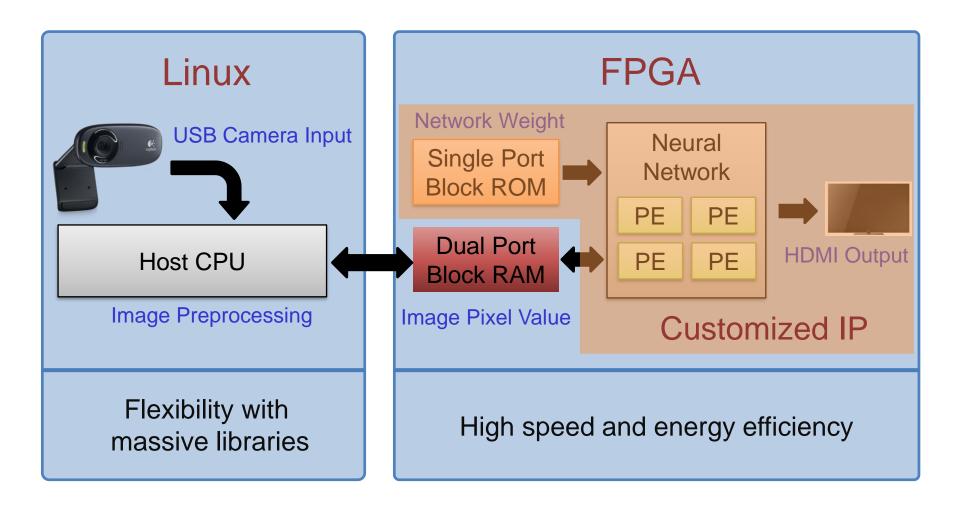
196



HW PARATE 0

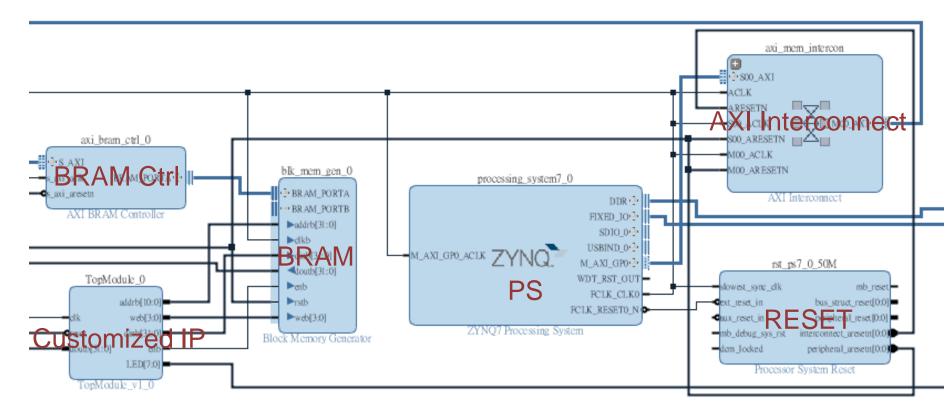


HW: Dual Port Block RAM (2/3) [13-15]





HW: Dual Port Block RAM (3/3) [13-15]



- Test on standalone
 - Can't read the value written into BRAM, always get 0
 - ❖ Use LED[7:0] as debug tool...





Simulation Result



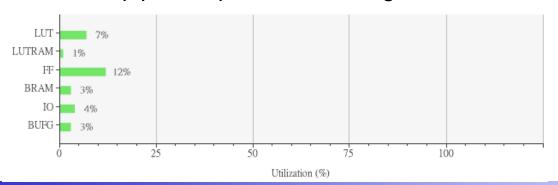
SW Write F

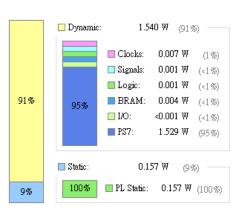
HW Calc

Predict Result3

Next Result 9

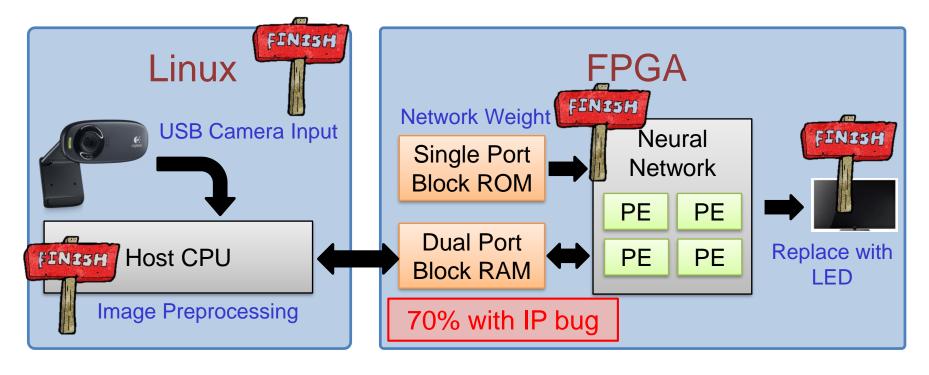
- Testbench performs ideally
 - Without pipeline, predict one image in about 8000 cycles







Conclusion



- * Hardware bug is every where
 - * Error messages are difficult to understand, FPGA bug, tool bug, version bug...
 - Experience-based
- ❖ Many thanks to TA奕達 & 俊棋學長



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Job Assignment

- ❖ 奕凡
 - Survey HDMI and try to fix bug
 - Image preprocessing to fit MNIST model
 - Slide (30%)
- ❖ 傑方
 - Neural network hardware
 - Train MNIST model and load weight to BRAM
 - Seven-segment display
 - USB camera driver on Linaro and software implement
 - Dual port block ram
 - ❖ Slide (70%)