AW305A Datasheet

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AW305A Features

CPU

- 32-bit DSP
- with IEEE754 Single precision FPU
- Icache
- 64Vectored interrupts
- 8 Levels interrupt priority
- Mathematic alaccelerate engine
- Support EMU

Memory

- On-chip SRAM (include cache)
- On-chip ROM
- Built-In Flash
- 4 region MPU protects

Clocks

- On-chip 16 MHz clock oscillator
- On-chip 200 kHz lower-temperature-drift clock oscillator
- 24 MHz crystal oscillator
- 32.768 KHz crystal oscillator

Audio

- One channel 16-bit DAC,SNR maximum 91dB
- One channel 16-bit ADC,SNR maximum 95dB
- One channel 16-bit APA, SNR maximum 99dB
- Audio DAC Sampling rates of 8kHz/11.025kHz/16kHz/22.05kHz/24kHz/32kHz/44.1kHz/48kHz are supported
- Audio ADC Sampling rates of 8kHz/11.025kHz/16kHz/22.05kHz/24kHz/32 kHz/44.1kHz/48kHz are supported
- AUDIO ADC Support one analog MIC, supports single-end or differential MIC/LINEIN input
- Supported digital MIC inputs(IIS Port)
- Audio DAC supports single-ended mode, need connect PA to drive speaker

Audio APA

- Mono Class-D Speaker Amplifier
- Use PWM modulation technique, support 32/44.1/48kHz sample rate
- support single-end or differential output to drive 4 or 8 ohm speaker directly.

Bluetooth

- Compliant with Bluetooth V5.4+BLE specification (QDID:222830)
- Support AoA TX direction finding
- Meet class2 and class3 transmitting power requirement
- Maximum +6dbm transmitting power
- BLE receiver with minimum -98dBm sensitivity
- bap 1.0\pacs 1.0\ccp 1.0\mcp 1.0\micp 1.0\vcp 1.0\csip 1.0

LP Touch

3-channel LP Touch with low power wakeup

Peripherals

- One full speed USB OTG controller
- One SD host controller for eMMC/SD
- Four multi-function 32-bit timers, support capture and PWM mode
- Three UART interface.
- I2C Master/Slave interface
- Three SPI Master/Slave interface
- I2S AUDIO Master/Slave interface
- 8-channel 12-bit ADC for analog sampling
- One CAN interface
- 17 Individually programmable and multiplexed GPIO pins
- Support IO function remapping
- Up to 17 external interrupt/wake-up source(low power available,can be multiplexed to any I/O)



PMU

Built-in LDO

Minimum 3uA current consumption in the soft-off mode

RTC with 32.768k osc

VPWR range: 1.8V to 5.5VIOVDD range: 2.2V to 3.4V

Packages

SSOP24

Temperature

Operating temperature: -40°C to +85°C

Storage temperature: -65°C to +150°C

Applications

Bluetooth intercom

Bluetooth TV remote controls





1 Block Diagram

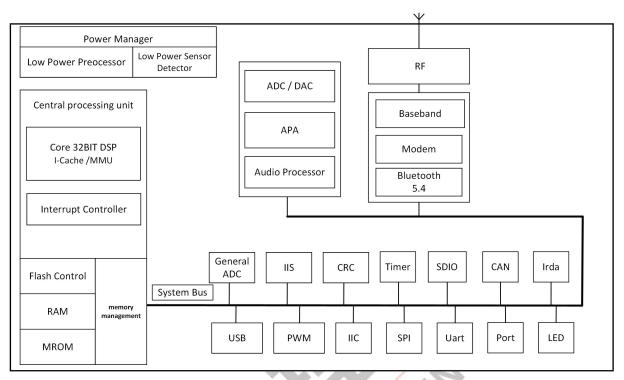


Figure 1-1 AW305A Block Diagram





2 Pin Definition

2.1 Pin Assignment

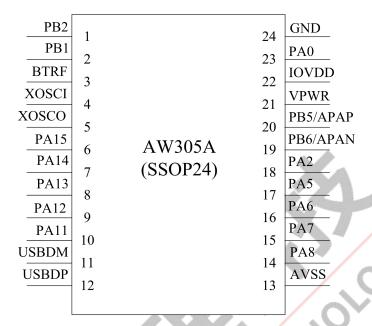


Figure 2-1 AW305A Package Diagram





2.2 Pin Description

Table 2-1 AW305A Pin Description

Touch5					
SPI2D3					
2 PB1 I/O GPIO OSCI_32K MCPWM_TMR2CK 3 RF Bluetooth RF antenna 4 XOSCI I Crystal Oscillator Input;					
2 PB1 I/O GPIO OSCI_32K MCPWM_TMR2CK 3 RF Bluetooth RF antenna 4 XOSCI I Crystal Oscillator Input;					
MCPWM_TMR2CK 3 RF Bluetooth RF antenna 4 XOSCI I Crystal Oscillator Input;					
3 RF Bluetooth RF antenna 4 XOSCI I Crystal Oscillator Input;					
4 XOSCI I Crystal Oscillator Input;					
5 XOSCO O Crystal Oscillator Output;					
and the second s	. 1				
ADC5: ADC Input Channel5					
6 PA15 I/O GPIO MCPWM_TMR0CK SPICLKA					
6 PA15 I/O GPIO SPICLKA					
UARTIRXA					
SD0CMDB					
ADC4: ADC Input Channel4					
MCPWM_FPIN2					
7 PA14 I/O GPIO SPIIDIA					
UARTITXA					
SD0DAT0B					
ADC3: ADC Input Channel3					
8 PA13 I/O GPIO MCPWM_FPIN1					
MCPWM_L1					
SPI1D3					
ADC differential input N					
9 PA12 I/O GPIO MCPWM_FPIN0					
	MCPWM_H1				
SPI1D2					
ADC differential input P					
10 PA11 I/O GPIO CLKOUT2					
SPI2DOC UART2RXC					
ADC6: ADC Input Channel6					
H. T. C. THOD D. W.					
11 LISDIM LO LIG SDA A					
(pull down) IIC_SDA_A SPI2DOB					
UARTIRXD					



				ADC6: ADC Input Channel6		
			GPIO	High Speed USB Data Positive		
12	USBDP	I/O	(pull down)	IIC_SCL_A		
			(puil down)	SPI2CLKB		
				UARTITXD		
13	AVSS	G		AUDIO Ground		
				ADC2: ADC Input Channel2		
14	PA8	I/O	GPIO	AIN_AP0: audio adc differential input AP0		
14	rAo	1/0	GPIO	ALNK_LRCKA		
				UART2RXB		
				AIN_AP4: audio adc differential input AP4		
15	PA7	I/O	GPIO	MIC_BIASCAP1		
13	1A/	1/0	GI IO	ALNK_SCLKA		
				UART2TXB		
				AIN_AP3: audio adc differential input AP3		
				AIN_AN: audio adc differential input N		
				CAP0		
16	PA6	I/O	GPIO	UART1_CTS		
10	1710	1.0	GHO	IIC_SDA_D		
				ALNK_DAT3A		
				SPI2DOA		
				UART0RXA		
				ADC1: ADC Input Channel1		
				AIN_AP2: audio adc differential input AP2		
		4		DAC: Digital-to-Analog Converter output channel		
17	PA5	I/O	GPIO	IIC_SCL_D		
		1		ALNK_DAT2A		
				SPI2CLKA		
		4		UART0TXA		
		The state of the s		Touch1		
		· ·		Long-press reset		
			GPIO	TMR2		
18	PA2	I/O	(pull up)	PWM1		
				ALNK_MCLKA		
				SPIICLKC		
			CDIO	UARTIRXC		
	PB6	I/O	GPIO			
19			(High Voltage Resistant)			
	APAN	О		APA differential output N		
			GPIO	ALNK_DAT1B		
20	PB5	I/O	(High Voltage Resistant)	SPI2DIB		
				UART1RXB		
	APAP	О		APA differential output P		



21	VPWR	P		Power supply 5v
22	IOVDD	P		IO Power 3.3v
23	PA0	I/O	GPIO (pull down) (High Voltage Resistant)	TMR0 PWM2
24	GND	G		Ground;

Note: Timer, IIC, ALNK, SPI, UART, SD, CAN function can be remapped to any I/O

Pin Type	Description	Pin Type	Description
P	Power	I/O	Input or Output
PO	Power Output	Ι	Input
PI	Power Input	0	Output
G	Ground	RF	RF antenna
AO	Analog Output		

3 Electrical Characteristics

3.1 Absolute Maximum Ratings

Table 3-1

Symbol	Parameter	Min	Max	Unit
Topt	Operating temperature	-40	+85	°C
Tstg	Storage temperature	-65	+150	°C
VPWR	Supply Voltage	-0.3	6	V
V_{IOVDD}	Voltage applied at IOVDD	-0.3	3.6	V
$ m V_{GPIO}$	Voltage applied to GPIO(Except PA0 /PB5/PB6)	-0.3	3.6	V
V _{HVTIO}	Voltage applied to High Voltage Resistant IO (PA0/PB5/PB6)	-0.3	+5.5	V

Note: The chip can be damaged by any stress in excess of the absolute maximum ratings listed below



3.2 PMU Characteristics

Table 3-2

Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions			
VPWR	Voltage Input	1.8	1	5.5	V				
Operating mode									
IOVDD	Voltage output	-	3.0	_	V	VPWR = 3.3V, 10mA loading			
IOVDD	Loading current	oading current 120 mA IOVE		IOVDD=3.0V@VPWR = 3.3V					
Low Power n	Low Power mode								
IOVDD	Loading current	-	-	20	mA	IOVDD=3.2V@VPWR = 3.7V			

3.3 IO Input/Output Electrical Logical Characteristics

Table 3-3

GPIO (Exc	GPIO (Except PA0/PB5/PB6) input characteristics										
Symbol	Parameter	Min		Тур	Max	Unit	Test Conditions				
V _{IL}	Low-Level Input Voltage	-(0.3		1.4	V	IOVDD = 3.2V				
$V_{ m IH}$	High-Level Input Voltage	1	.8		3.6	V	IOVDD = 3.2V				
High Volta	ge Resistant IO (PA0/F	B5/PB	6) input	character	ristics						
Symbol	Parameter	M	lin	Тур	Max	Unit	Test Conditions				
V _{IL}	Low-Level Input Voltage	-0	0.3		1.4	V	IOVDD = 3.2V				
V_{IH}	High-Level Input Voltage	1	1.8		5.5	V	IOVDD = 3.2V				
GPIO & Hi	igh Voltage <mark>Resistant</mark> I	O outp	ut char	acteristics							
Symbol	Parameter		GPIO D		Тур	Unit	Test Conditions				
Vol	0.1* IOVDD Drive co	Drive current		PA5~PA8 1~PA15 1,PB2, PB5,PB6 BDM SBDP	HD=0:-3 HD=1:-8 HD=2:-20 HD=3:-40~-50	mA	IOVDD = 3.2V				
V _{OH}	0.9* IOVDD Drive current		PA2,PA5~PA8 PA11~PA15 PB1,PB2, PA0,PB5,PB6 USBDM USBDP		HD=0:3 HD=1:8 HD=2:20 HD=3:40~50	mA	IOVDD = 3.2V				



3.4 Internal Resistor Characteristics

Table 3-4

Port	Internal Pull-Up Resistor	Internal Pull-Down Resistor	Comment
PA0,PA2,PA5~PA8 PA11~PA15 PB1,PB2,PB5,PB6	PU=0: NC PU=1: 10K PU=2: 100K PU=3: 1M	PD=0: NC PD=1: 10K PD=2: 100K PD=3: 1M	 PA2 default pull up 10KΩ PA0 default pull Down 10KΩ USBDM & USBDP default pull Down 15KΩ
USBDP	1.5K	15K	4、internal pull-up /pull-down resistance accuracy ±20%
USBDM	180K	15K	22070

3.5 Audio DAC Characteristics

Audio high voltage mode

Table 3-5

Parameter	MODE	Min	Тур	Max	Unit	Test Conditions
Frequency Response		20		20k	Hz	Fin=1kHz/0dB
Output Swing	Single-ended	*	0.57		Vrms	Fs=44.1kHz
THD+N	Single-ended		-82		ID.	B/W=20Hz~20kHz
S/N	Single-ended		91		dB	A-Weighted Filter
Noise Floor	Single-ended		16		uVrms	10k ohm loading

3.6 Audio ADC Characteristics

Audio high voltage mode

Table 3-6

Parameter	MODE	Min	Тур	Max	Unit	Test Conditions
Resolution				16	bits	
	Single-ended		0.85			Gain Level = 0
Maximum Input Laval	Single-chied		0.83		Vrms	Fin = 1kHz
Maximum Input Level	Differential	0.05			VIIIS	$F_S = 44.1 \text{kHz}$
	Differential		0.85			THD+N < 0.1%
SNR	Single-ended		93		dB	Gain Level = 0
SINK	Differential		95		иь	$F_S = 44.1 \text{kHz}$
THEAT	Single-ended		-80			Fin = 1kHz,Maximum Input
THD+N					dB	$B/W = 20Hz\sim20kHz$
	Differential		-85			A-Weighted Filter



3.7 APA Characteristics

Table 3-7

Parameter	VPWR	Min	Тур	Max	Unit	Tes	t Conditions
			2.30			R _L =10K	
	3.7V		1.76			$R_L=8\Omega$	
			1.47			$R_L=4\Omega$	Differential mode
Output Swing			1.43		Vrms	R _L =10K	Fin=1kHz/0dB B/W=20Hz~20kHz
	2.4V		1.03			$R_L=8\Omega$	
			0.77			$R_L=4\Omega$	
			99			R _L =10K	l.
	3.7V		96			$R_L=8\Omega$	/
SNR			96		dB	$R_L=4\Omega$	63
57.11			99			$R_L=10K$	
	2.4V		96			$R_L=8\Omega$	Differential mode Fin=1kHz/0dB B/W=20Hz~20kHz A-Weighted Filter
			94			$R_L=4\Omega$	
			-73			R _L =10K	
	3.7V		-38			$R_L=8\Omega$	
THD+N			-31		dB	$R_L=4\Omega$	
			-73			R _L =10K	
	2.4V		-36			$R_L=8\Omega$	
			-30			$R_L=4\Omega$	
Output power	3.7V		0.38			$R_L=8\Omega$	Differential mode
	1 1		0.54		W	$R_L=4\Omega$ $R_L=8\Omega$	Fin=1kHz/0dB
	2.4V	W.	0.13			$R_L=4\Omega$	
			0.14			IVL422	



3.8 BT Characteristics

3.8.1 Transmitter

1M Data Rate

Table 3-8-1-1

Paramete	er	Min	Тур	Max	Unit	Test Conditions
RF Transmit Power			0	6	dBm	
RF Power Contro	ol Range		20		dB	
	+2MHz		-40			
In-band spurious	-2MHz		-40		dBm	25℃ Power Supply
Emissions	+3MHz		-45		dBm	VPWR=3.7V 2440MHz
	-3MHz		-45			4 Layers Board
∆f1 avg	∆fl avg		250		KHz	(4)
△f2 min			210		KIIZ	0
\triangle f2 avg/ \triangle f1 avg			1		.0	

2M Data Rate

Table 3-8-1-2

Paramete	r	Min	Тур	Max	Unit	Test Conditions
	+4MHz		-40			
	-4MHz		-40			
Adjacent Channel	+5MHz		-40		1D	
Transmit Power	-5MHz	A	-40		dBm	25℃ Power Supply VPWR=3.7V 2440MHz 4 Layers Board
	+6MHz		-40			
	-6MHz		-40			
∆fl avg			500		KHz	
△f2 min			430		KIIZ	
\triangle f2 avg/ \triangle f1 avg			1.1			



3.8.2 Receiver

1M Data Rate

Table 3-8-2-1

<u> </u>						
Parameter		Min	Тур	Max	Unit	Test Conditions
Sensitivity		-98	-97		dBm	
Co-channel Interference Rejection			5		dB	
	+1MHz		-15		dB	25℃
	-1MHz		-20		dB	Power Supply
Adjacent Channel	+2MHz		-35		dB	VBAT=3.7V 2441MHz
Interference Rejection	-2MHz		-25		dB	4 Layers Board
	+3MHz		-30		dB	
	-3MHz		-25		dB	

2M Data Rate

Table 3-8-2-2

Parameter		Min	Тур	Max	Unit	Test Conditions
Sensitivity		-95	-94		dBm	
Co-channel Interference Rejection			3		dB	
	+1MHz		6	1	dB	25℃
	-1MHz		6		dB	Power Supply VBAT=3.7V
Adjacent Channel	+2MHz		-20		dB	2441MHz
Interference Rejection	-2MHz		-16		dB	4 Layers Board
	+3MHz		-25		dB	
	-3MHz		-30		dB	



3.9 ESD Protection

Table 3-9

Parameter	Тур.	Test pin	Reference standard
Human Body Mode	±4KV	All pins	JEDEC EIA/JESD22-A114
Machine Mode	±200V	All pins	JEDEC EIA/JESD22-A115
Charge Device Model	±500V	All pins	JEDEC EIA/JESD22-C101F
T -4-1	±200mA	All GPIO pins	JEDEC STANDARD NO.78E
Latch up	1.5xVopmax	All power pins	JEDEC STANDARD NO./8E

Note: 1.5xVopmax = 1.5 times maximum operating voltage.

4 Package Information

4.1 SSOP24

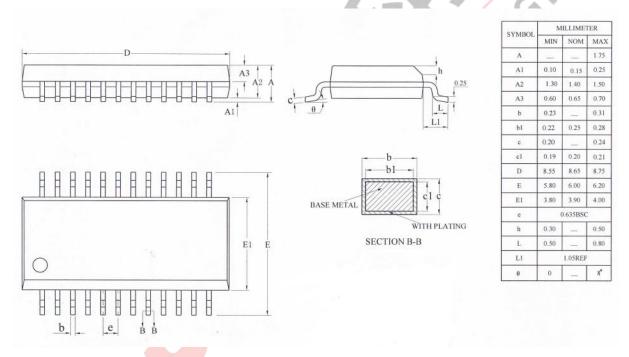
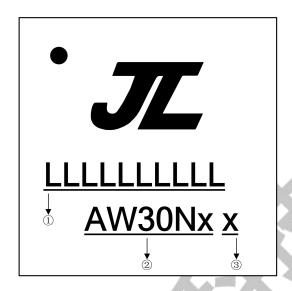


Figure 4-1 AW305A Package



5 IC Marking Information



- ① LLLLLLLLL: Production Batch
- ② AW30Nx: Chip Model
- 3 Built-in flash size
 - 0: No Flash Memory
 - 2: 2Mbit Flash
 - 4: 4Mbit Flash
 - 8: 8Mbit Flash
 - 6: 16Mbit Flash
 - 3: 32Mbit Flash



6 Solder-Reflow Condition

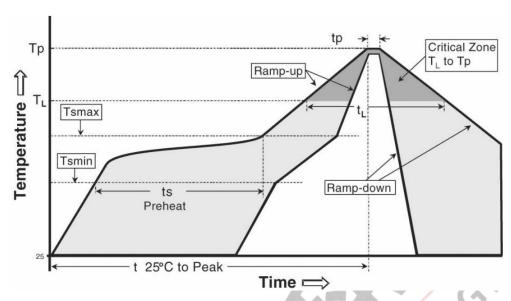


Figure 6-1 Classification Reflow Profile

Classification Profiles

Table 6-1

Tuble 0 1					
	Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly		
Temperature Min (T _{smin})		100℃	150℃		
Preheat/	Temperature Max (T _{smax})	150℃	200℃		
Soak	Time (ts) from (T _{smin} to T _{smax})	60-120 seconds	60-180 seconds		
Average ra	amp-up rate (T _{smax} to T _p)	3 ℃/second max	3 ℃/second max		
Liquidous temperature (T _L)		183℃	217℃		
Time (t _L) maintained above T _L		60-150 seconds	60-150 seconds		
Peak package body temperature (Tp)		See Table 5-2	See Table 5-3		
Time within 5°C of actual Peak Temperature (tp) ²		10-30 seconds	20-40 seconds		
Ramp-down rate (T _p to T _L)		6°C/second max	6°C/second max		
Time 25℃ to peak temperature		6 minutes max	8 minutes max		

Note 1: All temperatures refer to topside of the package, measured on the package body surface.

Note 2: Time within 5° C of actual peak temperature (tp) specified for the reflow profiles is a "supplier" minimum and "user" maximum.

SnPb - Classification Temperature

Table 6-2

Package Thickness	Volume mm³ < 350	Volume mm³ ≥ 350
<2.5 mm	240 +0/-5°C	225 +0/-5℃
≥2.5 mm	225 +0/-5°C	225 +0/-5°C



Pb-free - Classification Temperature

Table 6-3

Package Thickness	Volume mm³ < 350	Volume mm ³ 350 - 2000	Volume mm ³ > 2000
< 1.6mm	260°C	260°C	260°C
1.6 mm - 2.5mm	260°C	250℃	245°C
> 2.5mm	250℃	245℃	245℃

^{*}Tolerance:The device manufancturer/supplier shall assure process compatibility up to and including the stated classification temperature (this means Peak reflow temperature $+0^{\circ}$ C.For example 260° C+ 0° C)at the rated MSL level.

7 Revision History

Date	Revision	Description
2023.12.08	V1.0	Initial Release
2023.12.14	V1.1	Update APA Characteristics Update BT Characteristics
		.00.41