

## ***1. Description***

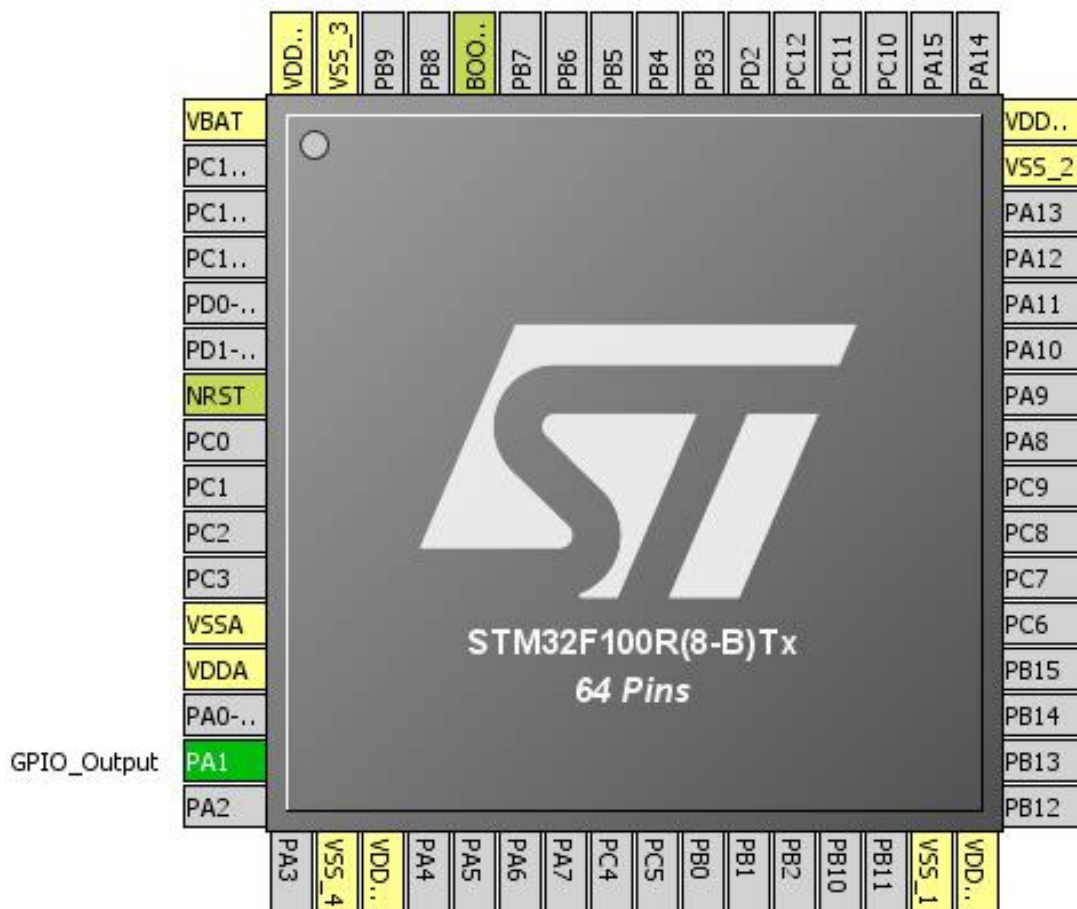
### 1.1. Project

Project Name	testConfig1
Generated with:	MicroXplorer 3.2
Date	10/19/2013

### 1.2. MCU

MCU family	STM32F1
MCU name	STM32F100R(8-B)Tx
MCU Package	LQFP64
MCU Pin number	64

## 2. Pinout Configuration



### ***3. Peripherals Configuration***

Peripheral	Mode	Remap	Fonction	Pin
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## ***4. Pins Configuration***

Pins	Pos	Functions
PA1 *	15	GPIO_Output

\* The pin is affected with an I/O function