

Relevant drawings	Diagram title	Description
CRSC0001	Top level	
CRSC0002	Microcontroller	
CRSC0003	Memory bank	
CRSC0004	Serial USB	
CRSC0005	Power	

Title

CRITICAL SYSTEM COMPUTER-Top level

Description

The top level overview of the entire critical system computer

Author

Sam Shan Jiang

Size

A3

Date

10/1/2011

Time

9:29:11 PM

Drawing No

CRSC0001

Version

1.0

Sheet

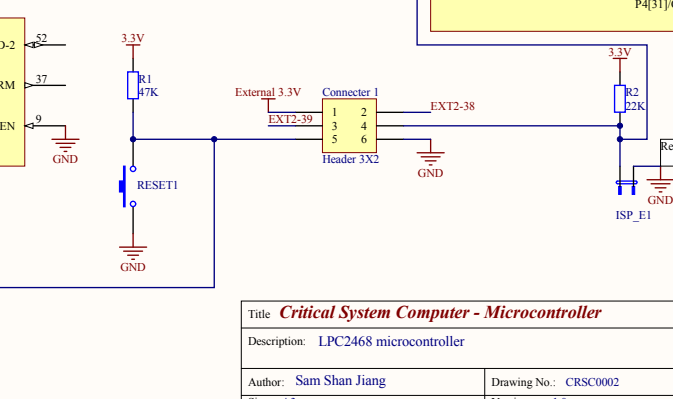
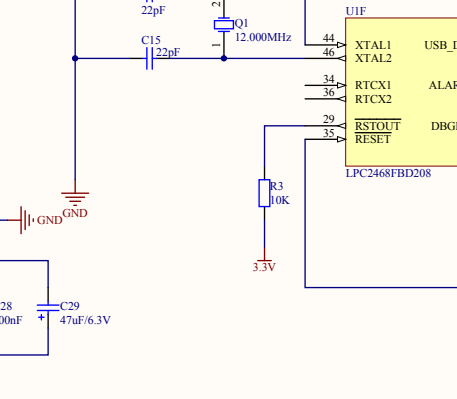
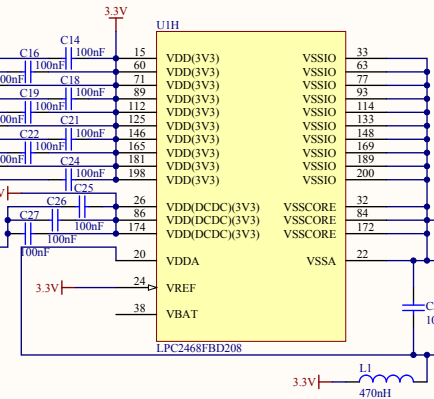
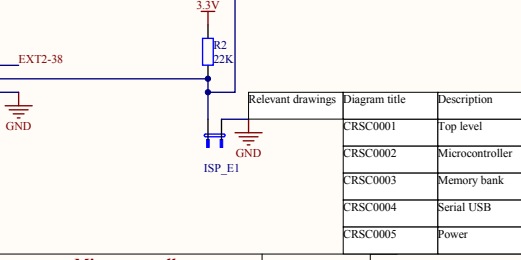
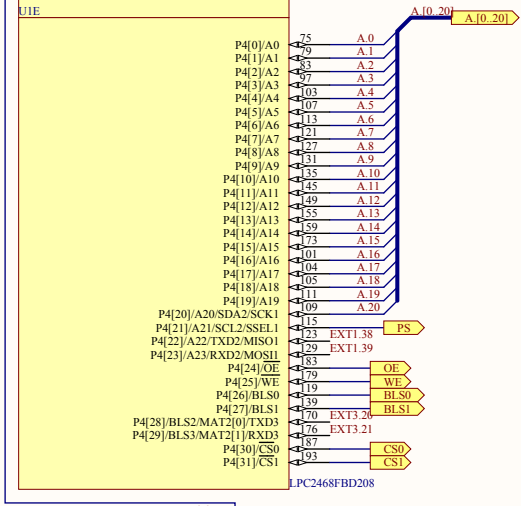
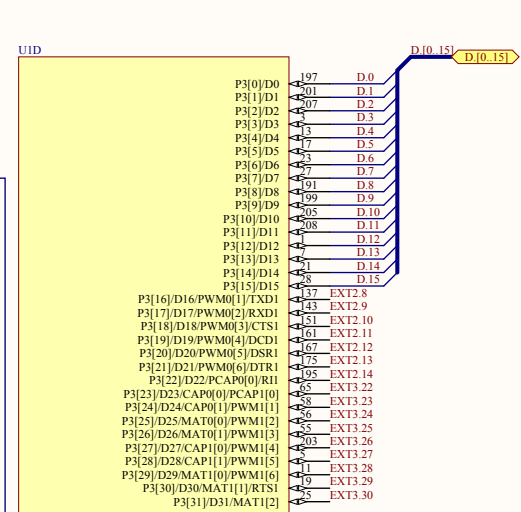
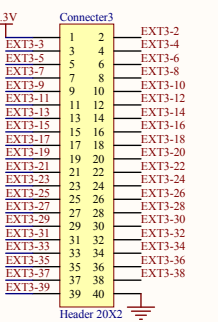
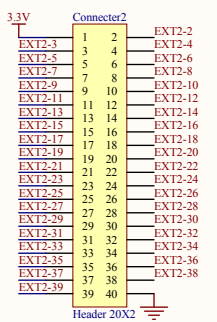
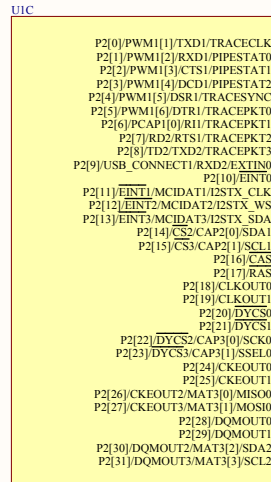
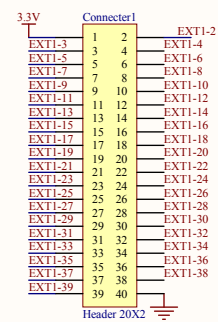
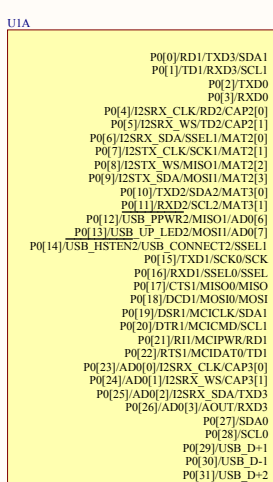
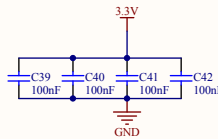
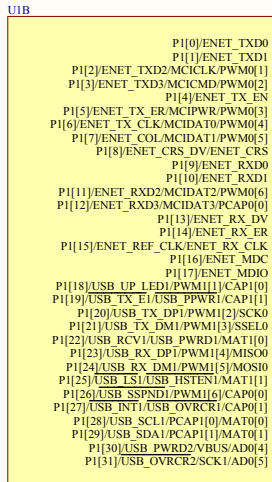
1 of 5

File

C:\Users\Thien Nguyen\Documents\BLUESat\Documentation SVN\PCB Integration\CRSC0001 SchDoc

BLUESAT

UNSW STUDENT SATELLITE PROJECT



Title: **Critical System Computer - Microcontroller**

Description: LPC2468 microcontroller

Author: Sam Shan Jiang

Drawing No.: CRSC0002

Size: A3

Date: 10/1/2011

Time: 9:29:11 PM

Version: 1.0

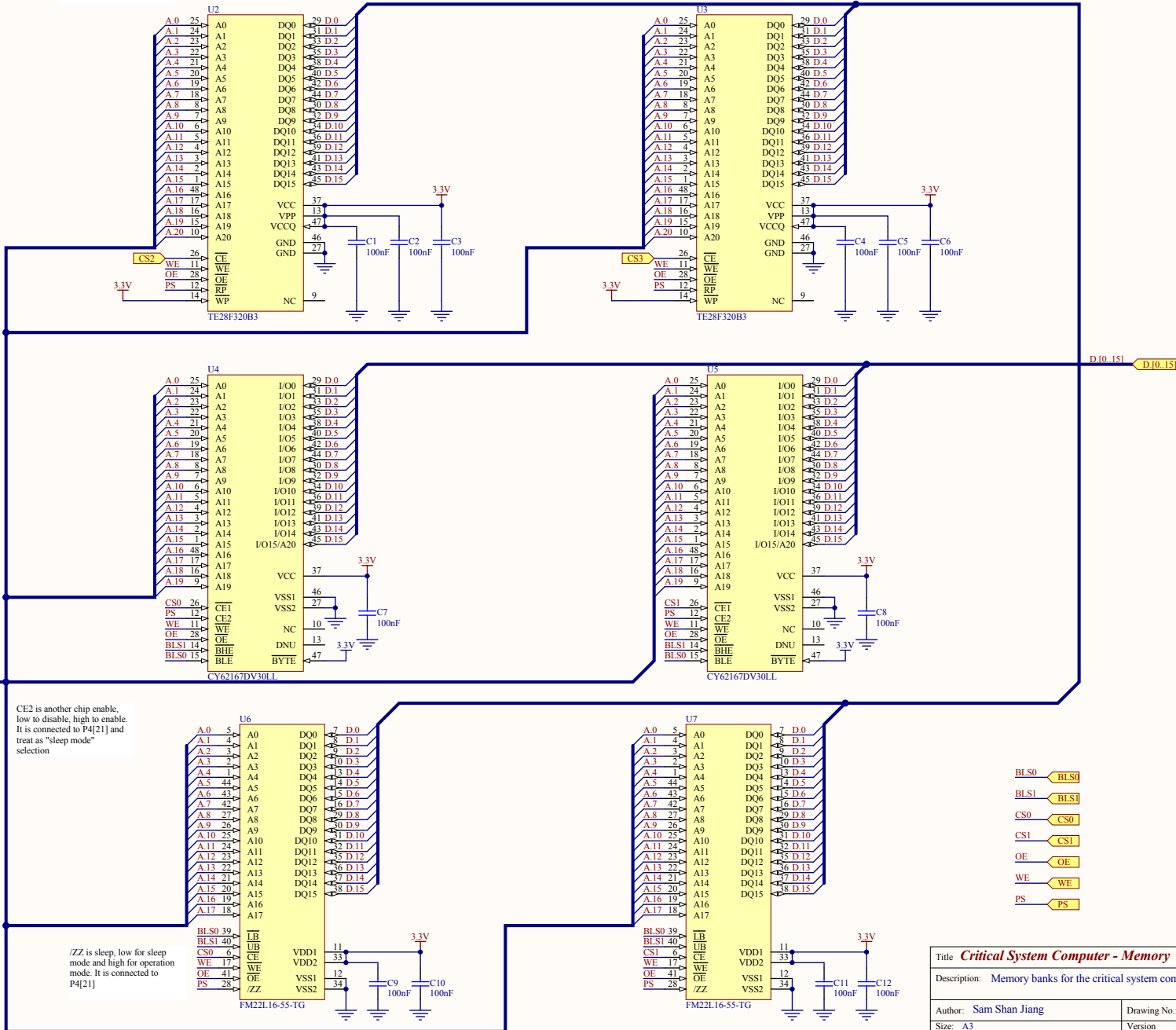
Sheet: 2 of 5

File: C:\Users\Thien Nguyen\Documents\BLUESat\Documentation\SVN\PCB Integration\CRSC0002\_SchDoc



UNSW STUDENT SATELLITE PROJECT

/RP is deep power down, low for deep power down mode and high for operation mode. It is connected to P4[21]



CE2 is another chip enable, low to disable, high to enable. It is connected to P4[21] and treat as "sleep mode" selection

/ZZ is sleep, low for sleep mode and high for operation mode. It is connected to P4[21]

# Title: **Critical System Computer - Memory**

Description: Memory banks for the critical system computer

Author: Sam Shan Jiang

Drawing No.: CRSC0003

Size: A3

Version: 1.0

Date: 10/1/2011

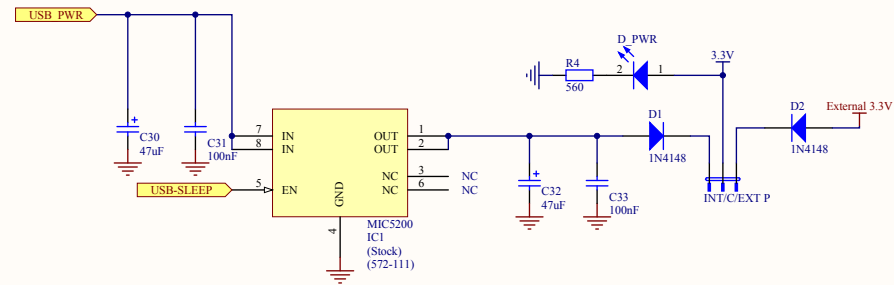
Time: 9:29:12 PM

Sheet: 3 of 5


File: C:\Users\Thien Nguyen\Documents\BLUESat\Documentation\SVN\PCB Integration\CRSC0003 SchDoc

Relevant drawings	Diagram title	Description
CRSC0001	Top level	
CRSC0002	Microcontroller	
CRSC0003	Memory bank	
CRSC0004	Serial USB	
CRSC0005	Power	

**BLUESAT**  
UNSW STUDENT SATELLITE PROJECT



Relevant drawings	Diagram title	Description
	CRSC0001	Top level
	CRSC0002	Microcontroller
	CRSC0003	Memory bank
	CRSC0004	Serial USB
	CRSC0005	Power

Title: <b>Critical System Computer - Power</b>		* * * * *	 UNSW STUDENT SATELLITE PROJECT
Description: Voltage regulator and power source select switch			
Author: Sam Shan Jiang	Drawing No.: CRSC0005		
Size: A3	Version: 1.0		
Date: 10/1/2011	Time: 9:29:12 PM		
File: C:\Users\Thien Nguyen\Documents\BLUESat\Documentation\SVN\PCB Integration\CRSC0005 SchDoc		Sheet: 5 of 5	