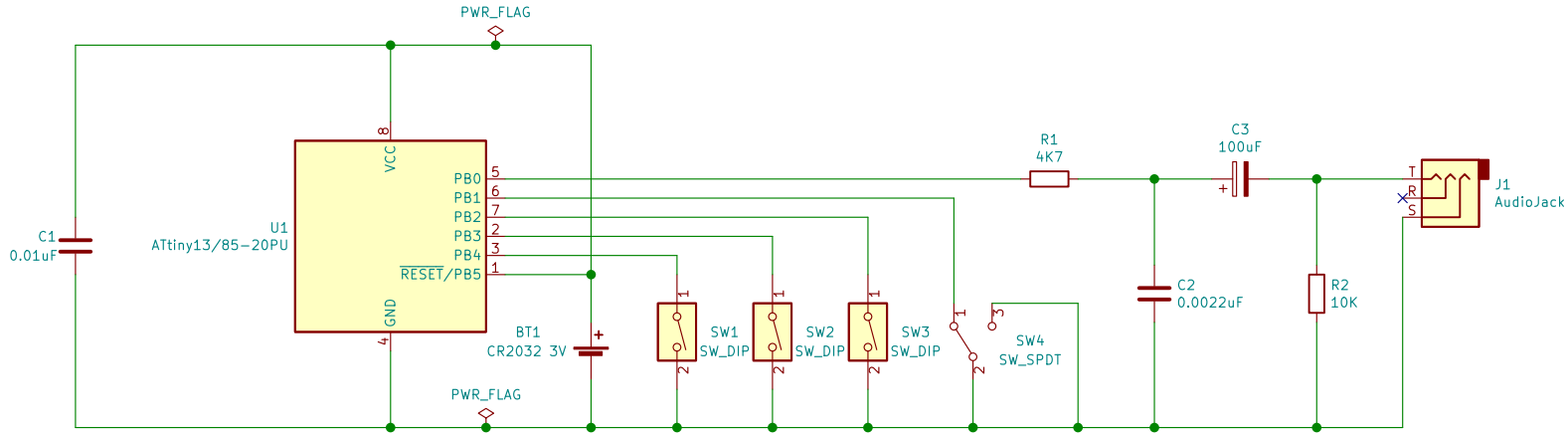
* PB0–5 of U1 are used in several ways for applications. In this case, PB0 (OC0A) is assumed to be the PWM output.
* R1 and C2 make a low–pass filter. The cut–off frequency is approx. 15392.2 Hz.
  If you set 18750 samples per second for DPCM, apply 0.0047uF capacitor for C2 to reduce high frequency noise (cut–off 7204.8 Hz).
* R2 and C3 make a high–pass filter. The cut–off frequency is approx. 0.159 Hz.
  However, it depends on the resistance in the input impedance of a load.
  For example, a load at 32 ohms resistance is combined with R2, resulting 31.898 ohms and cut–off at 49.895 Hz.



JimmyKenMerchant

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