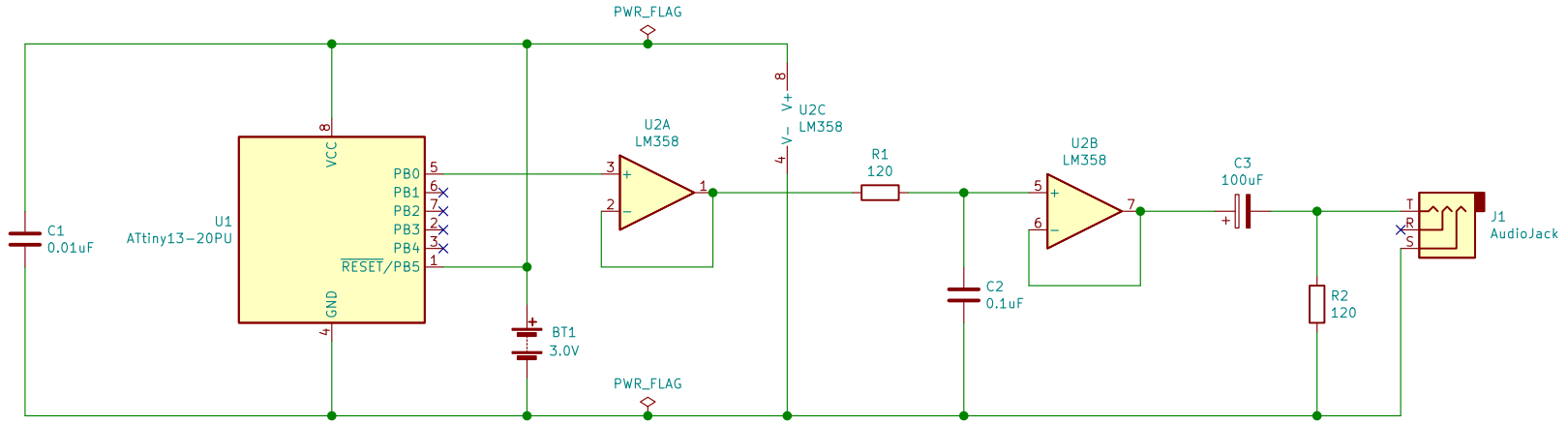
* PB0–5 of U1 are used in several ways for applications. In this case, PB0 (OC0A) is assumed to be the PWM output.
* U2A or U2B is a buffer made of a voltage follower, which has an input with high impedance (in terms of resistance).
* R1 and C2 make a low–pass filter. The cut–off frequency is approx. 13270 Hz.
  Option: B10K potentiometer next to R1 in series (cut–off 157 Hz – 13270 Hz). Caution that high resistance makes voltage drop.
  If you set 18750 samples per second for DPCM, apply 0.2uF capacitor for C2 to reduce high frequency noise (cut–off 6630 Hz).
* R2 and C3 make a high–pass filter. The cut–off frequency is approx. 13.26 Hz.



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