g	GE Energy	Functio	Functional Testing Specification  LOU-GEF-DIF2 1050HL Board				
	Inspection & Repair Services Louisville, KY						
	Test Procedure for DIF2 Printed	Circuit Board for a 10	50HL Control				
	MENT REVISION STATUS: Determined by the last entry in	the "REV" and "DATE" col					
REV.	DESCRIPTION		SIGNATURE	REV. DATE			
Α	Initial release		R Diercks	10/15/2007			
В							
С							

### © COPYRIGHT GENERAL ELECTRIC COMPANY

Hard copies are uncontrolled and are for reference only.

PROPRIETARY INFORMATION – THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION OF GENERAL ELECTRIC COMPANY AND MAY NOT BE USED OR DISCLOSED TO OTHERS, EXCEPT WITH THE WRITTEN PERMISSION OF GENERAL ELECTRIC COMPANY.

PREPARED BY Rick Diercks	REVIEWED BY	REVIEWED BY	QUALITY APPROVAL Charlie Wade
<b>DATE</b> 10/15/2007	DATE	DATE	<b>DATE</b> 10/15/2007

LOU-GEF-DIF2-A
REV. A

GE Energy
Inspection & Repair Services
Louis ville, KY

Functional test procedure for 1050HL DIF2 Printed Circuit Board

## 1. SCOPE

**1.1** The instructions apply to all DIF2 boards in test.

# 2. STANDARDS OF QUALITY

2.1 Refer to the current revision of the IPC-A-610 standard for workmanship standards.

## 3. APPLICABLE DOCUMENTS

- **3.1** The following document(s) shall form part of this specification to the extent specified herein. Unless otherwise indicated, the latest issue shall apply.
  - 3.1.1 GEK-71770

# 4. ENGINEERING REQUIREMENTS

- 4.1 Description
  - 4.1.1 The 1050 Control is a solid-state, integrated circuit controller/processor system using LSI circuits for data processing and control. The static logic circuits are arranged on modular, plug in, printed circuit boards, clearly identified by type. The circuit boards are mounted with functional grouping. In addition, a board identification number marks each rack slot.

### 4.2 Equipment Cleaning

- **4.2.1** Equipment should be clean and free of debris prior to applying power unless performing an initial check. Refer to the local documented procedures for cleaning guidelines.
- 4.3 Equipment Inspection
  - **4.3.1** Equipment should be visually inspected for any defects prior to applying power. This inspection should include the following as a minimum:
    - 4.3.1.1 Wires broken or cracked
    - 4.3.1.2 Terminal strips / connectors broken or cracked
    - **4.3.1.3** Loose wires
    - 4.3.1.4 Components visually damaged
    - 4.3.1.5 Capacitors leaking
    - 4.3.1.6 Solder joints damaged or cold
    - 4.3.1.7 Circuit board burned or de-laminated
    - 4.3.1.8 Printed wire runs burned or damaged

LOU-GEF-DIF2-A
REV. A

GE Energy
Inspection & Repair Services
Louis ville, KY

Page 3 of 4

# 5. **EQUIPMENT REQUIRED**

**5.1** The following equipment is required to perform the process requirements. Equipment may be substituted provided that all accuracy's and test ratios are equivalent or better.

Qty	Reference #	Description
1	GE 1050HL	Control with axis cart

## 6. Purpose:

6.1 To describe the procedure for testing the circuitry of the DIF2 board using the 1050HL simulator.

## 7. General:

**7.1** The DIF2 board does not have its own specific software diagnostics, therefore the board is tested by operating the diagnostics for the other boards in the system.

## 8. TESTING PROCESS

#### 8.1 Procedure

- **8.1.1** Remove the test DIF2 board from slot 5 and insert the board to be tested.
- **8.1.2** Special Mode Switch should be on (UP).
- **8.1.3** Press "ON".
- **8.1.4** "00" or "20" should appear in the message display and "?" in the alphanumeric display.
- **8.1.5** Press "P4", "1", and "ENTER". This instructs the control to read from the resident diagnostics boards.
- **8.1.6** A "T" will appear in the alpha display. Press "R" and "ENTER". This instructs the control to run all of the software diagnostics. See Exhibit A.
- **8.1.7** Observe message display for error codes.
- 8.2 \*\*\*TEST COMPLETE \*\*\*

g

## LOU-GEF-DIF2-A REV. A

### GE Energy Inspection & Repair Services Louisville, KY

Page 4 of 4

ŧ

### 8.3 Exhibit A

POM Board Diagnostics for the 1950H Control Operating Procedure

Q.S.I.No. 3,7,5.713

GEK-71770

EXHIBIT A

#### VERIFY

Sufficient directions are now entered. The following procedure explains how to step through a display of the first of tosses. 'V' is on the display.

Also displayed are the ID number and communes (if any) of the first test which has been sefected. Treas the Next pusided too and the next test in the list is displayed. The iteration index is also displayed all the white.

If the tests and commands are correct, push  $\underline{\mathbf{Forward}}$  to begin testing.

#### SHORTCLT TO RUN ALL UNAIDED TESTS

Some tests require the assistance of the troubleslaceter while they succeed. For instance, the dontrol station display exerciser (COCC) relies on the user to spot failures in the control station display bardware. Tests which run without assistance are called unaided. Table 2 lists the unaided tests.

Table 2 UNAIDED TESTS

Data Controller Processor U100 None Tata Controller Memory 0200 None Axis Controller Memory 2000 None CCTU 5000 None			
Tata Controller Memory 0200 None Axis Controller Processor 1000 None Axis Controller Memory 2000 None CCTU 3000 None	Test	D.	Communat
(99-12)	Tata Controller Memory Axia Controller Processor Axis Controller Memory CGTU Axis Boards	0200 1000 2000 3000 4000	None None None None Suitest No.

All unsided tests can be run by pressing R and Enter. Testing begins automatically. Continuous (tertition and stopping for errors is assumed (default condition). This command will include tost 5000 if Machine Setup Data (MSD) indicates that a spindlo board should be in the Control or if MSD were lost.

#### ID Number 2009

Another way to select all unaided tests is to input 9990 as an ID number. In this case, however, testing does not begin amountically. The user can select additional tests, and be must answer the questions about iterating and suppling for errors.

Example 7. Select all unalded tests and the RS-232 test (9490). Mixed mode (command '2'). Specify iteration of all selected tests and whether or not to stop for errors.

PRESS XEYS									
		BI,		K Sk	RE A.	AIN DO		MESS	AG3
9	9	۵	D	9	. r			D	0
₽	đ	٥	9	9	T			0	ט
è	0	9	5	9	T·			٥	٥
٥	9	3	g	s	T			G	ø
Bater	0	0	0	0	T			0	7
4	ņ	۵	0	4	т			a	7
D	9	c	4	D	T			D	7
9	0	4	ô	ð	T			Đ	7
Enter	ņ	4	G	0	C				7
2	ß	4	·a	ρ	C 2			G	7
Enter	9	U	a	0	'r			a	a
₫ <b>o</b> rward	0	4	0	0	I			0	7
ė	g	4	Ð	G	I		۵	ø	7
0	0	4	n	a	1		D	9	7
Entor	0	4	0	٥	٤	0	0	Ð	7
Buter	0	1	0	υ	v	0	8	a	0

Note that the index number jumps from 00 to 67. This happens because the 7 unaided tests were selected because the operator ordered librarabor 9900.

© COPYRIGHT GENERAL ELECTRIC COMPANY

Hard copies are uncontrolled and are for reference only.