

## 1.0 SCOPE:

This Document establishes the performance requirement and recommended tests for the Gate Driver identified as:

### DS3800HPTJ

This specification will check digital logic, analog transfer functions and component tolerances.

## 2.0 TEST EQUIPMENT & DOCUMENTATION

### 2.1 Special Equipment Required

Pulse Generator  
+36VDC supply .5 amp  
-18VDC supply .5 amp

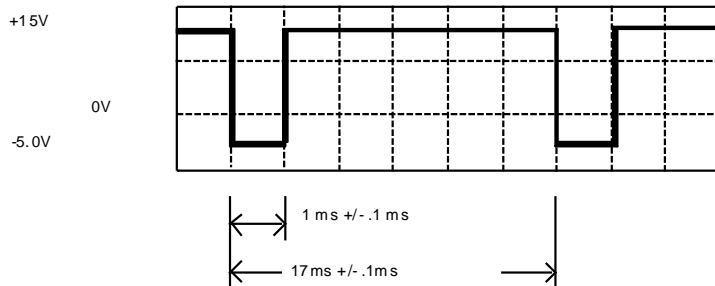
2.2 Connect all DCOM pins together first, then wire to ACOM. Analog signal power supplies, oscilloscope, and voltmeters should connect to ACOM for the most accurate readings.

2.3 Element Load of 10 ohms 1/4 watt (W) to (R) (68A7035)

## 3.0 Special Tests

### 3.1 Verify Gate Drive

3.1.1 Apply +36VDC +/- .5VDC to JA(1) common to JA(2) and -18VDC +/- .5VDC to JA(6) com to JA(4). Apply waveform illustrated below to JA(5) referenced to common JA(4).



### Approx. Settings If using a Wavetec:

Freq. mult. = 10K  
Frequency = .1  
Atten. = 0  
Fine tune Freq. adjust  
Amplitude, & symmetry  
controls for Proper  
waveform.

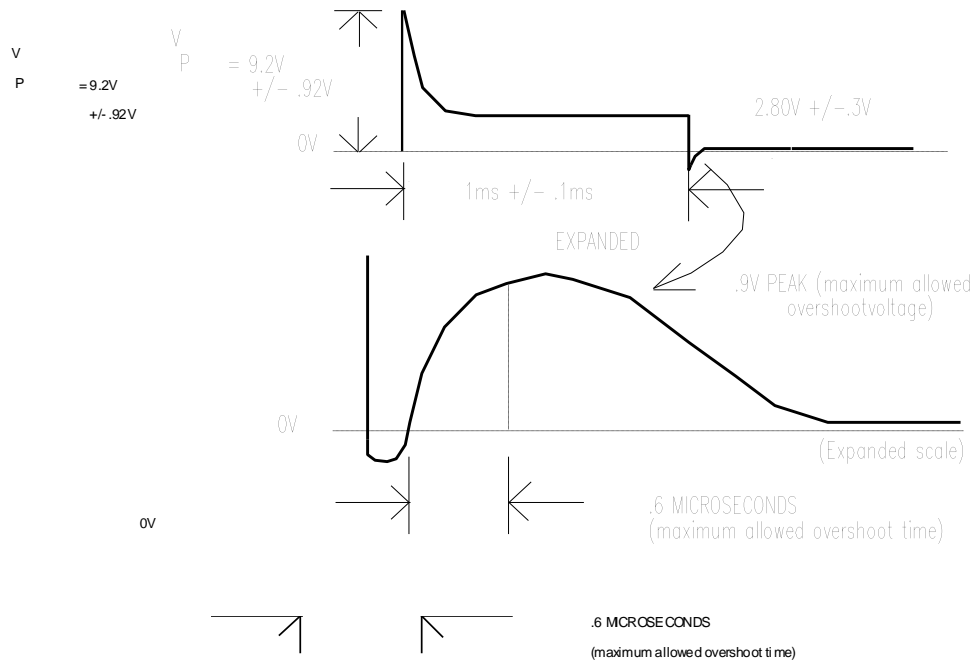
Gate Driver  
DS3800HPTJ

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Filename: HPTJ.DOC  
Graphics: HPTJF1.DXF  
HPTJF2.DXF

#### 4.0 Verify Gate Drive

4.1 Tie Red to common (JA4).

4.2 With a scope, verify the output illustrated below across the 10 ohm load resistor (white referenced to red).



End of Test

REV.	INIT.	REASON FOR CHANGE	DATE
001	MLR		13OCT80
002	JMT	ADDED WAVETEK SETTINGS	12AUG88
003	JJW	RETYPED WITH ISO FORMAT	17NOV94
004	JJW	CONVERTED FROM WORDPERFECT FILE TO DOC FILE	15AUG95

IN WINWORD