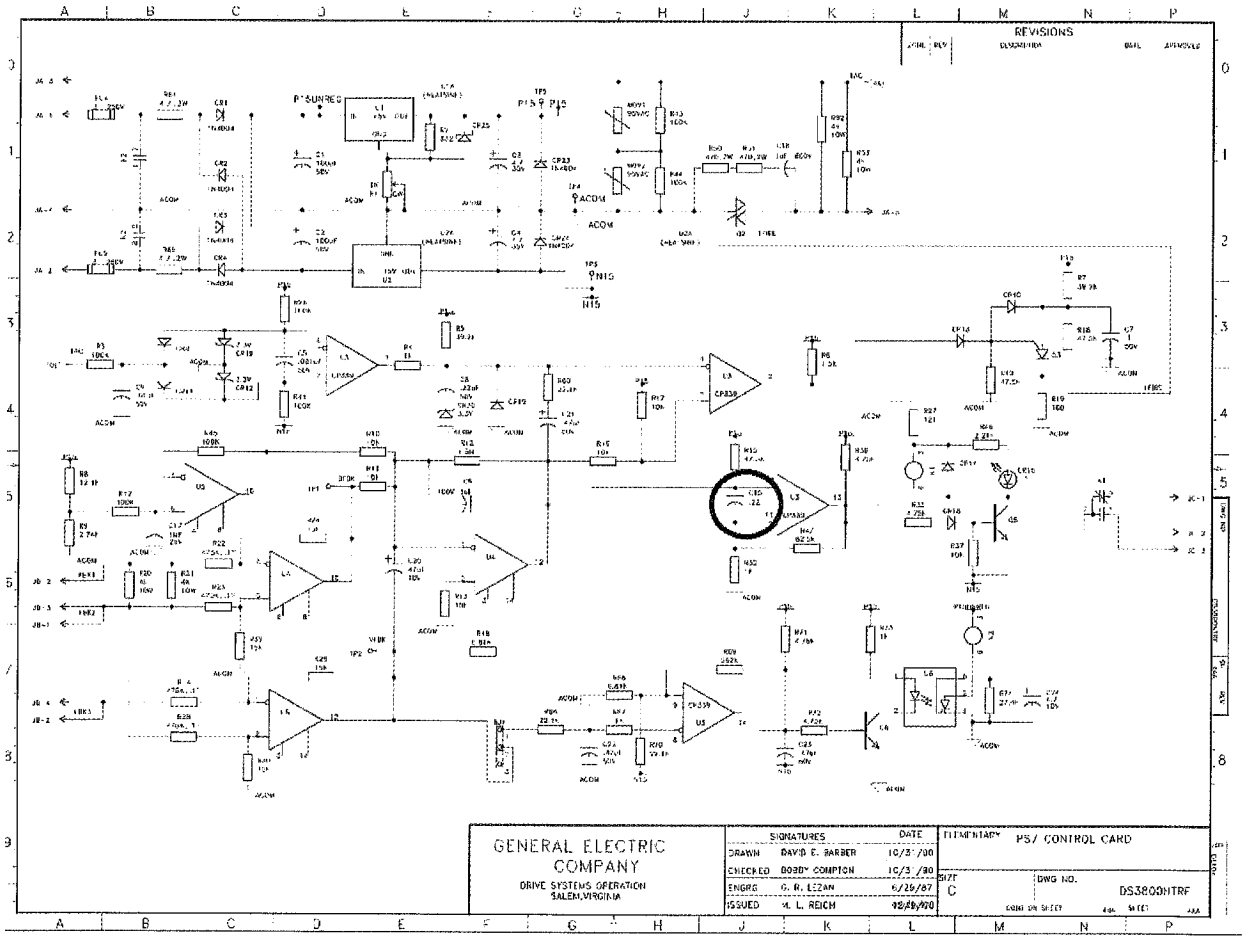
DS3820PS7A procedure

Rev. D

DS3800NTRF1G1E modification

Louisville shall replace C15 with a .47uF -50v ceramic cap GE part # 104X122AA 405

RE-LABEL THE CARD DS3800NTRF1H1E



DS3820PS7A procedure

Rev. C

Due to intermittent issues it is necessary to change resistors **R14, R22, R23, R28** on all units. Resistor part # is 68A7014C4750F (475K ohm .5w 1%).

9.1.0 Scope

This document establishes the performance requirement and recommended test for the supply

DS3820PS7

9.2.0 Test Equipment

Test equipment shall be provided which meets or exceeds the requirements and accuracies defined in this specification.

9.3.0 Power Supply Requirements

9.3.1 The AC power supply requirements are:

Form A: 0 to 130VAC, 10 amps minimum, 50 or 60 HZ
Form B: 0 to 506VAC, 2.5 amps minimum, 60 HZ
Form C: 0 to 420 VAC, 2.5 amps minimum, 50 HZ

The DC power supply required for both forms is 50VDC, $\geq$ 50MA.

9.3.2 Input Definitions:

Due to the existence of three forms, the AC input for a given test shall be noted as either high line, nominal line or low line and shall be defined as follows:

	Form A	Form B	Form C
Low Line -	72.5 $\pm$ 0.5VAC	290 $\pm$ 2VAC	239 $\pm$ 2VAC
Nom. Line -	115 $\pm$ 1VAC	460 $\pm$ 4VAC	380 $\pm$ 2VAC
High Line -	126.5 $\pm$ 1VAC	506 $\pm$ 4VAC	418 $\pm$ 2VAC

9.4.0 Loading

9.4.1 Two load resistances are needed for power loading:

21 $\pm$ 0.2Ω, 380 watts minimum

286 $\pm$ 3Ω, 26 watts minimum

It will be necessary to step from one resistance to another as shown in Fig. 1.

It will be necessary to periodically open the load ckt.

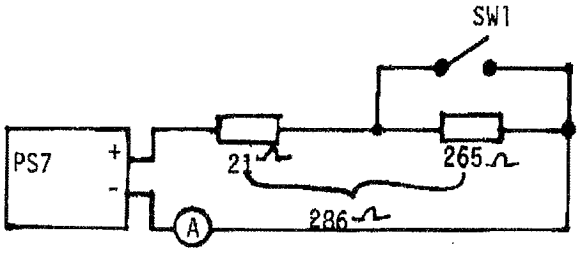


FIG. 1

REV. 1 OEB RE-TYPED CCH	REV. 4	REV. 7	PRINTS TO DL119	ENGINEER J. Wilk.	GENERAL ELECTRIC	TEST INSTRUCTIONS
REV. 2 OGA 9/12/88 PJA	REV. 5	ISSUED 8/26/82				
REV. 3	REV. 6	MADE BY J. Hylton			SALEM, VA. U.S.A.	DS3820PS7 CONT. ON SH. 9AB SH. NO. 9AA

DSD 0151 (05-76)

R1 = 12K 10 watt resistor, 68A7020P120F or equivalent CR1, CR2 = Red LED's, 68A7806P1 or equivalent.

Warning: This load will be at 160VAC potential and should be protected accordingly.

9.5.0 Initial Setup

9.5.1 Connect the power load of Fig. 1 to the PS7 output terminals on TB1 through a 0-10 A DC meter.

9.5.3 Remove the ground connection between TB1 and the PS7 chassis.

9.5.4 Verify that no other ground paths to the chassis exist by the following procedure:

(a) Connect a 50 volt DC power supply between the chassis and ACOM (TP4) through a 1K 10 watt resistor and ammeter.

(b) With the 50 volt DC supply energized, the current through the 1K resistor should not exceed 50 μA.

(c) Reverse the polarity of the connection and repeat the test. Current still should not exceed 50 μA.

9.6.0 Output Check

9.6.1 Connect the variable voltage AC source to the input terminals of PS7 located on the lower terminals of the FU1 and FU2 fuse holders.

9.6.2 Set the load to 28Ω.

9.6.3 Set the input voltage to nominal.

(a) The DC output voltage should be .88± 2.2 volts

(b) The DC output current should be 0.31± .02 amps

REV. 1 DGA 4/13/83 PJR	REV. 4	REV. 7	PRINTS TO DL119	ENGINEER	GENERAL ELECTRIC	TEST INSTRUCTIONS
REV. 2 DGA 12/14/82 BC	REV. 5	ISSUED 8/26/82			SALEM, VA. U.S.A.	DS3820PS7
REV. 3	REV. 6	MADE BY J. Hylton				CONT. ON SH. 9AC SH. NO. 9AB

DSD 0151 (09-76)

9.6.4 Step the load from 286 OHM to 21 OHM (Keep input at nominal).

The DC output voltage should not droop more than 4VDC.

9.6.5 With the load at 21 OHM reduce the AC input to low line.

(a) the DC output voltage should not fall below 75 VDC.

(b) The IMOK light should remain lit.

9.6.6 Further reduce the AC input until the IMOK light just goes out.

The DC voltage should be 67 ± 2 VDC.

9.6.7 Open the load circuit and observe the DC output voltage while slowly varying the input between low line and high line. DC volts should be 85-91 VDC.

9.6.8 Set the input to nominal. Then step the load from open ckt. to 21 OHM The DC output should not dip below 70 VDC.

End of test.

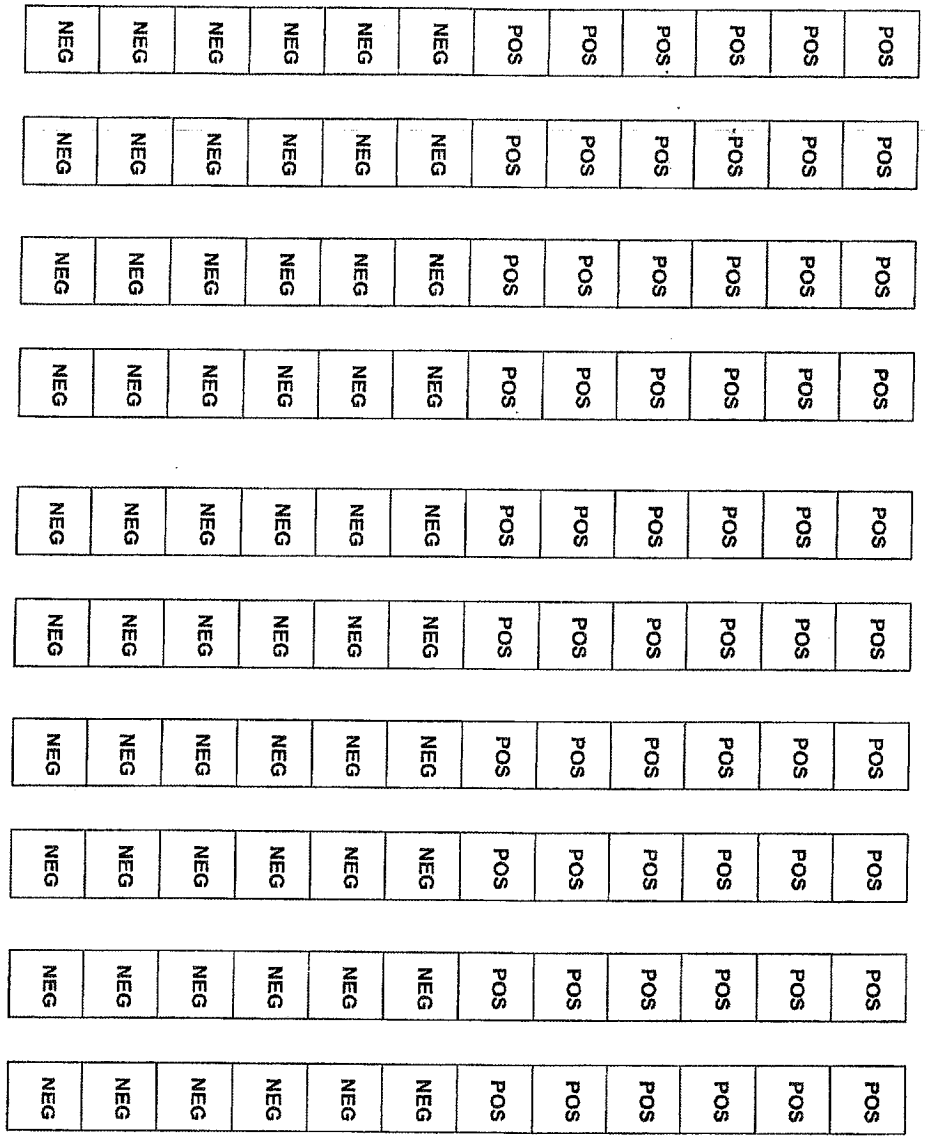
REV. 1 DCC 12/18/87	REV. 4	REV. 7	PRINTS TO DL119	ENGINEER	GENERAL ELECTRIC	TEST INSTRUCTIONS
REV. 2	REV. 5 RE	ISSUED 12/18/87				
REV. 3	REV. 6	MADE BY J Hylton/BC			SALEM, VA. U.S.A.	DS3820PS7 CONT. ON SH. FL SH. NO. 9AC

DIETZGEN 135-4

OBD 0151 (09-76)

Rev:-B

Labels for DS3820P7A unit.



1) Please use these labels for TB marking. They are located in the DS3820P57A folder. See attachment photo.

FU3
FU2
LINE
NEG
NEG
NEG
NEG
NEG
NEG
POS
POS
POS
POS
POS
POS