
305A2080 TEST INSTRUCTIONS

I. SCOPE

The following describes the setup and test procedure for the Volts/HZ Trip Alterrex Output PWB 305A2078.

REF ELEM: 158C2489

II. SPECIAL TEST EQUIPMENT

none

III. POWER SUPPLY REQUIREMENTS

SUPPLY	NOM.	TOL.	PINS
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P24	24.0V	+/- .2V	[7,8]
COM (P24COM)			[9,10]

IV. INITIAL SETUP

A. DAUGHTER BOARD SETUP (none)

B. TEST SETUP DESCRIPTION

1. Connect a 2.21K resistor (7030 type) between each of the following sets of points.

From	To
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[11]	[8]
[12]	[8]
[30]	[8]

2. One lamp (type 218A4867PIRD or equivalent) is required to be connected to each of the following points. The opposite end of the lamp is connected per the test procedure.

Lamp	Point
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L13	[13]

V. TEST DEFINITIONS AND SPECIAL NOTES

1. Unless otherwise specified, the following conditions apply through-out the test procedure.
 - a. Voltages are positive DC.
 - b. DC inputs should be within 2 millivolts of nominal.
 - c. Inputs are to be floating unless a signal is specifically applied.
 - d. Once an input is applied it should be left applied until specifically told to remove it.
2. All scope measurements must be made with a scope that is completely isolated.

VI. TEST PROCEDURE

1. Set up and apply power per sections III and IV.
2. Verify 10.0 +/- .5V at IC102-14 with respect to TP101.
3. With an oscilloscope verify the waveform at IC104-11 with respect to 101TP. The waveform should be a square wave with 2 to 5 pulses per second.
4. Open SW102 and then depress and release the Reset Push-button (101PB).
5. Connect L13(-) to COM.
6. Close SW102. Verify the following:
 - L13 should be on.
 - 102L (Alarm Test/Trip Lockout) should be flashing.
 - Pin-12 should go from P24V to 0V
7. Open SW102 and then verify L13 is off.
8. Press PB101 and verify 102L remains off and Pin-12 returns to P24V.
9. Apply 15.0 +/- .1V through a 2K resistor to [17] with respect to [19]. Verify 101L (TRIP) flashes on and off and Pin-30 goes from P24V to 0V.
10. Remove 15V/2K input from [17] press PB101. Then verify 101L turn off and Pin-30 goes to P24V.
11. With [15] open verify greater than 22V at [11] with respect to TP101.
12. Apply P15V Through a 2Kohm resistor to P15V with respect Pin-3, then verify less than 100 millivolts at [11] with respect to TP101.

END OF TEST

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TEST INSTRUCTION REVISION STATUS

REV	INIT	DESCRIPTION OF CHANGE	DATE COMPLETE
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0	REV	First made for 305A2078	06/22/84
1	GSC	Corrections to Test Instructions	04/12/85
2	awe	Converted to WORD format	02/18/98