

REV.
NO.

TITLE

P3K-AL-0359-A01

TEST INSTRUCTIONS FOR LOAD/PRESSURE LIMIT
LOGIC CIRCUIT BOARD 1L2-D001
FIRST MADE FOR (Ass'y Drawing 117D7783 G-1)

CONT ON SHEET

2

SH NO.

1

REVISIONS

I. CIRCUIT DESCRIPTION**E H C TEST**
BLDG. 273 1320

The circuit board contains the logic for the load setback limit and the initial pressure limit functions.

The inputs associated with load setback limit function are pins 22, 25, 28, 31, and 35 while the outputs are pins 23, 26, 29, 32, and 34 which provide 24V to the appropriate analog to logic relays.

The inputs associated with the pressure limit function are pins 13, 15, 16, and 18; the output is pin 20 which provides 24V to an analog to logic relay.

Additional outputs are pins 6, 7, 9, and 11 which provide 24V to indicating lamps.

II. CIRCUIT SPECIFICATIONS

1. Before the board is coated diodes CR1 - CR16 should be checked (e.g. with an ohmmeter) especially in the reverse direction to insure that redundancy is effective. The rest of the tests can be conducted with the board coated as well as uncoated.
2. Connect ground to pin 40 and +24 VDC to pin 38. Connect 24V, 20 MA lamps to pins 6, 7, 9, 11, 13, 15, 20, 23, 26, 29, 32, 34, and 35. The logic and path continuity should conform with the following table:

ACTIVE FOR 21
BY [Signature] DATE 2/10/77

ET-273

273-2

273-12

273-13

273-71

PRINTS TO

MADE BY D.Mone Oct. 28, 1974

APPROVALS

Stream Turbine
Schenectady, N.Y.DIV OR
DEPT.

P3K-AL-0359-A01

ISSUED

OCT 28 1974

LOCATION

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CODE IDENT NO.

REV
NO. 0

TITLE

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P3K-AL-0359-A01

TEST INSTRUCTIONS FOR LOAD/PRESSURE LIMIT

LOGIC CIRCUIT BOARD 1L2-D001

(Ass'y Drawing 117D7783 G-1)

CONT ON SHEET 3 SH NO. 2

FIRST MADE FOR

REVISIONS

INPUTS AT PINS

CORRECT OUTPUTS AT PINS

3	4	5	6	7	8	9	10	11	1	2	3	4	5	6	7	8	9	10	11	12	13
2	3	5	16	18	22	25	28	31	6	7	9	11	13	15	20	23	26	29	32	34	35
1	2	3	4	5	6	7	8	9													
0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	0	0	0	1	1	1
0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	0	0	1	0	1	1
0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0	1	0	0	1	1
0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	1	0	0	0	1	1
0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	0	0	0	0	1	0	1	1	1	0	0	0	0	0	0
0	0	0	1	1	0	0	0	0	0	0	0	1	0	1	0	0	0	0	0	0	0
0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
1	0	0	0	0	0	0	0	0	0	1*	1*	1*	1	0	0	0	0	0	0	0	0
1	0	0	1	0	0	0	0	0	0	1*	1*	1	1*	1	1	1	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0

0: OV or lamp off

1: 24V or lamp on (1* reduced voltage due to 68.1Ω resistors)

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APPROVALS

Steam Turbine

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Schenectady, N.Y.

LOCATION

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SH NO. 2

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P3K-AL-0359-A01

TEST INSTRUCTIONS FOR LOAD/PRESSURE LIMIT
LOGIC CIRCUIT BOARD 1L2-D001

CONT ON SHEET

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SH NO.

4

FIRST MADE FOR

REVISIONS

PROCEDURE	L1	L2	L3	L4	L5	L6	L7	L8	L9	L10	L11	L12	L13	L14
1 All switches down				X									X	
2 S11 up				X										
3 S11 down				X									X	
4 S10 up				X						X				
5 S10 down				X					X					
6 S9 up				X									X	
7 S9 down				X										
8 S8 up				X									X	
9 S8 down				X										
10 S6 up				X										
11 S7 up				X										
12 S6 down				X										
13 S7 down				X										
14 S5 up	X			X										
15 S5 down				X										
16 S4 up		X		X										

MADE BY D.Mone Oct. 28, 1974

APPROVALS

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P3K-AL-0359-A01

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4

PRINTS TO

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TITLE

TEST INSTRUCTIONS FOR LOAD/PRESSURE LIMIT
LOGIC CIRCUIT BOARD 1L2-DO01

P3K-AL-0359-A01

CONT ON SHEET

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SN NO.

5

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REVISIONS

[illegible]

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D. Mone Oct. 28, 1974

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APPROVALS

Steam Turbine

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P3K-AL-0359-A01

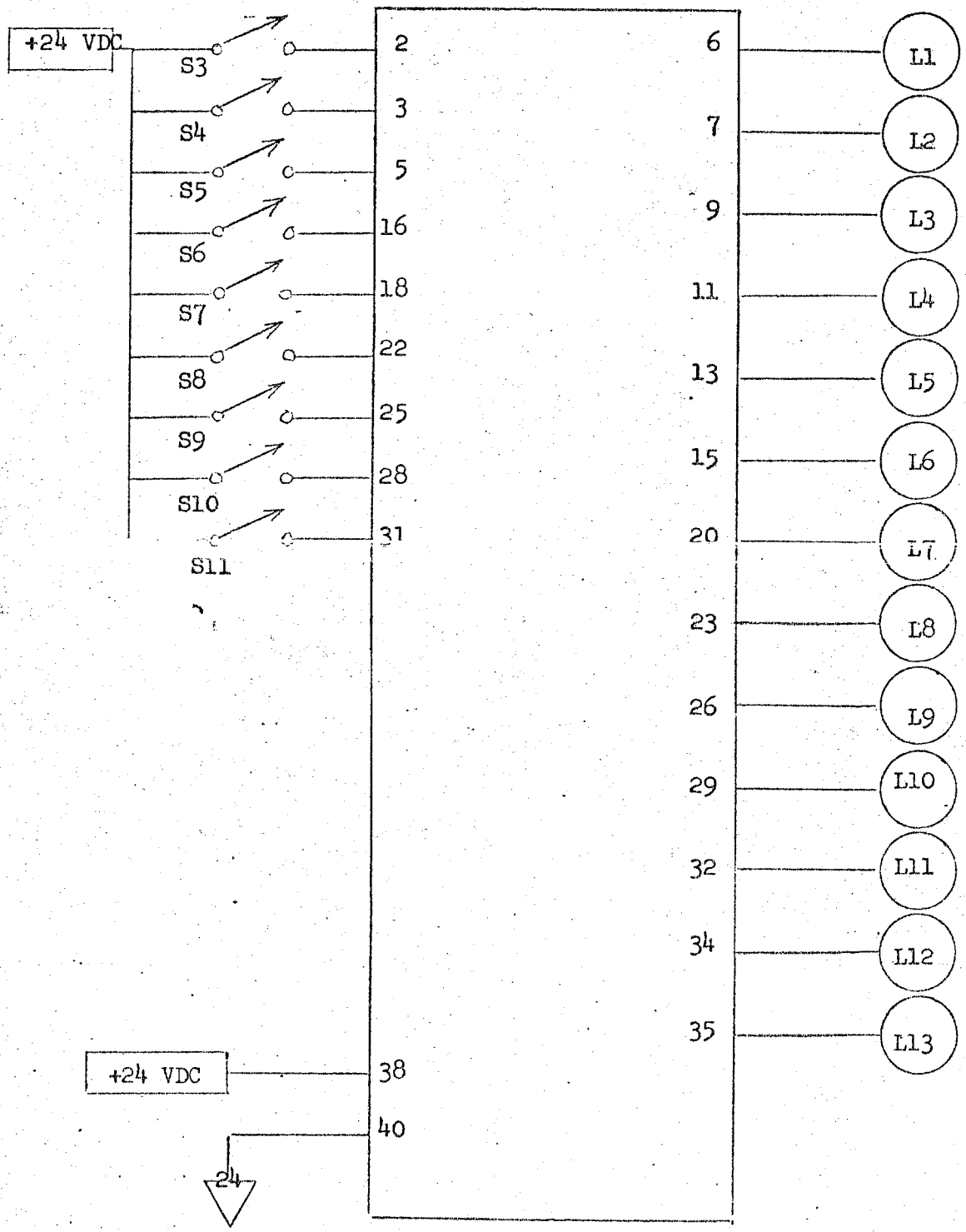
CONT ON SHEET 6

SH. NO. 5

REV NO. 
 P3K AL-0359-A01
 CONT ON SHEET 7 SH NO. 6

TITLE
 TEST INSTRUCTIONS FOR LOAD/PRESSURE LIMIT
 LOGIC CIRCUIT BOARD 1L2-D001
 FIRST MADE FOR (Ass'y Drwg. 117D7783 G-1)

FIGURE A



REVISIONS

PRINTS TO

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 D.Mone Oct. 28, 1974

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 OCT 28 1974

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P3K-AL-0359-A01

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REV. NO.		TITLE		CONT ON SHEET		SH NO. 7	
P3K-AL-0359-A01		TEST INSTRUCTIONS FOR LOAD/PRESSURE LIMIT LOGIC CIRCUIT BOARD 1L2-D001		CONT ON SHEET		SH NO. 7	
P3K-AL-0359-A01		FIRST MADE FOR (Ass'y Drawing 117D7783 G-1)					
PREPARED BY <u>D. Economou</u> DATE <u>4-23-1973</u>						REVISIONS	
D. Economou EHC DESIGN ENGINEERING							
APPROVED BY <u>P.C. Callan</u> DATE <u>10-25-74</u>							
P.C. Callan - MANAGER EHC DESIGN ENGINEERING							
TEST PROCEDURE PREPARED BY <u>C. Bugg</u> DATE <u>8/5/74</u>							
C. Bugg EHC TEST ENGINEER							
TEST PROCEDURE APPROVED BY <u>M.J. Sindoni</u> DATE <u>10/24/74</u>							
M.J. Sindoni EHC DESIGN ENGINEERING							
MADE BY <u>D. Mone Oct. 28, 1974</u>						PRINTS TO	
ISSUED <u>OCT 28 1974</u>		APPROVALS		Steam Turbine		DIV OR DEPT.	
				Schenectady, N.Y.		P3K-AL-0359-A01	
				LOCATION		CONT ON SHEET	
						SH NO. 7	