REV NO.	TITLE
P24B-AL-4831 CONT ON SHEET 2 SH NO. 1	Circuit Board Test Procedure OP AMP/DFG Amplifier 948D881 G-4 FIRST MADE FOR MARK I

refer to sch. 996D907

1. Connect test circuit as shown in Fig. A.
2. Check test supply voltage (Tol. ± 1 MV)
3. S3 down -GND
   S4 down -GND
4. Check ckt card supply voltages at TP4 and TP2 for +13.0 VDC and -13.0 VDC (Tol. ±1.0 V) 14.1 -14.48

CHECK OP AMP

5. Adj. R8 for 0 VDC at TP5.
6. S3 up. VS1
7. Adj. VS1 for -0.100 VDC.
8. Check TP5 volts for +2.50 VDC (Tol. ±100 MV)
9. Adj. VS1 for -1.000 VDC.
10. Check TP5 volts for +5.200 to +7.200 VDC.
11. Adj. VS1 for +1.000 VDC.
12. Check TP5 volts for -5.200 to -7.200 VDC.

CHECK DFG DRIVING AMP

13. Adj. R20 for 0 VDC at TP3.
14. S4 up. VS2
15. Adj. VS2 for +5.00 VDC at TP1.
16. Adj. R1 for +5.00 VDC at TP3.

FINAL ZERO ADJUST CHECK

NOTE: Do Not Use A Card Extender

17. S3 down
    S4 down Gnd
18. Make a final trim with R8 for 0 VDC at TP5.
19. Make a final trim with R20 for 0 VDC at TP3.
20. Apply red locking solution to R20 and R8. = DON'T DO for Bruns
21. End of Test.

REVISIONS

1	AUG 15 1974 D. Moore / Rolland

PRINTS TO: ET-273, 273-2, 273-12, 273-132, 273-138, 273-71

MADE BY	APPROVALS	DIV OR DEPT. / LOCATION	
B.A. Gimmel Apr. 1,1968		Steam Turbine	P24B-AL-4831
ISSUED AUG 15 1974		Schenectady	CONT ON SHEET 2 SH NO. 1

RE

9TO

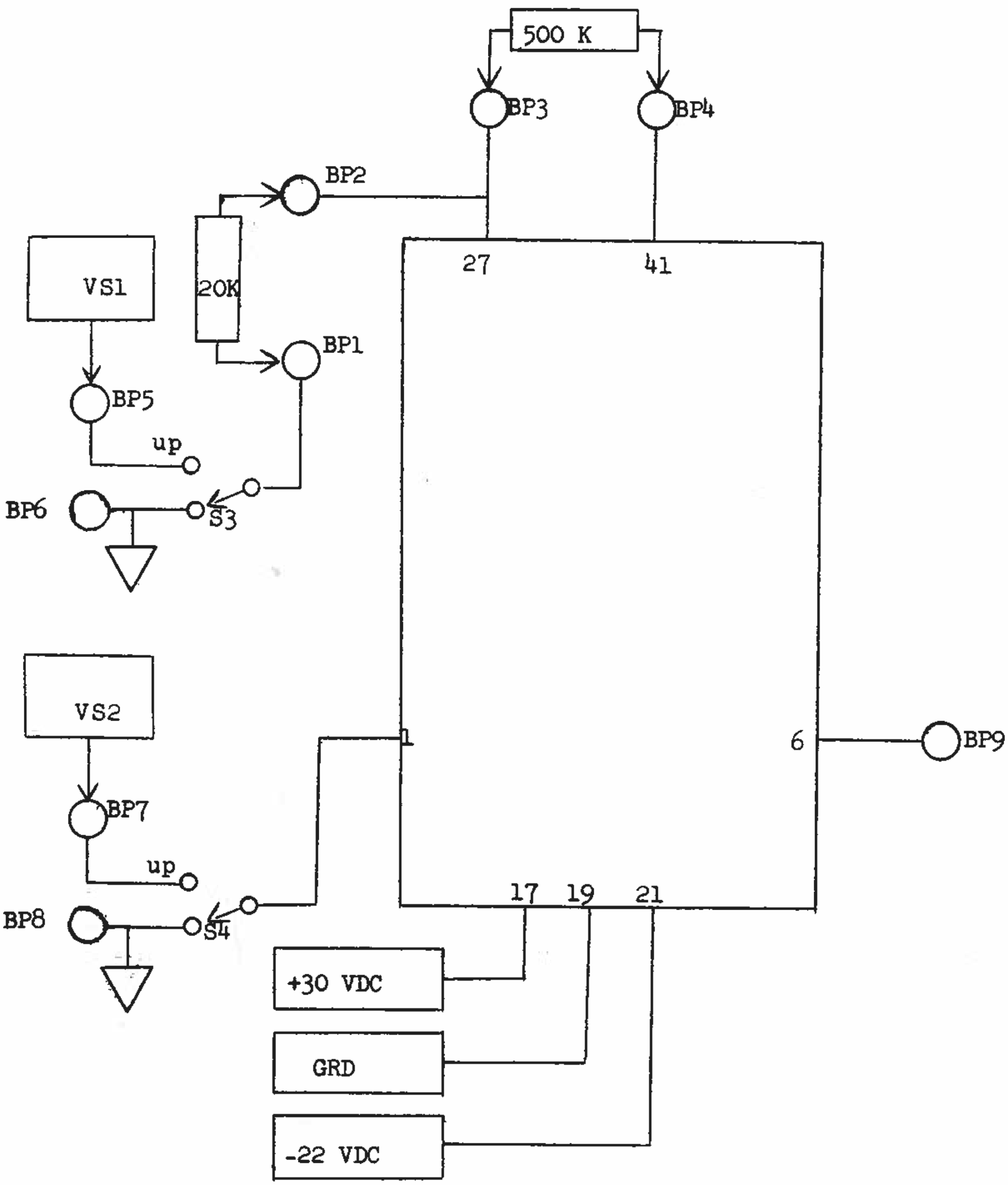
REV NO. 1

P24B-AL-4831
CONT ON SHEET 3 SH NO. 2

TITLE
Circuit Board Test Procedure
OP AMP/DFG Amplifier 948D881 G4
FIRST MADE FOR

REVISIONS
1 D.Y. Merc AUG 1 5 1974

--FIGURE A--



PRINTS TO

MADE BY B.A. Gimmel Apr. 1, 1968
ISSUED AUG 1 5 1974
APPROVALS
Steam Turbine DIV OR DEPT.
Schenectady LOCATION

REV NO.	TITLE
P24B-AL-4831	Circuit Board Test Procedure OP AMP/DFG Amplifier 948D881 G4
CONT ON SHEET --- SH NO. 3	FIRST MADE FOR

REVISION

AUG 15 1974

Test Procedure
Prepared By: C R Bugg DATE 11/27/73
C. R. Bugg
EHC TEST ENGINEER

Test Procedure
Reviewed By: S S Abelson DATE 8/7/74
EHC DESIGN ENGINEERING

Approved By: P C Callan DATE 8-7-74
P. C. Callan - Manager
EHC DESIGN ENGINEERING

PRINTS TO

Data Sheet

Job # _____

Serial # _____

Date _____

Data Sheet for __948D881G004_____

Test Procedure __P24B-AL-4831_____

Burn-in Start _____

Burn-in Stop _____

Technician _____

Test Procedure Step	Nominal	Lower Limit	Pre-Burn in Results	Post Burn in Results	Upper Limit	Pot Values If applicable CW	CCW	Pass/Fail
2	30VDC	29.999V			30.001V			
	-22VDC	-22.001V			-21.999V			
4	+13VDC	+12VDC			+14VDC			
	-13VDC	-14VDC			-12VDC			
5	0VDC							
5 -R8								
8	2.5VDC	2.4VDC			2.6VDC			
10		5.2VDC			7.2VDC			
12		-5.2VDC			-7.2VDC			
13	0VDC							
13 - R20								
16	5VDC							
16 -R1								
18	0VDC							
19	0VDC							

1/9/2012

cw