g		GE Industr	ial Systems	Functional Testing Specification						
	Renewal Ser Louisville,KY		LOU-GED-4136J56							
	Test Procedure for a 4136J56G0001 Card									
DOCUM	MENT REVISION STATUS	: Determined by the last e	ntry in the "REV" a	nd "DATE" col	umn					
REV.			SIGNATURE	REV. DATE						
Α	Initial release, re-wr	ite of Salem Procedure	)		D. Laemmle	3/20/03				
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#### Functional test procedure for a 4136J56G0001 Card

#### 1. SCOPE

**1.1** This is a functional testing procedure for a 4136J56G0001 Card.

## 2. STANDARDS OF QUALITY

2.1 Refer to the current revision of the IPC-A-610 standard for workmanship standards.

## 3. APPLICABLE DOCUMENTS

3.1 The following document(s) shall form part of this specification to the extent specified herein. Unless otherwise indicated, the latest issue shall apply.3.1.1

## 4. ENGINEERING REQUIREMENTS

- 4.1 Equipment Cleaning
  - **4.1.1** Equipment should be clean and free of debris prior to applying power unless performing an initial check. Refer to the local documented procedures for cleaning guidelines.
- 4.2 Equipment Inspection
  - **4.2.1** Equipment should be visually inspected for any defects prior to applying power. This inspection should include the following as a minimum:
    - 4.2.1.1 Wires broken or cracked
    - 4.2.1.2 Terminal strips / connectors broken or cracked
    - **4.2.1.3** Loose wires
    - 4.2.1.4 Components visually damaged
    - 4.2.1.5 Capacitors leaking
    - 4.2.1.6 Solder joints damaged or cold
    - 4.2.1.7 Circuit board burned or de-laminated
    - 4.2.1.8 Printed wire runs burned or damaged

## 5. EQUIPMENT REQUIRED

**5.1** The following equipment is required to perform the process requirements. Equipment may be substituted provided that all accuracy's and test ratios are equivalent or better.

Qty	Reference #	Description
1		Fluke 85 DMM (or Equivalent)

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#### 6. TESTING PROCESS

#### 6.1 Setup

- 6.1.1 Hook up power supplies and S1-S10 per the test setup drawing. Hook up the 500 ohm resistors from pins 38 to 41 to 26v per drawing but leave the other pins open.
- **6.1.2** Follow the test steps and the chart, operating the switches and checking the voltages on the pins corresponding to the LED state. Frequencies are checked on the Fluke on DC hertz.
  - Note: When the test calls for a particular LED to be on or off, check the voltage at the pin the LED is connected to per the test drawing. Pin low LED on. Pin high LED off. Blinking slow or fast can be determined by watching the voltage change rate of a pin that is to be flashing slow or fast.

# 6.2 Testing Procedure

- 6.2.1 See Attached. Follow the test steps and the test table, operating the switches and checking the voltages on the pins corresponding to the LED indicated.
- 6.3 \*\*\*TEST COMPLETE \*\*\*

#### 7. NOTES

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3-20-03;11:10AM;GE |NDSYS ;502 493 0640 # 1/ TOPE BOARD TEST أأنا أأست شريط فهاض الكاء وأمص ASSEMBLY DRAWING PC BOARD DRAWING 4136J56G1 TEST KIT SCHEMATIC DRAWING 4136J56 4136J25 1.0 INSPECTION .3 Solder/Wire \_\_\_\_\_ .5 Key Slot \_ 7 & 19 .l Identification .4 Temp. Cycle \_2 Comp./Conn.\_\_\_\_\_ REMARKS: Added step 4.6 JAW 2/26/85 2.0 SET-UP CONNECT UP +5, +15, +26, AND COM TO TEST KIT. 2.1 HOOK HE SOO A RESIDENCE PER 2.2 SET ALL SWITCHES OFF. DRAWING . HOOF UP SWITCHES ALSO, PLUG BOARD INTO TEST KIT. 2.3 LUBUR STATE SPENS OFCE. FON LOW - LED ON SIN HIGH & LED OFF 3.0 TEST CONNECT A 1K RESISTOR FROM COMMON TO THE FOLLOWING PINS, ONE AT A TIME. THE VOLTAGE AT EACH PIN WILL BE +.4 TO +.65 YDC.
PINS 5, 9, 10, 11, (12), 23, 24, 26, 28 +0.75 RPH
CONNECT A LK RESISTOR FROM COMMON TO THE FOLLOWING PINS, ONE AT A TIME. 3.2 THE VOLTAGE AT EACH PIN WILL BE +.5 TO +.8 VDC. PINS (8) (13), (14), 15. Remove resistor. 4.0 FREQUENCY ADJUST TURN S5 ON. 4-1 TURN S7 ON, THEN OFF. ALL LEDS OUT. TURN S6 ON, THEN OFF. LED 3 ON. 4.2 4.3 TURN S1 ON. ADJ R16 FOR 13 HZ AT PIN 32. 4.4 TURN S1 OFF. S2 ON. ADJ R15 FOR 130 HZ AT PIN 32. TURN S5 OFF. 13 HZ AT PIN 32. TURN S5 ON. 5.0 CHANNEL CHECKS TURN S5 OFF. S2 OFF. 5.1 SET SWITCHES PER FOLLOWING TABLE AND CHECK THAT ONLY THE LEDS CALLED FOR ARE ON, OR TURNING OFF AND ON.

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	3-20-03;11:10AM;GE IN	IDSYS				;502	493 0640	# 2/ 6
	SWITCHES	<u>ON</u>	1.0 w	BLINKING SLOW	BLINKING FAST	<u>on</u>	-81	- 10 Programme Control Control Control
,	S6 ON, THAN OFF	33 Low						
<b>(</b>	SI ON	7 tow	38000 <b>L3</b>	32 VARYIN L1 526W				
	S1 OFF, S2 ON	1.2	3 & L3	<b>]</b> .	SO VAR F	ast.		
	S2 OFF, S3 ON	3.2. L1	3.∑ L3	L2				
	S3 OFF, S4 ON	F.1	3 § <b>L3</b>		. L2			
	S4 OFF	<b>73</b>	7 + 3:	(1) 12 15 15 15 15 15 15 15 15 15 15 15 15 15	(4.23)			
	S7 ON, THEN OFF		1 <b>E -</b> 39	G-012 5.15 D	(280)			
	S8 ON, THEN OFF	중약 <b>L6</b>						:
	SI ON	L5	डु ११ <b>L6</b>	3 🖇 1.4				
	S1 OFF, S2 ON	Ľ5	3° <b>L6</b>		\$\S <b>L4</b>			
	S2 OFF, S3 ON	3 <u>2</u> L4	ু ক L6	€ <b>L</b> 5				
	S3 OFF, S4 ON	33 L4	პ% <b>L6</b>		L5			
	s4 OFF	3 <i>9</i> L6	6 4 33	Gr hist	- 4.2 x)			
<b>4</b>	S7 ON, THEN OFF	– иой	DE − 34	GOES WIG	er (tree)			
	\$9 ON, THEN OFF	<b>1.9</b>						
	SI ON	18 18	<i>үь</i> <b>19</b>	ĹŹ	ŕ			
	S1 OFF, S2 ON	<u>.</u> 18	년 5 1.9		ક ઈ <b>1.7</b>			
	S2 OFF, S3 ON	35° <b>L7</b>	પું≎ L9	ા L8				
	s3 off, s4 on	, \$ € <b>L7</b>	L9		L8			
	S4 OFF	ង១ L9	4 43.	1414 00	(P, 24)			
	S7 ON, THEN OFF	– иои	E - 40	coss : :-	· (210)			<u> </u>
	s10 on, then off	L12	L13					
	S1 ON	29 L11	L12	110		L13		
,	S1 OFF, S2 ON	Lii	ર્ખ≀ L12		<b>r</b> 10	L13		
l i	S2 OFF, S3 ON	Lio	L12	12°? 1111		L13		
	S3 OFF, S4 ON	ଞ୍ଚ L10	니? L12		Lii	L13	•	
<b>6</b> )	S4 OFF	식 ( L12	L13	.29+34 6	०,४) वकत्व	e)		
	S7 ON, THEN OFF	- NON	E 14	l sodeni.	దీశల 160క	2 20 0.5		·

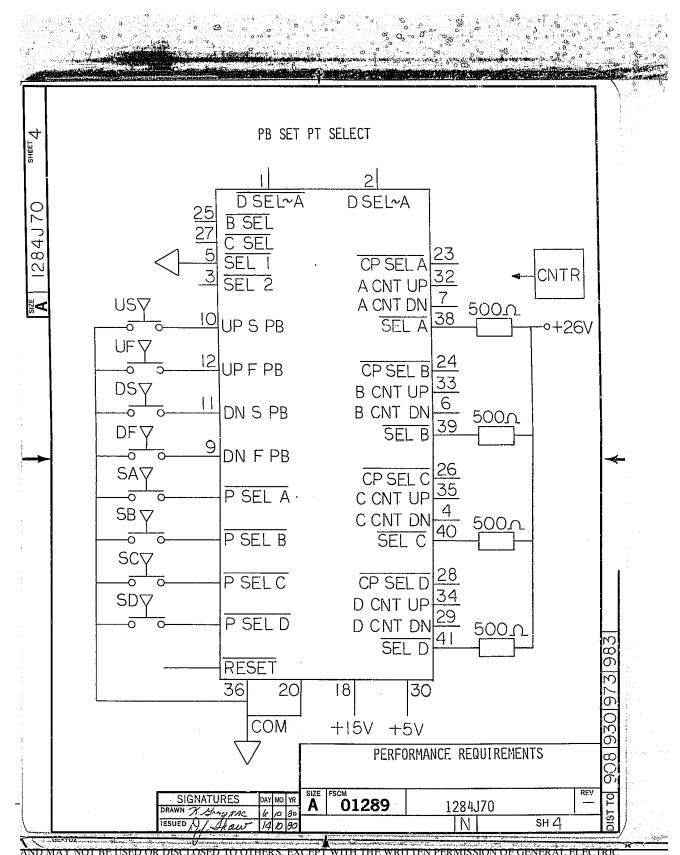
6.0 CURRENT CHECK

6.18 HEAD ACROSS THE +5 VDC CURRENT TEST JANOILONGOODE +0002 30 NOTE TO 82 NOW - VDC

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