CIRCUIT BOARD TEST SPECIFICATION
AUXILIARY RELAY BOARD 125V - 117D6678
FIRST MADE FOR EHC MARK II

REVISIONS

Refer to the drawing to see that

1. The relays coils all use 125 VDC.
2. The common of eight relays is connected to one input terminal.
3. The case ground of eight relays is connected to one input terminal.
4. The high side of each relay coil goes to a separate input terminal.
5. Each relay has two form C sets of output contacts -- all are independently connected to output terminals.
6. There are no interconnections on the card except the coil commons and the case grounds of each group of eight relays.
7. There are no time delay relays.

The relay specifications are in the "U" sheets.

ET-273
273-2
273-12
273-71
273-13
PRINTS TO

With the minus side of the power source connected to the (E) 125 terminal(s) and the plus applied (app) and removed (rem) alternately to coil terminals, the relays pick up and drop out as shown in the chart. A one (1) indicates an energized relay. A zero (0) indicates a de-energized relay and is usually omitted from the chart--for clarity.

			K1	K2	K3	K4	K5	K6	K7	K8	K9	K10	K11	K12	K13	K14	K15	K16
1.	TB 7- 97	app	1															
		rem																
2.	TB 7- 98	app		1														
		rem																
3.	TB 7- 99	app			1													
		rem																
4.	TB 7-100	app				1												
		rem																
5.	TB 8-105	app					1											
		rem																
6.	TB 8-104	app						1										
		rem																
7.	TB 8-103	app							1									
		rem																
8.	TB 8-102	app								1								
		rem																
9.	TB10-115	app									1							
		rem																
10.	TB10-114	app										1						
		rem																
11.	TB10-113	app											1					
		rem																
12.	TB10-112	app												1				
		rem																
13.	TB 9-107	app													1			
		rem																
14.	TB 9-108	app														1		
		rem																
15.	TB 9-109	app															1	
		rem																
16.	TB 9-110	app																1
		rem																

REVISIONS

TEST INSTRUCTIONS 1PC2-A001

Assembly 117D6678

Schematic 117D6679

PROCEDURE:

1. Connect the circuit under test as described in the setup instructions.

2. Test the logic circuitry using Table A.

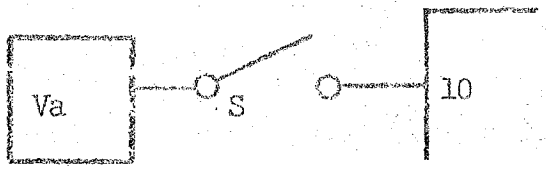
SETUP INSTRUCTIONS - FMCC 1PC2-AOOX

1. The loads to be applied to the circuit under test are ±1% tolerance.
2. Unless otherwise specified, all voltage to be applied and to be measured is +24.00 VDC (tolerance ±100 MV)
3. This circuit card has 116 pins total with 116 pins in use. The terminal board configuration is 6 - 16 pt. and 4 - 5 pt.
4. Connect pins 101, 106, 111 and 116 to ground.
5. Va = 125 VDC.

INSTRUCTIONS FOR USE OF LOGIC TABLE

A. s 10 Va = set 10 to Va.

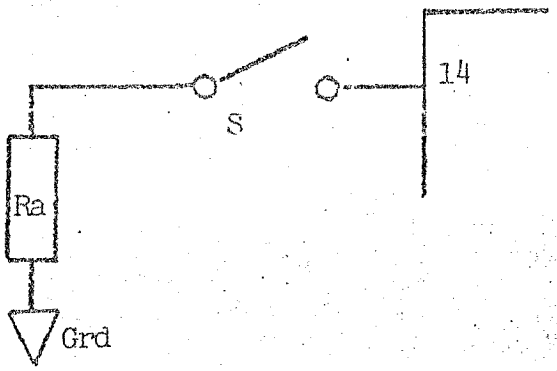
Close S to apply Va to Pin 10
If Va is not specified, assume +24.0 VDC.



B. r 10 Va = reset 10 to Va

open S

C. s 14 Ra = set 14 to Ra



Close S to apply load Ra to Pin 14.

D. An "X" in the logic table indicates a voltage measurement of +24.00VDC. A letter (ie "C") in the logic table indicates a voltage measurement, the voltage being equal to that specified by the subscript letter (ie "Vc"). A blank space indicates a 0 VDC measurement.

E. The voltage at all pins and all test points will be measured at each step of the logic table.

REV NO. O

P3K-AL-0340-A01
CONT ON SHEET 7 SH NO. 6

TITLE
CIRCUIT BOARD TEST SPECIFICATION
Auxillary Relay Board 125V
FIRST MADE FOR

REVIS

125 VDC AT PINS: 97 98 99

24 VDC AT PINS:

	INITIAL CONDITIONS	51	50	55				54	53	54				57	56
		50	49	50				53	52	53				56	55
		3	2	3	51		6	6	5	6	54		9	9	8
		2	1	2	50		5	5	4	5	53		8	8	7
1	INITIAL CONDITIONS														
2	s 2														
3	s 50														
4	s 97 Va														
5	r 97 Va														
6	r 2														
7	r 50														
8	s 5														
9	s 53														
10	s 98 Va														
11	r 98 Va														
12	r 5														
13	r 53														
14	s 8														
15	s 56														
16	s 99 Va														

PRINTS

MADE BY D. Mone Jan. 15, 1975
ISSUED JAN 15 1975

APPROVALS

Steam Turbine DIV OR DEPT.
Schenectady LOCATION

P3K-AL-0340-A01
CONT ON SHEET 7 SH NO.

REV NO.

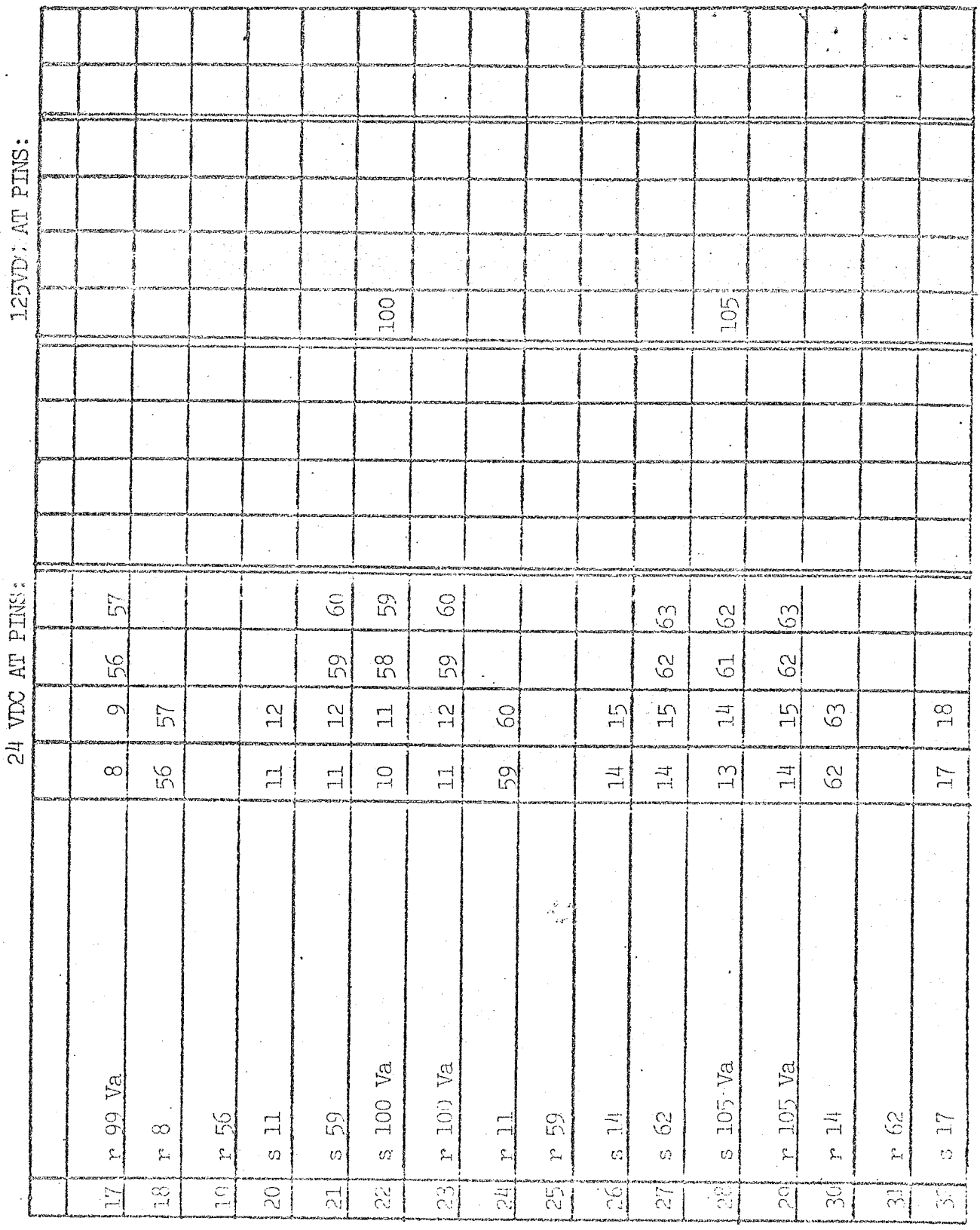
P3K-AL-0340-A01

CONT ON SHEET 8 SH NO. 7

TITLE

CIRCUIT BOARD TEST SPECIFICATION
Auxiliary Relay Board 125V
FIRST MADE FOR

REVISIO



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P3K-AL-0340-AO1

CONT ON SHEET 9 SH NO. 8

TITLE

CIRCUIT BOARD TEST SPECIFICATION
Auxiliary Relay Board 125V

FIRST MADE FOR

REVISIO

125VDC AT PINS:			104						103						102			
24VDC AT PINS:		66	65	66				69	68	69				72	71	72		
		65	64	65				68	67	68				71	70	71		
		18	17	18	66		21	21	20	21	69		24	24	23	24	72	
		17	16	17	65		20	20	19	20	68		23	23	22	23	71	
	c 65	s 104 Va	r 104 Va	r 17	r 65	s 20	s 68	s 103 Va	r 103 Va	r 20	r 68	s 23	s 71	s 102 Va	r 102 Va	r 23		
	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48		

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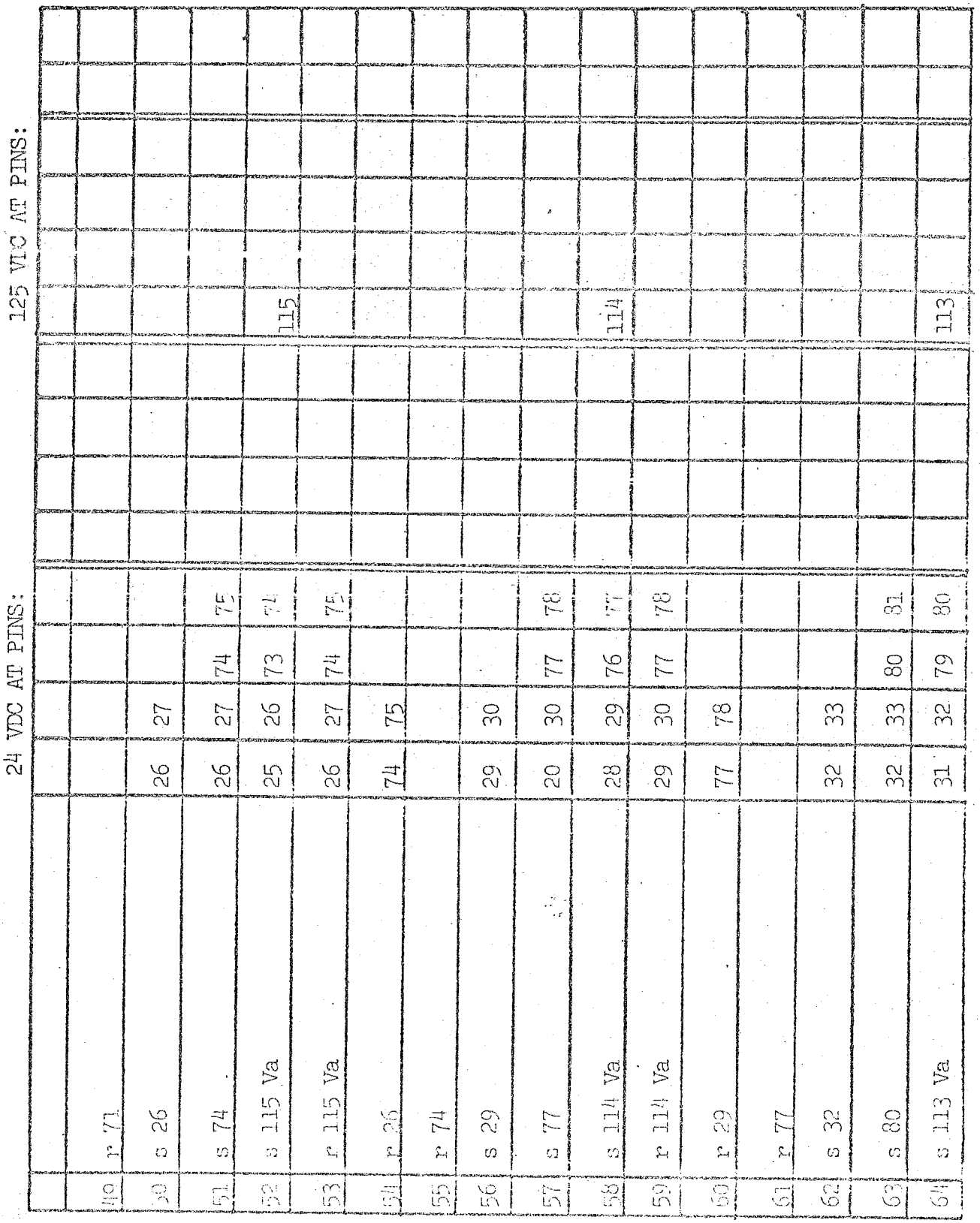
CONT ON SHEET SH NO

P3K-AL-0340-A01
CONT ON SHEET 10 SH NO. 9

TITLE
CIRCUIT BOARD TEST SPECIFICATION
Auxiliary Relay Board 125V
FIRST MADE FOR



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REV NO.

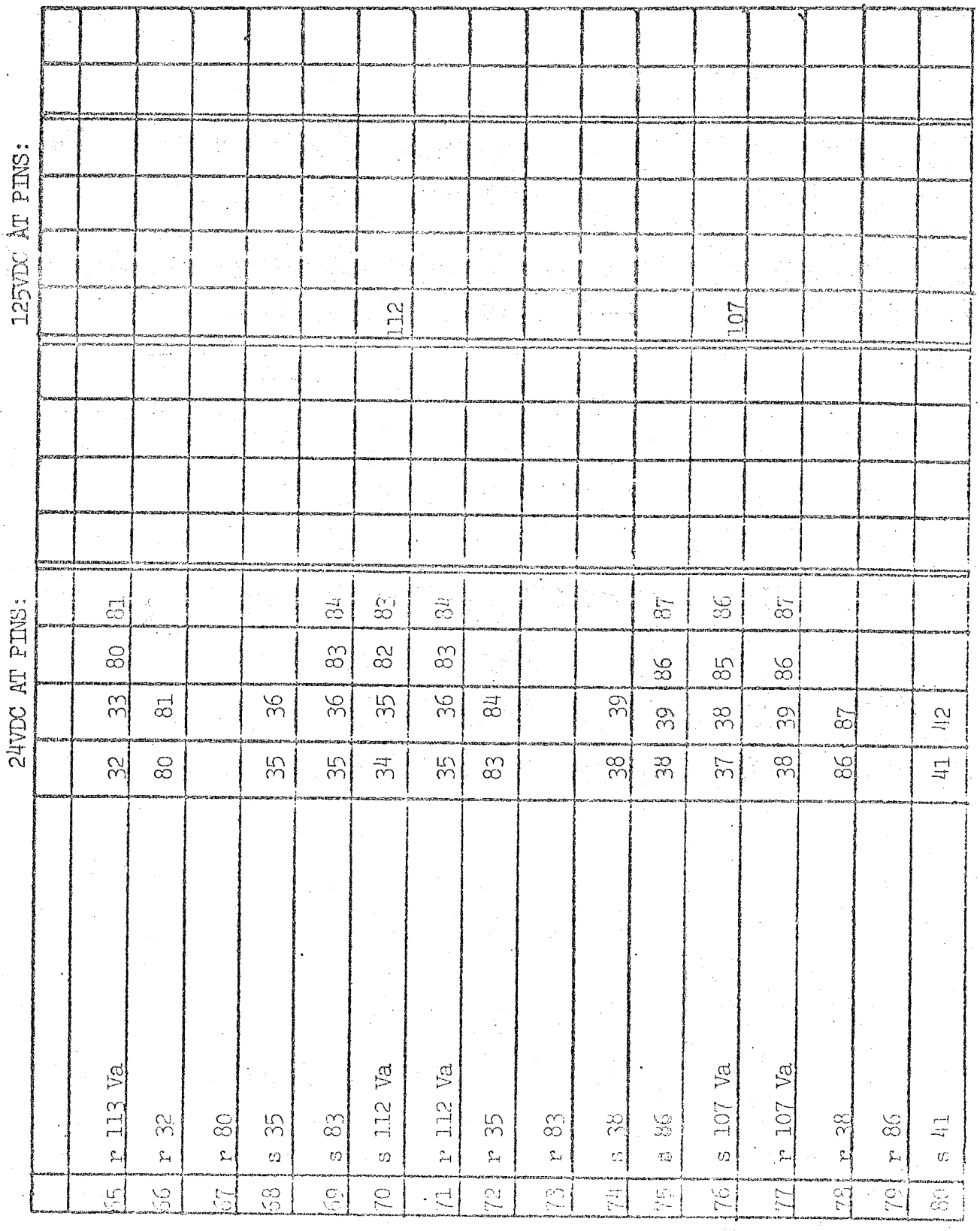
P3K-AL-0340-A01

CONT ON SHEET 11 SH NO. 10

TITLE
CIRCUIT BOARD TEST SPECIFICATION
Auxiliary Relay Board 125V

FIRST MADE FOR

REVIS



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Schenectady
LOCATION

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CODE

REV NO.

P3K-AL-0340-A01

CONT ON SHEET 12 SH NO. 11

TITLE

CIRCUIT BOARD TEST SPECIFICATION
Auxiliary Relay Board 125V

FIRST MADE FOR

REVISI

125 VDC AT PINS: 108, 109, 110

24 VDC AT PINS:

90	89	90				93	92	92				96	95	96		
89	88	89				92	91	92				95	94	95		
42	41	42	90		45	45	44	45	93		48	48	47	48	96	
41	40	41	89		44	44	43	44	92		47	47	46	47	95	
s 89	s 108 Va	r 108 Va	r 41	r 89	s 44	s 92	s 109 Va	r 109 Va	r 44	r 92	s 47	s 95	s 110 Va	r 110 Va	r 47	
81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	

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P3K-AL-0340-A01

CONT ON SHEET 13 SH NO. 12

TITLE
CIRCUIT BOARD TEST SPECIFICATION
Auxiliary Relay Board 125V

FIRST MADE FOR

CONT ON SHEET 13 SH NO.

REV

24 VDC AT PINS:

97	r 95
98	End of Test

MADE BY
D.Mone Jan. 15, 1975

ISSUED
JAN 15 1975

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P3K-AL-0340-A01
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TITLE CIRCUIT BOARD TEST SPECIFICATION
Auxiliary Relay Board 125V---117D6678

FIRST MADE FOR

REVISIONS

PREPARED BY: George W. Kessler DATE: 3/6/73
George W. Kessler
EHC DESIGN ENGINEERING

APPROVED BY: P C Callan DATE: 1-13-75
P.C. Callan - MANAGER
EHC DESIGN ENGINEERING

TEST PROCEDURE
PREPARED BY: C R Bugg DATE: 12/23/74
C. Bugg
EHC TEST ENGINEER

TEST PROCEDURE
APPROVED BY: George W. Kessler DATE: 1/6/75
George W. Kessler
EHC DESIGN ENGINEERING

MADE BY D.Mone Jan. 15, 1975
ISSUED JAN 15 1975

APPROVALS

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Schenectady, N.Y. LOCATION

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