

SIZE  
A

165A741AD

SHEET

2

REV

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D515/C-3

## TEST INSTRUCTIONS FOR POS 3 MDT-80S

1. Clip lead in the following components:

R1 1 K  
R2 1 K  
R3 1 K  
R4 1 K  
R5 1 K  
R6 1 K  
R8 4.99 K  
R9 4.99 K  
R10 10 K

\*Leave all other spots open.

2. Connect the circuit as shown in Fig. 1. Note that PS2 and PS3 are clipped in at the junction of C22 and R28, and C24 and R33 respectively. Turn on power and observe that M1 and M2 read less than \_\_\_\_\_ Ma.

3. Adjust PS2 to 0V. Vary P2 and verify that the range at TP3 is 5.3V to 10.9V. Adjust PS2 to get 0V at TP3.

4. Adjust PS3 to 0V. Vary P3 and verify that the range at TP4 is 5.3 to 10.9V. Adjust PS3 to get 0V at TP4.

5. With 0V at TP3 and TP4, adjust PS1 to 0V. Turn P1 fully CCW. M3 should now read less than 20 Ma. Now turn Board POT P1 fully CCW. M3 should read over 400 Ma. Verify that TP6 ranges from -1.33V to 1.33V as this is done. Adjust P1 to get 125 Ma on M3.

6. Check the gain of the servo amplifier. Adjust PS1 to get 100 Ma on M1. Record the TP2 voltage. Adjust PS1 to get 350 Ma on M1. Record the TP2 voltage. The difference between the two voltages should be .375V  $\pm$  1%.

7. Noise check. With an ungrounded scope check that the noise between TP9 and TP10 is less than 250 Ma.

Data Needed: Quiescent Current Requirement from  $\pm 15V$  supplies for normal board.

DIST. TO: 12G, 14E, 19J, RW219A

GENERAL ELECTRIC  
MDTD FITCHBURG  
DEPT LOC

SIZE

GOOD IDENT NO

A

TEST INSTRUCTIONS FOR 125D458AD  
(LYNN TEST)

JD Wronski, 9/4/80

SCALE

165A741AD

SHEET

2 CONT

## Data Sheet

[illegible]

CW

## Data Sheet

[illegible]

.604  
.211

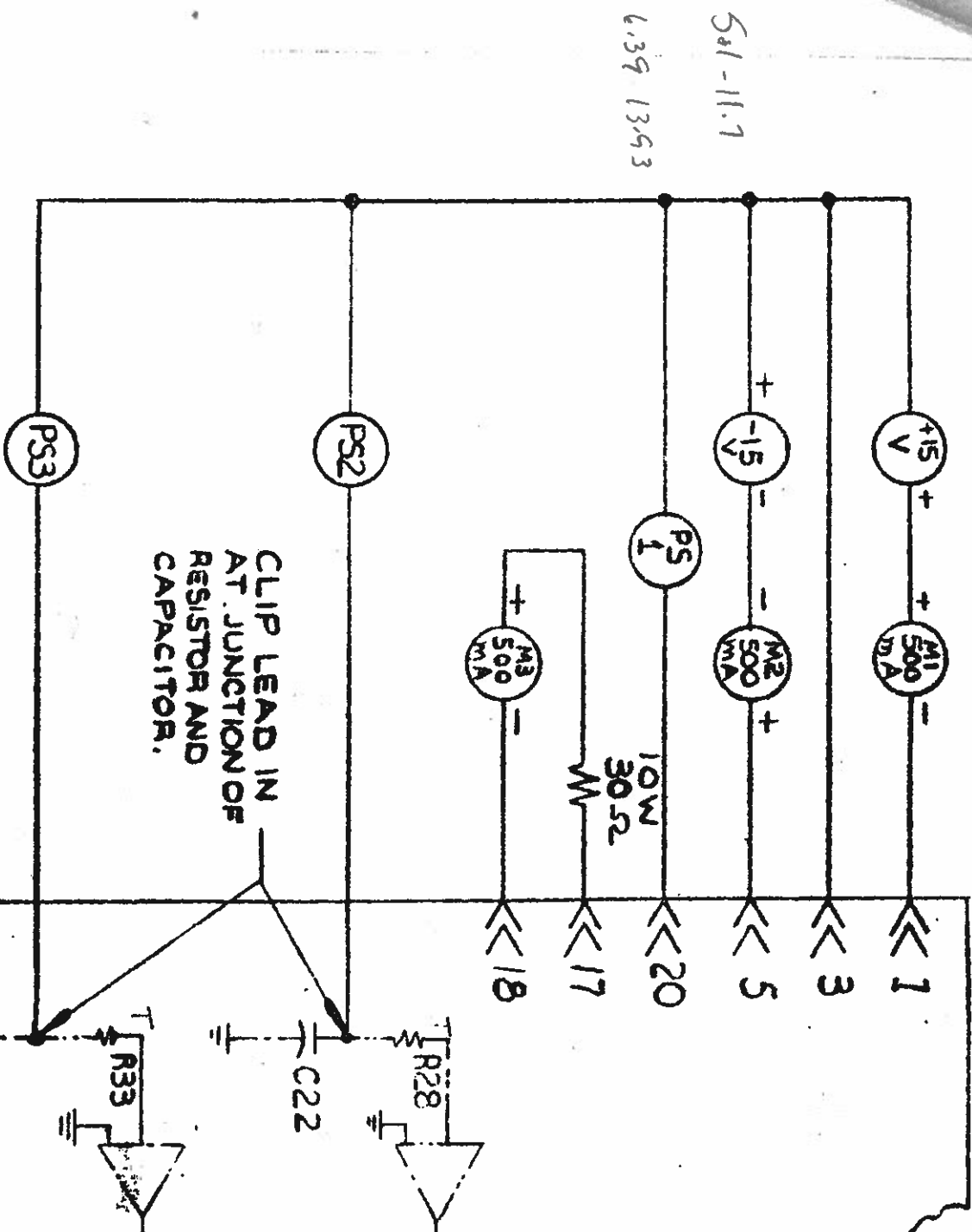


FIG 1