GENERAL (%) ELECTRIC 224X673AA TITLE CONT ON SHEET 2 REVERSE LOGIC CARD 224X673AA TEST INSTRUCTIONS FIRST MADE FOR SH NO.] 193X185AAG01 1.0 SCOPE REVISIONS This instruction covers the test procedure for production testing of the 193X185AAGol reverse legic card. Test conditions are given in Section 3.0. Performance is covered in Engineering Specification 224X329AA. 2.0 INSTRUCTIONS In these tests a "high" (H) refers to 3.5 to 5.25 volts. A 'Low" (L) refers to zero to 0.5V. 2.01 REVERSING Run through the following truth table in sequence. Tabs 9 and 12 need not be checked after the first three steps. Input Conditions Output Check Tab 14 Tab 16 Tab 13 Tab 6 Tab 5 Tab 17 9 Tab 8 11 12 Η L Н Н L H Η L Η L Н L Η L L Н L Η Н L H L L H L L Pulse H Pulse H L y L Pulse H L Η L Η L Pulse H L L Pulse H L Ι. Pulse H Η 2.02 LINEAR TIMING Turn both pots P477 and P478 full clockwise. Short Tab 5 to common. Place a -10 volt step into Tab 19. Tab 10 should take more than 35 seconds to time up to ± 10 volts ($\pm .25$ volts). Remove the ± 10 volts to Tab 19 and Tab 10 should take more than 35 seconds to time down to zero. 2.03 REFERENCE CLAMP Again apply -10 volts to Tab 19. Then apply a +3 volts to Tab 30 and the Tab 10 should settle to a final value between zero and -1 volt. 2.04 REVERSING CLAMP With -10 volts applied to Tab 19 and Tab 5 open, short Tab 13 to common. TO(1) Tab 10 should time to a final value between zero and -1 volt. Momen-5D(BW) tarily short Tab 5 to common, Tab 10 should return to +10 (+.25V). 5E(BW) Now open Tab 13, Tab 10 should again go to 0 to -1V 50C(2BW)

					5(SL)
MADE BY					PRINTS TO
ISSUED J. G. Tracy	and discounts	_SPEED_VARIATOR	DIV OR DEPT.	224X673AA	
73,1000 m Feb 5.71	100:01	ERIE, PA.	LOCATION	CONT ON SHEET 2	SH NO. 1
FF-803-WB (2-70) PRINTED IN U.S.A	•	A			

5R(BW)

GENERAL (%) ELECTRIC 224X673AA REV NO. CONT ON SHEET F1 sh NO. 2 TITLE REVERSE LOGIC CARD TEST INSTRUCTIONS 224X673AA 193X185AAG01 CONT ON SHEET F1 sh NO. 2 FIRST MADE FOR REVISIONS 2.05 CURRENT SUMMING Short Tab 5 to common and run through the following sequence, checking the output at Tab 22. Condition Output Check Tab 27 = +2VTab 25 = H-3.1 to -3.5V Tab 25 = LTab 27 = +2V0 to +0.2V. Tab 28 = +2V. Tab 16 = H-3.1 to -3.5V Tab 28 = +2VTab 16 = L+ 0 to +0.2VTab 29 = +2V. Tab 14 = H-3.1 to -3.5V.Tab 29 = +2VTab 14 = L0 to +0.2V2.06 With Tab 29 = +5V. and Tab 14 = H, the output on Tab 20 can be varied with pot P476 and output on Tab 24 with P475. 2.07 Input to Tab 21 through a 10K resistor. Output check on Tab 26. The input voltage for which a change in outvoltage takes place should be: Output Input 9.3v to +11.7v0 change to approx. +10V. -9.3V to -11.7V 0 change to approx. -10V. 3.0 TEST CONDITIONS 3.01 DC Supply Voltages: +19.8 to 20.2V -19.8 to 20.2V 4.9 to 5.1V Room Temp. 4.0 REQUALIFICATION Ù. This card should be requalified by Quality Control every 18 months or every 200 production cards, whichever comes first. Ö Ü Ŧ. Ò TD(I) 5D(BD) 5E(BW) 5QC(2BV

MADE BY J.G. Tracy 10 to hm Feb 5.71

ERIE,

SPEED_VARIATOR PA.

224X673AA

LOCATION CONT ON SHEET F1

CODE IDENT NO

5R(BW) 5(SL) PRINTS TO

FF-803-WA (I-70) PRINTED IN U.S.A

+