GE Power Generation Engineering

Materials and Processes Engineering
Schenectady, NY 12345

PROCESS SPECIFICATION

P3K-AL-0045-A01

PROCESS INSTRUCTIONS FOR TESTING THE PRESSURE-LOAD GATE AMPLIFIER CIRCUIT BOARD 114D6003 G-2

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INACTIVE FOR NEW DESIGN

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PROCESS INSTRUCTIONS FOR TESTING THE PRESSURE-LOAD GATE AMPLIFIER CIRCUIT BOARD

FIRST MADE FOR Drawing 114D6003 G-2

REVISIONS

SCOPE

This process instruction provides a method for performing a functional check with the necessary adjustments for the subject circuit board prior to installation in the EHC cabinet.

TEST PROCEDURE

(1) Examine the circuit board to see that the electrical components and printed circuits are not physically damaged.

(2) Measure the capacitance of capacitors C3, C4, and C5 between pins 32 to 19, 34 to 19, and 36 to 19, respectively, with a capacitance bridge to assure that each is between 14.4 and 21.6 microfarads.

(3) Check for continuity between pins 8 and 9.

(4) Check for continuity between pins 6 and 7.

(5) Plug circuit board 114D6003 G-1 into the test fixture.

(6) Use an ohmmeter to check that there are no short circuits between any combinations of pins 17, 19, 21, 38, and 40.

(7) Connect a resistance of 900 ohms between pins 37 and 19.

(8) Connect a resistance of 900 ohms between pins 27 and 19.

(9) Connect the input of a high gain dc operational amplifier to pin 29 and the output to pin 41.

(10) Connect a well regulated plus (+) 30.0 volt dc power supply to pins 17 and 38. The negative voltage terminal should be connected to pin 19.

(11) Connect a well regulated minus (-) 22.0 volt dc power supply to pins 21 and 40. The positive voltage terminal should be connected to pin 19.

(12) All voltage measurements will be made with respect to testpoint TP11

(13) Adjust R3 to provide +10.40 volts at pin 37.

(14) Adjust R2 to provide -10.40 volts at pin 27.

(15) Apply a test voltage of +0.500 volts to pin 20 as measured at TP1. The voltage at pin 25 and TP2 should be between -4.90 and -5.15 volts.

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REVISIONS

(16) Apply a test voltage of -0.500 volts to pin 20 measured at TP1. The voltage at pin 25 and TP2 should be between -0.10 and +0.10 volts.

(17) Remove all test equipment.

(18) Remove the circuit board from the test fixture and identify it with a suitable mark to indicate that it has been tested and adjusted in accordance with this instruction.

PREPARED BY: Hans Keller DATE: Feb 26, 1970
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APPROVED BY: P C Callan DATE: 3/2/70
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