

# TEST INSTRUCTIONS INTERFACE BOARD

ASSBL 4136J58

SCHEM 4136J27

1.  $\overline{\text{IN1/0}}$ ,  $\overline{\text{OUT1}} < .4\text{V}$  IN  $\approx .1$  MS, LED1 ON.
2.  $\overline{\text{IN1/OPEN}}$ ,  $\overline{\text{OUT1}} > 2.5\text{V}$  IN  $\approx .5\text{MS}$ , LED1 OFF.
3. REPEAT FOR  $\overline{\text{IN2}} \rightarrow \overline{\text{IN12}}$ .
4. APPLY 26 VOLTS THRU  $325\Omega$  TO PIN 6. PIN 5 OPEN, PIN 6 = 26V, PIN 5 AT +4V, PIN 6  $< .4\text{V}$ .
5. REPEAT 7 FOR PINS 9, 10; 15, 16; 25, 26.

## PERFORMANCE REQUIREMENTS

SIGNATURES			DAY	MO	YR
DRAWN	WZM	TAC	2	10	80
ISSUED	B. Shaw		14	10	80

SIZE A FSCM 01289

1284J72

REV  
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N

SH 2

DIST TO 908 930 973 983