

2 7 7 A 3 9 0 4

CONT ON SHEET 2

SH NO. 1

REV
NO.

2 7 7 A 3 9 0 4

CONT ON SHEET 2

SH NO. 1

TITLE

TEST SPECIFICATIONS

FIRST MADE FOR 44C372639G01

REVISIONS

STANDING INSTRUCTIONS

FOR

VOLTS/HZ TRIP

INPUT

PRINTED CIRCUIT BOARD

44C372639

Distribution Copies

- 1 - QC Test
- 1 - QC Engineer
- 1 - Engineer

3EL1

4QA1

1RA2

4EK1

DL13

PRINTS TO

MADE BY

R. K. Gerlitz 790212

ISSUED

2/21/79

APPROVALS

SR *Pr...*
2-20-79

Drive Systems

Salem, VA USA

DIV OR
DEPT.

LOCATION

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REV NO. 2 7 7 A 3 9 0 4	TITLE TEST SPECIFICATIONS
CONT ON SHEET 3	SH NO. 2

FIRST MADE FOR 44C372639G01

I. Test Equipment Required

- A. Printed Circuit Board Test Setup - 44C931365.
- B. Adaptor - Amp. Mod. II 30 Pin.
- C. Power Supply Cable.
- D. Patchboard PB-8.
- E. Drawings
 - 44C309886 Elementary REV.3
 - 44C372639 Assembly
 - 44C931365 Elementary of Test Table
 - MI 217000 V-62 CMOS applies.

II. Connection

- A. Connect the adaptor cable to "PL1" on the Universal Tester (U.T.).
- B. Connect the Amp. Mod. I Power Supply cable to PL1 on Universal Power Supply and to Power Supplies per marking.
- C. Insert Patchboard PB-8 in carrier of Universal Tester and close.
- D. Connect a AC/DC digital voltmeter to "BJ-1" Red (+) and Black (-).
- E. Insert a 28 volt lamp in "L14".

III. Wire Check

Pin	To	Test Point	Resistance (Ohms)
27		2	104.5K 115.5K
20		7	10.25 to 11.75K
16		8	.95K to 1.05K

Visual Check

	Resistance (Ohms)
13R	10K
19R	10K
30R	10K
37R	10K
42R	100K
5R	5.1K
10R	6.2K
16R	6.2K
27R	1 Meg.

Adjust 1P fully counter clockwise.
4P fully counter clockwise.
3P fully counter clockwise.

REVISIONS

1) 44C309886 10-18-79
2) SES 841002

3EL1
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PRINTS TO

MADE BY R. K. Gerlitz 790212

APPROVALS
SIR P. A. ...
2-1-77

Drive Systems
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CONT ON SHEET 3 SH NO. 2

REV NO.	
2 7 7 A 3 9 0 4	
CONT ON SHEET 4	SH NO. 3

TITLE	TEST SPECIFICATIONS
FIRST MADE FOR	44C372639G01

IV. Setup

- A. Turn all switches to OFF or normal on both the UT and UPS.
- B. Turn all power supplies to zero on the UT and all variacs to zero on the UPS.
- C. Apply power to test stand.
- D. Insert board under test (B.U.T.) into test adaptor.
- E. On 1TB connect jumper to ter.2 and 3.

V. Electrical Test

- A. Close "SW-1". Depress "LPB-1" and adjust power supply #1 (PS-1) to 24 ± 0.2 VDC at "BJ-1". *connect 24vcom to pin 10 and*
- B. Close "SW-10", depress "LPB-2" and adjust PS-2 to 15 ± 0.1 VDC at "BJ-1". *15V to pins 1+ and 3-*
- C. Depress "LPB-3" and adjust PS-3 to 15 ± 0.1 VDC at "BJ-1". *15V to pin 3+ and 5-*
Depress "LPB-5", "BJ-1" = 12.5 ± 0.5 VDC. *pin 18 = 12.5*
Close "SW-14". *connect pin 14 to +24V through a*

D. Adjust -

3P	TP3 (+) to TP1 (-)
CW	15 ± 0.1 VDC
CCW	6.0 ± 0.3
Set	11 ± 0.005

Open "SW-14". *Remove 24V from pin 14*

E. Adjust 6P

CW	TP-5 (+) to TP1 (-)
CCW	15 ± 0.1 VDC
Set	6.0 ± 0.3
	12 ± 0.005

- F. Depress the main power pushbutton. Close 3Ø power switch on the U.P.S. Close "SW-5". Depress "LPB-7". Increase Ø1 balance variac on the U.P.S. until "BJ-1" reads 17 ± 0.1 VAC. *17 VAC pin 3-11*

G. Adjust 2P

CCW	TP2 (+) to TP1 (-) VDC
CW	-2.9 ± 0.3
Set	-6.85 ± 0.3
	-5 ± 0.005

- H. Increase Ø1 balance variac on the U.P.S. until TP2 (+) to TP1 (-) = -5.9 ± 0.05 VDC.

Retyped

REVISIONS

1. 10-10-79
2) SES 841002

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PRINTS TO

MADE BY	R. K. Gerlitz 790212
ISSUED	2/21/79.

APPROVALS	Dr R. P. ...
	2.1.11

Drive Systems
Salem, VA USA

DIV OR
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2 7 7 A 3 9 0 4

CONT ON SHEET 4 SH NO. 3

CODE IDENT NO.

CONT ON SHEET 5 SH NO. 4

CODE IDENT NO.

REV NO.	TITLE TEST SPECIFICATIONS		CONT ON SHEET FL	SH NO. 5
CONT ON SHEET	SH NO.	FIRST MADE FOR	44C372639	

P. Connect time interval counter as follows:

Start	Stop	Common
TP4	TPP	TP1
SLOPE +	Slope +	
Trigger-1V	Trigger-1	
Atten 10	Atten 10	

*See Note.

Q. Open "SW-5" *Remove AC from 11*
Reset Counter
Close "SW-5" *APPLY AC to pin 11*
Counter shall read 1.2 to 1.7 seconds

R. Connect jumper 1TB to ter.1 and 2
and repeat step Q
Counter shall read less than 10 milliseconds
Return jumper to ter. 2 and 3 and tighten

S. Open "SW-1". Then open or return to normal all remaining switches.
Turn all variacs and DC power supplies to zero.
Remove All power

*NOTE: For Steps Q and R, momentarily jumper 6C and 7C to discharge them before checking time interval.

MADE BY
R.A.Morris 791017

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10/17/79

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DRIVE SYSTEMS

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CONT ON SHEET FL SH NO. 5

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