REVISIONS

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REV / V 2

TITLE

BOARD TEST SPECIFICATION - VOLTAGE SENSOR, 1P2-E007 - SCHEMATIC 117D7368

P3K-AL-0336-A01 3 CONT ON SHEET

2 SH NO.

FIRST MADE FOR

## 6. TEST SPECIFICATION

- Connect the board as shown in Figure 1.
- With PSl set at 0 volts and 24 VDC set on PS2, close S1 and S2. Indicators 1 and 2 shall indicate, indicator 3 shall not.
- C. Press in succession PBI, PB2, and PB3. No change shall occur in the indicators.
- Increase the voltage between TP52 and TP53 at a 10 volt per second , rate. Stop when the voltage reaches 125 ± 0.5 VDC: There shall be no change in the indicators.
- Press and release PB1. Indicator91 and 2 shall cease indicating.
- Reduce the PSI voltage at a 5 volt per second rate. Indicators 1 and 2 shall indicate where the voltage between TP52 and TP53 is 115.5  $\pm$  2.5 VDC. Increase the voltage to 125  $\pm$  0.5 volts.
- Press and release PB2. Indicators i and 2 shall cease to indicate.
- Press and release PB3. There shall be no change in the status of the indicators.
- Increase the voltage between TP52 and TP53 at the 5 volt per second rate to 145 + 0.5 VDC. Indicators 2 and 3 shall indicate when the voltage reads 133 + 3 VDC.
  - Reduce the voltage between TP52 and TP53 to 125 + 0.5 VDC. shall be no changes in the status of the indicators.
- Press and release PB3. Indicators 2 and 3 shall cease indicating.
- Μ. Repeat steps J and K. Press and release PB2. Indicators 2 and 3 shall cease indicating.
- Reduce PS1 and PS2 to 0 VDC. Open S1 and S2.

## REPORT SHEET

ACTION

Close S1 & S2 Ind. 1, 2 indicate Ind. 3 not indicate

APPROVALS

PRINTS TO

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Steam Turbine

Schenectady, N.Y.

REACTION

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P3K-AL-0336-A01

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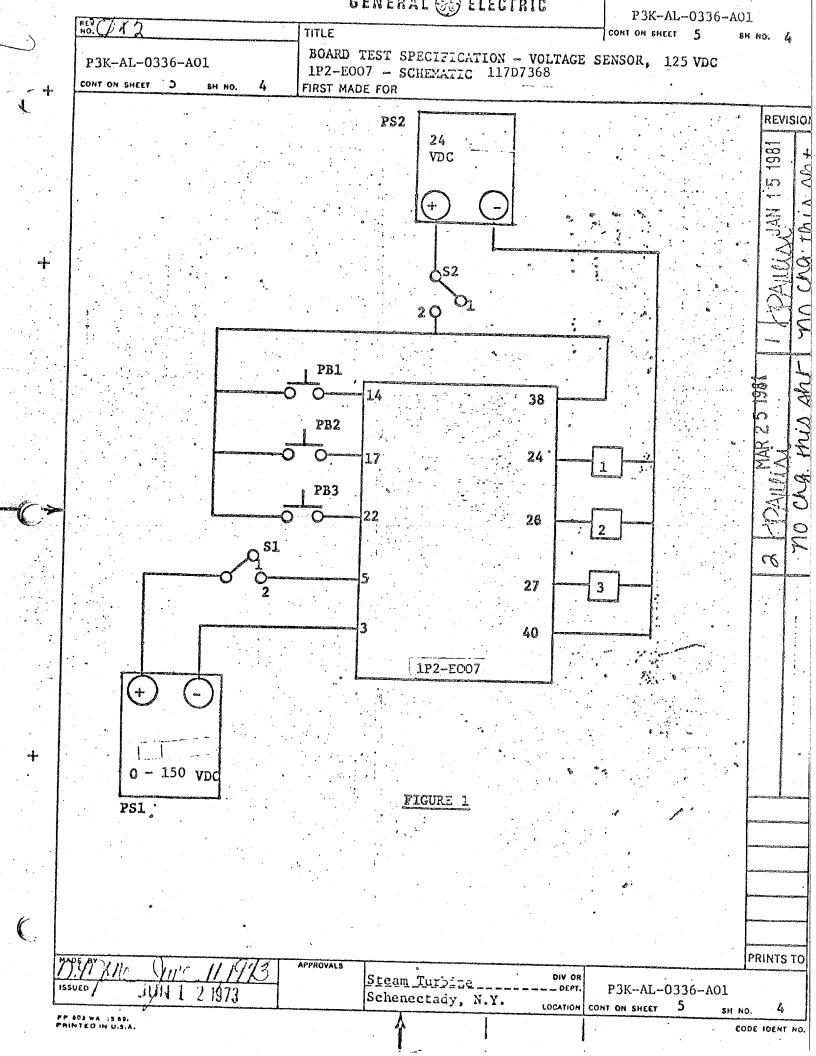
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PF-803-WA (11-72)

CODE IDENT NO



P3K-AL-0336-A01

SH NO.

Board Test Specification-Voltage Sensor, 125 VDC

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CONT ON SHEET

FIRST MADE FOR MARK II

TEST INSTRUCTIONS 1P3-

Assembly 117D8507

Schematic 117D7368

The 125 VDC sensor monitors the output voltage level of its specific power supply. If the voltage level falls below 113 VDC, \*\*\*

KI de-energizes, contact KI-1 closes to apply a signal to pins 24 and 26.

If the voltage level rises above 136 VDC, KL1 energizes, contact KL1-1 closes to apply a signal to pins 27 and 26. A reset signal applied to pins 14 or 17 will pick up K2 allowing KI-1 to latch in. Similarly, a reset signal applied to pins 22 or 17 will pick up KL1-R.

## TEST PROCEDURE:

- Refer to Fig. A for test circuit.
- 2. All switches down.
- 3. Read voltage of VS1 from TP53 to TP52.
- 4. Adj. VSl for 0 VDC.
- 5. The chart which follows outlines the procedure for testing the relay logic of this circuit.

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Steam Turbine
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Location Cont on Sheet 6

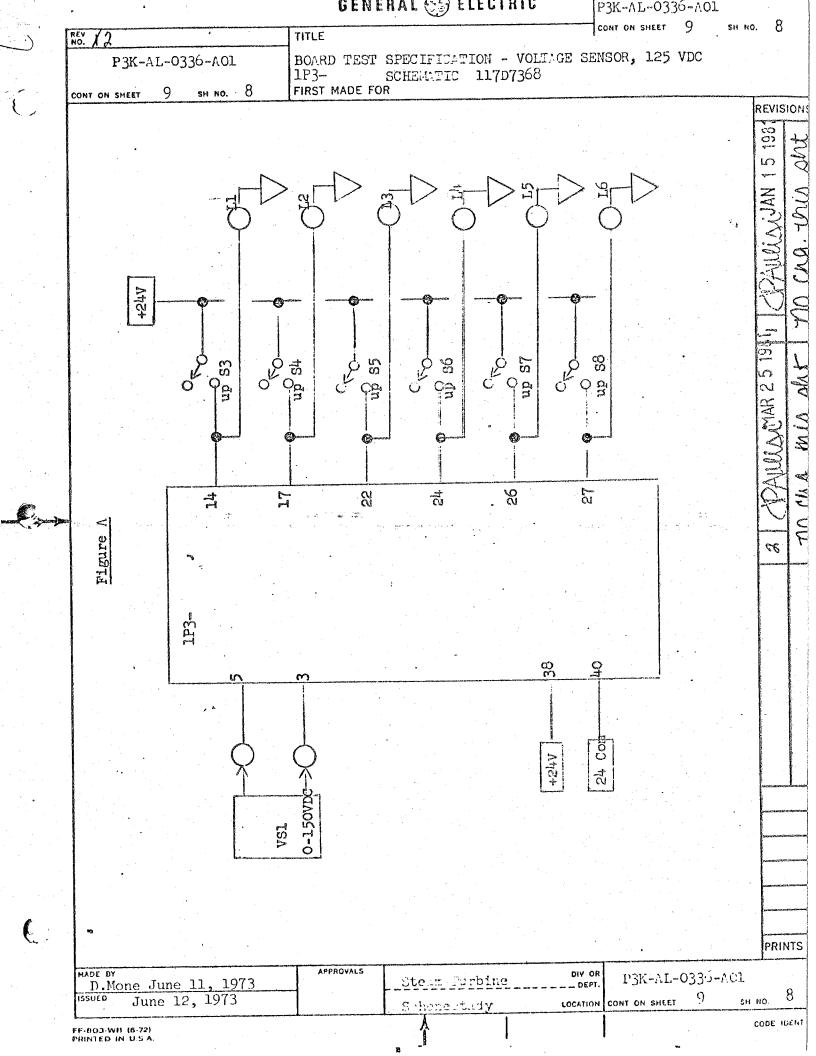
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