ASSEMBLY DRAWING

PC BOARD DRAWING

125D460AE

SCHEMATIC DRAWING

125D443AE

TEST KIT 165A663AE FIXTURE #54

1.0 IN	ISPECTION			,
•	l Identification	.3 Solder/Wire	.5 Key Slot	
•	2 Comp./Conn.	.4 Temp. Cycle	.6	Name of the second second second second
			.7	#=\-{
REMARKS:				
		e e e e e e e e e e e e e e e e e e e	Market Statement	

2.0 BOARD SET-UP

JUMPER ADD; R532 (5.23K), IC505 (4702 F/V CONVERTER).

3.0 TEST SET-UP

- 3.1 TURN POWER SWITCH OFF.
- CONNECT +15 VDC, -15 VDC, -12 VDC, +5 VDC, +12 VDC AND COMMON TO TEST ્3.2
- 3.3 CONNECT +5 VDC SQUARE WAVE GENERATOR FROM SGI(+) TO COM. SET TO 0 HZ.
- SET S1, S2, S3, AND S4 OFF (LEFT POSITION). 3.4

BOARD TEST 4.0

PLUG BOARD INTO AE POSITION. TURN POWER SWITCH ON.

STEP

- 4.1 READ +15 VDC CURRENT, 45 MA. MAX.
- 4.2 READ -15 VDC CURRENT, 40 MA. MAX.
- 4.3 READ -12 VDC CURRENT, 10 MA. MAX.
- 4.4
- READ +5 VDC CURRENT, 200 MA. MAX.

 CONNECT SCOPE FROM . 7 TP501(+) TO COMMON. VI MUST BE 7.0 VDC MAX. 4.5 SCOPE SETTINGS; DC MÓDE - 1 V/DIV - 0.1 MSEC/SC SWEEP

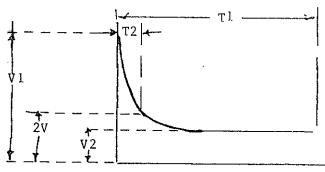


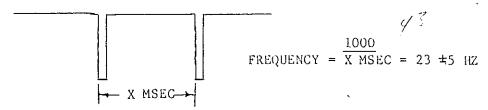
FIGURE 1

0 VDC.

- 4.6 V2 MUST BE .5 VDC MAX.
- 4.7 T2 MUST BE .05 MSEC MIN.

4.45 SET SG1 TO 5620 ±5 HZ.

4.8 CONNECT SCOPE FROM 6 TP511 TO COMMON. SETTINGS; DC MODE - 1 V/DEC 10 MSEC.CM SWEEP.



FREQUENCY MUST BE 23 ±5 HZ OR 35.7 TO 55.5 MILLISEC BETWEEN PULSES.

	rkequenct host be 23 +3 hz ok 33.7 to 33.9 https://doc.org/when toeses.	
4.9	SET SGI TO .5 ±.1 HZ.	
4.10	KI MUST BE ON.	
4.11	SET SGI TO 3 ±.1 HZ.	
4.12	KI MUST BE OFF.	
4.13	SET < 2 P501, < 3 P502, 4 P504 FULL CCW. SET S2 ON. SET SGI TO OHZ.	
4.14	CONNECT DVM TO (11) TP509. MUST BE 0.00 ±.010 VDC.	
4.15	SET SG1 TO 2000 ±2 HZ.	
4.16	TP509 MUST BE +1.98 ±.030 VDC.	
4.17	SET SGI TO 6000 ±6 HZ.	
4.18	TP509 MUST BE +5.95 ±.07 VDC.	
4.19	SET SGI TO 10,000 \pm 10 HZ.	
4.20	TP509 MUST BE +9.90 ±.11 VDC.	,
4.21	SET SG1 TO 10,000 ±10 HZ.	
- 4.22	SET SI ON. CONNECT O TO 1 MA METER TO A5 TEST JACKS. SET 3 P502	
	FOR +1.0 ±.01 MA.	
4.23	SET SQ1 TO 6000 ±6 NZ.	
4.24	SET <3> P502 FOR +1.00 ±.01 MA AT A5 TEST JACKS.	
4.25	SET SĞ1 TO 8000 ±8 HZ.	
4.26	SET <3 P502 FOR +1.00 ±.01 MA AT A5 TEST JACKS.	
4.27	SET SÍ OFF. SET SG1 TO 10,000 ±10 HZ.	
4.28	SET S3 ON. CONNECT O TO I MA METER TO A6 TEST JACKS. SET 2 P501 FOR	
	+1.00 ±.01 MA.	
4.29	SET SQ1 TO 6000 ±6 HZ.	
4.30	SET <2 P501 FOR +1.00 ±.01 MA AT A6.	
4.31	SET SG1 TO 8000 ±8 HZ.	
4.32	SET $\langle 2 \rangle$ P501 FOR 1.00 ±.01 MA AT A6.	
4.33	SET S3 OFF. SET SG1 TO 10,000 ±10 HZ.	
4.34	SET S4 ON. CONNECT 0- TO I MA METER TO A7 TEST JACKS. SET 4 P504 TO	
	1.00 ±.01 MA.	
4.35	SET SCI TO 6000 ±6 HZ.	
4.36	SET (4) P504 TO 1.00 ±.01 MA. AT A7.	
4.37	SET SCI TO 8000 ±8 HZ.	
4.38	SET 4 P504 TO 1.00 ±.01 MA. AT A7.	
4.39	SET SA OFF. SET SGI TO 4200 ±4 HZ.	A-110
4.40	SET (I) P503 UNTIL KI TURNS OFF. CW ON.	
4.41	SET SQ1 TO 6825 ±7 HZ.	
4.42	SET (I) P503 UNTIL KI TURNS ON, THEN JUST OFF.	
4.43	SET SGI TO 5670 ±5 HZ.	
4.44	SET (1) P503 UNTIL KI TURNS ON, THEN JUST OFF.	

ΑE	FITCHBURG	TEST	INSTRUCTIONS
1.25D460AE			

SHEET 3 OF 3

4.46 KI MUST BE ON.

4.47 CONNECT SCOPE TO <8 TP503, NOISE MUST BE LESS THAN 50 MV.
4.48 CONNECT SCOPE TO ANODE OF CR503, NOISE MUST BE LESS THAN 50 MV.
4.49 CONNECT SCOPE TO ANODE OF CR508, NOISE MUST BE LESS THAN 50 MV.

TURN POWER OFF.