

REV. NO. <u>01</u>	TITLE	CONT ON SHEET 2	SH NO. 1
P3K-AL-0319-A01	TEST INSTRUCTIONS FOR PU TEST ANALOG/LOGIC CIRCUIT BOARD		
CONT ON SHEET 2	SH NO. 1	FIRST MADE FOR EHC MARK II	

I. SCOPE

CAND TEST FILE

This instruction outlines the test specifications for circuit board 1PU3-B001 (Board Drawing 117D7797 G1).

II. CIRCUIT DESCRIPTION

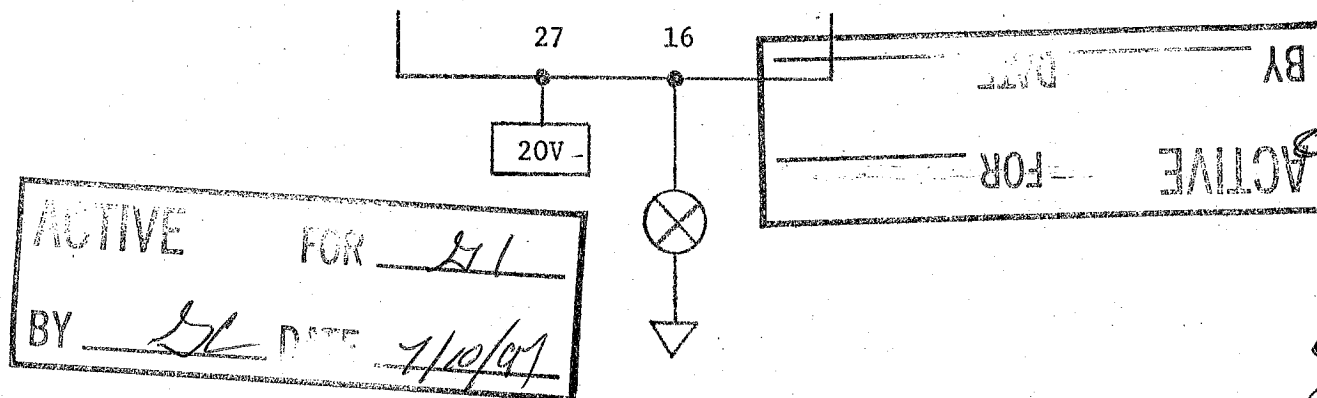
This circuit board is used to consolidate all of the analog to logic relays that provide switching signals to the power load unbalance circuit and the early valve actuation circuit. The board contains three 24V mercury-wetted relays.

III. CIRCUIT SPECIFICATIONS

Connect permanently Pin 40 to ground and Pin 38 to +24V power supply. A +24V voltage should appear between TP1 and TP2. The board should not draw current through Pin 38 at any time. The logic and conduit continuity should be checked according to the following table, with the board in a vertical position.

Input Pins		Output Pin Pairs			
22	24	23-17	27-16	27-15	27-14
OV	OV	Open Circuit	Open c.	Open c.	Open c.
24V	OV	Closed Circuit	Open c.	Open c.	Open c.
OV	24V	Open Circuit	Closed c.	Closed c.	Closed c.
24V	24V	Closed Circuit	Closed c.	Closed c.	Closed c.

When the above pin pairs form closed circuits, they should be able to carry about 20 ma without appreciable voltage drop. Suggested test set up: (Example for pair 27-16)



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1 0.7 MAR 2 1973
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TEST INSTRUCTIONS

1PU3-B001

Assembly
Schematic

117D7797
117D7796

PROCEDURE:

- A. Refer to P3K-AL-0494-A02 for test circuits, equipment list, and set up instructions.
- B. Table A provides the step by step procedure to follow in testing this circuit.
- C. Table B provides a step by step description of input voltages (to use as a troubleshooting aid).

REVISIONS

1107 Mar. JUN 21 1976
R. P. R. R. R.

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TEST INSTRUCTION FOR PU TEST A/L

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FIRST MADE FOR MK II

ACTION

VOLTAGE AT PINS

REVISIONS

STEP 1 INIT. COND.

14

15

16

17

STEP 1 INIT. COND.

STEP 2 APP P38

STEP 3 APP P27

STEP 4 APP P23

STEP 5 APP P24

A

A

A

STEP 6 REM P24

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STEP 7 APP P22

A

STEP 8 REM P22

Ø

STEP 9 REM P23

STEP 10 REM P27

STEP 11 REM P38

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SH NO. 3

CODE IDENT NO.

REV. NO. 1

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P3K-AL-0319-A01

TEST INSTRUCTIONS FOR PU TEST ANALOG/LOGIC CIRCUIT BOARD

CONT ON SHEET ---

SH NO. 3

FIRST MADE FOR EHC MARK II

REVISIONS

PREPARED BY:

D. Economou

DATE:

2/5/1973

D. Economou

EHC DESIGN ENGINEERING

APPROVED BY:

P.C. Callan

DATE:

3-8-73

P.C. Callan - MANAGER

EHC DESIGN ENGINEERING

TEST PROCEDURE

PREPARED BY:

C.K. Bugg

DATE:

2/9/73

C. Bugg

EHC DESIGN ENGINEER

Reviewed By:

D. Economou

DATE:

2/23/73

D. Economou

EHC DESIGN ENGINEERING

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MADE BY

D.Mone Mar. 20, 1973

APPROVALS

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MAR 20 1973

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