

REV NO. 0	TITLE	CONT ON SHEET 2	SH NO 1
P3K-AL-0022	CV PREAMP. BD. CIRCUIT BOARD TEST		
CONT ON SHEET 2	FIRST MADE FOR PL 994D153 (Schem. 994D189)		

I) GENERAL DESCRIPTION

This board provides the closure of the V.C. position loop. This board has an input E_{SL} , valve stem lift demand signal, from the DFG BD. and an input E_F , valve position feedback, from the SADI board.

The forward loop contains:

- 1) OP-AMP as a preamplifier.
R36 - loop gain adjustment.
- 2) Hard limit on opening rate.
R37 - opening rate adjustment.
- 3) Valve test closing bias.
R38 - closing (test) rate adjustment.

The feedback loop contains:

- 1) OP-AMP to prevent loading the feedback signal from the SADI.
R46 provides 0-5V range for E_F .
- 2) BIAS ADJUSTMENT R40 to overcome linear transducer offset and 5% mechanical bias in the servovalve.
- 3) ADJ. R42 to provide a unity feedback loop.

DATA SHEETS

A valve position unit data sheet is prepared for each turbine. The data sheet provides numerical values, for a specific turbine, to be used with this turbine. (See Fig. 1)

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ISSUED (Oct. 10 1969)		Schenectady, N. Y.	LOCATION	CONT ON SHEET 2 SH NO. 1

P3K-AL-0022

CV PREAMP. BD. CIRCUIT BOARD TEST

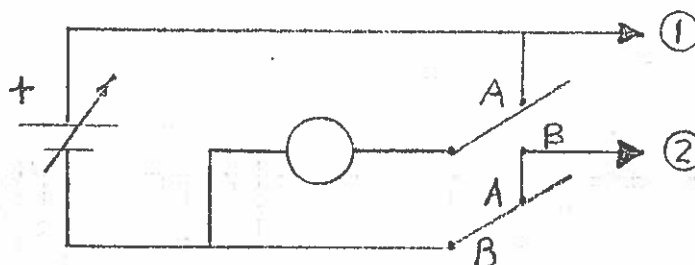
CONT ON SHEET 3

SH NO. 2

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II) INITIAL ADJ. - OP-AMP FDB. & LOOP FDB. (PERFORM WITH B10, R9, and R14 OPEN)

A) Test Set Up



Note: Make sure DVM reads zero when input terminals are shorted. If not, adjust DVM

During this adjustment the circuit board has to be disconnected. Note that the PLUS-side of the variable power supply is connected to the 1 terminal.

B) OP-AMP FDB

- 1) Set SW to POS. A.
- 2) Connect terminal 1 with TP2 (RD).
- 3) Connect terminal 2 with B4.
- 4) Adjust variable voltage source until DVM reads V_4 .
- 5) Set SW to POS. B.
- 6) Adjust R36 until the ammeter reads current I_4 .

C) Loop FDB

- 1) Set SW to POS. A.
- 2) Connect terminal 1 with TP4.
- 3) Connect terminal 2 with B4.
- 4) Adjust variable voltage source until DVM reads V_5 .
- 5) Set SW to POS. B.
- 6) Adjust R42 until the ammeter reads current I_5 .

Note: It may be necessary to open circuit one end of R9 to obtain correct resistance.

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CONT ON SHEET 3

SH NO. 2

REV NO. 0

TITLE

CONT ON SHEET 4 SH NO. 3

P3K-AL-0022

CV PREAMP. BD. CIRCUIT BOARD TEST

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III) FINAL TEST

- A) Connect -22V to pin 21.
Connect +30V to pin 17.
Connect signal ground pin 19.
Insert B10, R9, and R14.

Do not connect Figure 2 until step E15

B) FDB. Loop OP-AMP

- 1) Set R46 at 11 turns from either end.
- 2) Measure E_F at TP4 with DVM.
- 3) Adjust R47 to bring E_F as close to zero as possible.

C) Adj. of Opening Rate Limit

- 1) Turn R40 fully CCW.
- 2) Apply +5V on pin 36.
- 3) Adjust R37 until the voltage E_{SA} (TP2) reads $V_{OP. LIMIT}$.

D) Adj. of Vlv. Test Closing Bias

- 1) Connect pin 5 with pin 7.
- 2) Remove +5V from pin 36.
- 3) Adjust R38 until the voltage E_{SA} (TP2) reads $V_{CL LIMIT}$.
- 4) Remove lead which connects pins 5 and 7.

E) Gain and Bias Adj.

- 1) Turn pot 9 fully CCW (Fig. 2).
- 2) Apply $E_F = -5V$ at pin 39.
- 3) Adjust R46 to give $E_F = -5V$ at TP4.
- 4) Turn R40 fully CCW.
- 5) Apply 50K in parallel with R5 + R36.
- 6) Apply +5V at pin 36.
- 7) E_{SA} (TP2) should be 0.0V. If not, adjust R42 until E_{SA} is 0.0V.
- 8) Apply +5V at pin 39.
- 9) E_F (TP4) should read +5V. If not, adjust R46 until $E_F = +5V$.
- 10) Connect pin 36 to signal ground, pin 19.
- 11) Change resistance in parallel with R5 + R36 to 5K.
- 12) Adjust R40 for $E_{SA} = -0.25$ at TP2.
- 13) Check OP. AMP. FDB. resistor setting: *Returning* ~~Leaving~~ all other voltage and resistor settings as they are at end of step (9); apply V_6 volts at pin 36. E_{SA} (TP2) should read V_7 volts. If not, adjust R36 until $E_{SA} = V_7$, within $\pm 25\%$.

(Note: The V value given must be less than $V_{OP LIMIT}$ to be correct.)

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CONT ON SHEET 4

SH NO. 3

REVIS

R

PRINTS

273-7

273-2

273-1

273-1

273-1

273-1

273-1

273-1

273-1

273-1

REV NO. 0

TITLE

CONT ON SHEET 5

SH NO. 4

P3K-AL-0022

CV PREAMP. BD. CIRCUIT BOARD TEST

CONT ON SHEET 5

SH NO. 4

FIRST MADE FOR PL 994D153 (Schem. 994D189)

REVIS

III) FINAL TEST (Continued)

E) Gain and Bias Adj. (Continued)

OPEN

- 14) Use power supply to vary E_{SL} (pin 36).
- 15) Switch S17 to Pos. B. ~~Close~~ S10.
- 16) Apply $E_{SL} = 1.0V$ and E_F (TP4) should go to 4.0V.
- 17) Apply $E_{SL} = 2.0V$ and E_F should go to 3.0V.
- 18) Apply $E_{SL} = 3.0V$ and E_F should go to 2.0V.
- 19) Apply $E_{SL} = 4.0V$ and E_F should go to 1.0V.
- 20) If steps 16 through 19 do not check, use pot 9 to set $E_F = 5V$ when E_{SL} is 0V and then repeat steps 16 through 19.

Put red glyptol on R42 and R36.

f-1
ET-2
273-
273-
273-
273-
273-
R
PRINT

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SH NO. 4

REV NO. 0

CONT ON SHEET 6 SH NO.

P3K-AL-0022

TITLE

VPU SETTING DATA

3 SLOPE DFG.

FIRST MADE FOR

TURBINE:

VALVE:

TEST INSTR: P24B-AL-4945, P24B-AL-
P24B-AL-4948

CONT ON SHEET 6 SH NO. 5

OP. AMP.

FEEDBACK:

$V_4 =$

volts

$I_4 =$

uamp

$R_f = V_4 / I_4 =$

KOHM

$V_6 =$

volts

$V_7 = \frac{R_f}{R_1} V_6 =$

RATE LIMITS:

$V_{OPLIMIT} =$

volt

$V_{CLLIMIT} =$

volt

LVDT SIZE =

in

FIGURE 1

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SH NO. 5

REV NO. 0

0

TITLE

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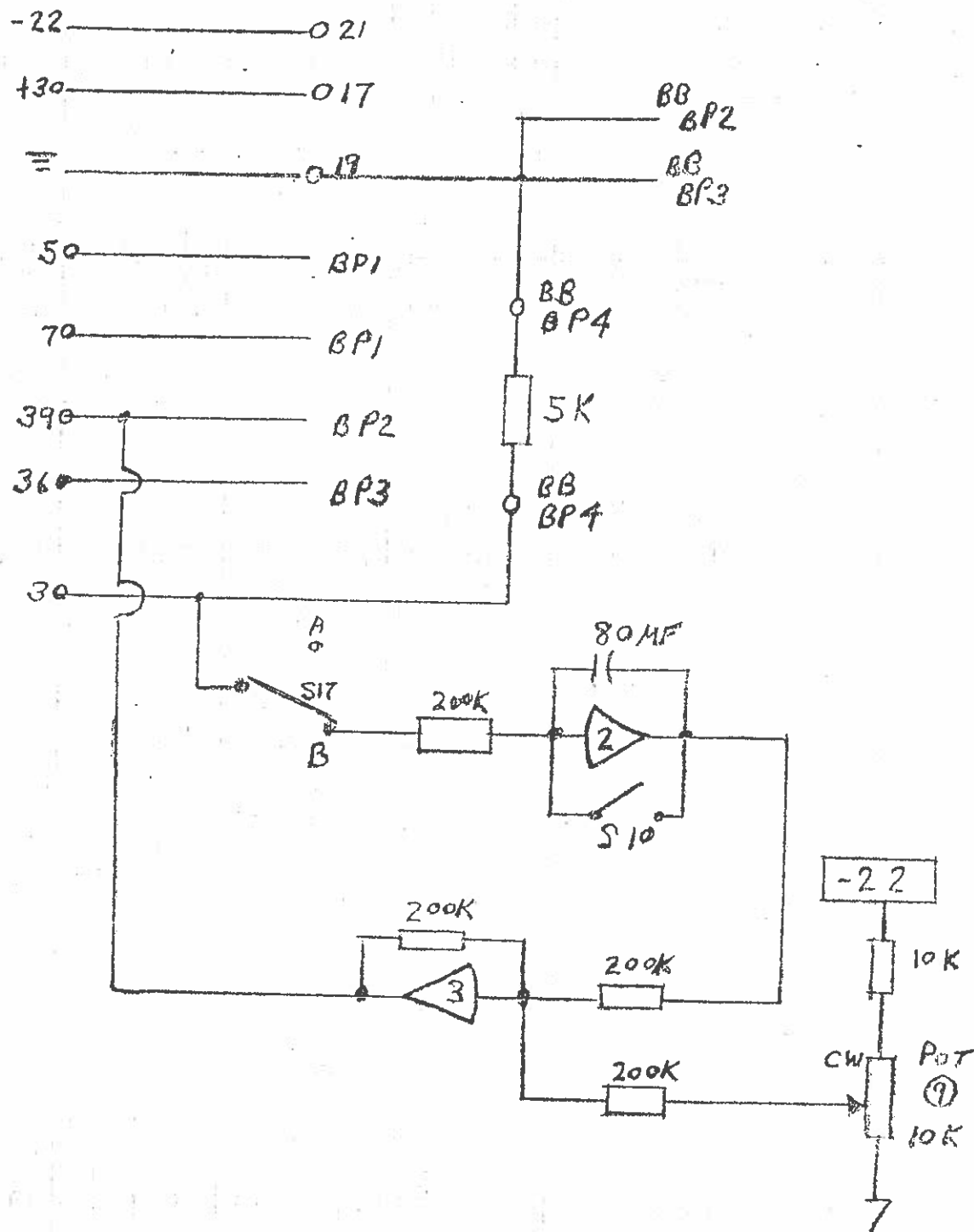
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SH NO. 6

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TEST SCHEMATIC
FIGURE 2

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SH NO. 6

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TITLE

CONT ON SHEET --

SH NO. 7

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CV PREAMP. BD. CIRCUIT BOARD TEST

CONT ON SHEET --

SH NO. 7

FIRST MADE FOR PL 994D153 (Schem. 994D189)

REVIS

PREPARED BY:

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Turbine Control Design Engineering

DATE

10/6/69

APPROVED BY:

P. C. Callan
P. C. Callan, Manager
Turbine Control Design Engineering

DATE

10/7/69

REVIEWED BY:

R. J. Dellorfano
EHC Test Engineer

DATE

FL

F2

ET-2

273-7

273-2

273-1

273-1

273-1

R

PRINTS

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CONT ON SHEET --

SH NO 7

1270

Data Sheet

Job # _____								
Serial # _____					Burn-in Start _____			
Date _____								
Data Sheet for _____ 994D153G0008/G0013/G0019 _____					Burn-in Stop _____			
Test Procedure _____ P3K-AL-0022 _____					Technician _____			
Test Procedure Step	Nominal	Lower Limit	Pre-Burn in Results	Post Burn in Results	Upper Limit	Pot Values If applicable CW CCW		Pass/Fail
C-3 - R37 - TP2	-3.357V	Vop limit						
D-3 - R38 - TP2	+2.857V	Vcl limit						
E7 - TP2	0.0V	0.0V						
E9 - TP4	+5.0V	+5.0V			+5.0V			
E13 - TP2	+3.0V	+2.25V			+3.75V			
E16 - TP4	4.0V	+3.91V			+4.09V			
E17 - TP4	3.0V	+2.91V			+3.09V			
E18 - TP4	2.0V	+1.91V			+2.09V			
E19 - TP4	1.0V	+0.91V			+1.09V			
Brunswick Test								
3 - R47 @ Pin-18								
4	0.0V							
5 - R36								
6-7 - R46								
8-9-10 - R37	-3.357V							
11 - R42 @ TP2								
13 -R40 @ TP2								
15-16-17 - R38	+2.857V							
20	+5.00V							
22	+2.50V							
24	0.00V							