g GE Industrial Systems	**Functional Testing Specification**
Renewal Services ***Louisville,KY***	**LOU–GED-IC3600TSKA**

Test Procedure for an IC3600TSKA Startup Control and Suicide Card

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Frank Howard			Robert Dwall
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Functional test procedure for a Startup Control and Suicide Card

1. SCOPE

1.1 This is a functional testing procedure for an IC3600TSKA

2. STANDARDS OF QUALITY

2.1 Refer to the current revision of the IPC-A-610 standard for workmanship standards.

3. APPLICABLE DOCUMENTS

3.1 The following document(s) shall form part of this specification to the extent specified herein. Unless otherwise indicated, the latest issue shall apply.
3.1.1

4. ENGINEERING REQUIREMENTS

4.1 Equipment Cleaning
- **4.1.1** Equipment should be clean and free of debris prior to applying power unless performing an initial check. Refer to the local documented procedures for cleaning guidelines.

4.2 Equipment Inspection
- **4.2.1** Equipment should be visually inspected for any defects prior to applying power. This inspection should include the following as a minimum:
  - **4.2.1.1** Wires broken or cracked
  - **4.2.1.2** Terminal strips / connectors broken or cracked
  - **4.2.1.3** Loose wires
  - **4.2.1.4** Components visually damaged
  - **4.2.1.5** Capacitors leaking
  - **4.2.1.6** Solder joints damaged or cold
  - **4.2.1.7** Circuit board burned or de-laminated
  - **4.2.1.8** Printed wire runs burned or damaged

5. EQUIPMENT REQUIRED

5.1 The following equipment is required to perform the process requirements. Equipment may be substituted provided that all accuracy's and test ratios are equivalent or better.

Qty	Reference #	Description
1		Fluke 85 DMM (or Equivalent)
2		0-+5VDC Power Supplies
1		+67DC Power Supply
1		Rainbow Box
1		IC3600 Interface Box

6. TESTING PROCESS

6.1 Setup

6.1.1 Connect +67VDC between pins 24(+) and pins 1 or 51(-). *Do not turn on power at this time.*

6.1.2 Connect pins 45, 46 and 48 through a 500 ohm, .5W resistor (a resistor for each pin) to +5VDC and common. (Pin 1 or 51) *Do not turn on power at this time.*

6.2 Testing Procedure

6.2.1 Monitor pin 45 with DVM, turn on +5VDC power and verify +5VDC on DVM.

6.2.2 Turn on +67VDC power and DVM should fall to less than +1VDC then quickly rise back to +5VDC level. Remove all power supplies and allow card to discharge to less than 1mVDC.

6.2.3 Monitor pin 46, repeat step 6.2.2 and you should get same results.

6.2.4 Monitor pin 48, repeat step 6.2.2 and you should get same results. Remove +5VDC but leave +67VDC on card.

6.2.5 Verify 10K ohms (+/- 10%) between pins 20 and 6.

6.2.6 Verify 0 ohms between pins 20 and 8.

6.2.7 Apply 3VDC to pin 25 using pin 1 or 51 as common.

6.2.8 Verify an open between pins 20 and 6.

6.2.9 Verify an open between pins 20 and 8.

6.3 ***TEST COMPLETE ***

7. NOTES