

REV NO.

2 7 7 A 3 6 6 6

CONT ON SHEET 2 SH NO. 1

TITLE

TEST SPECIFICATIONS

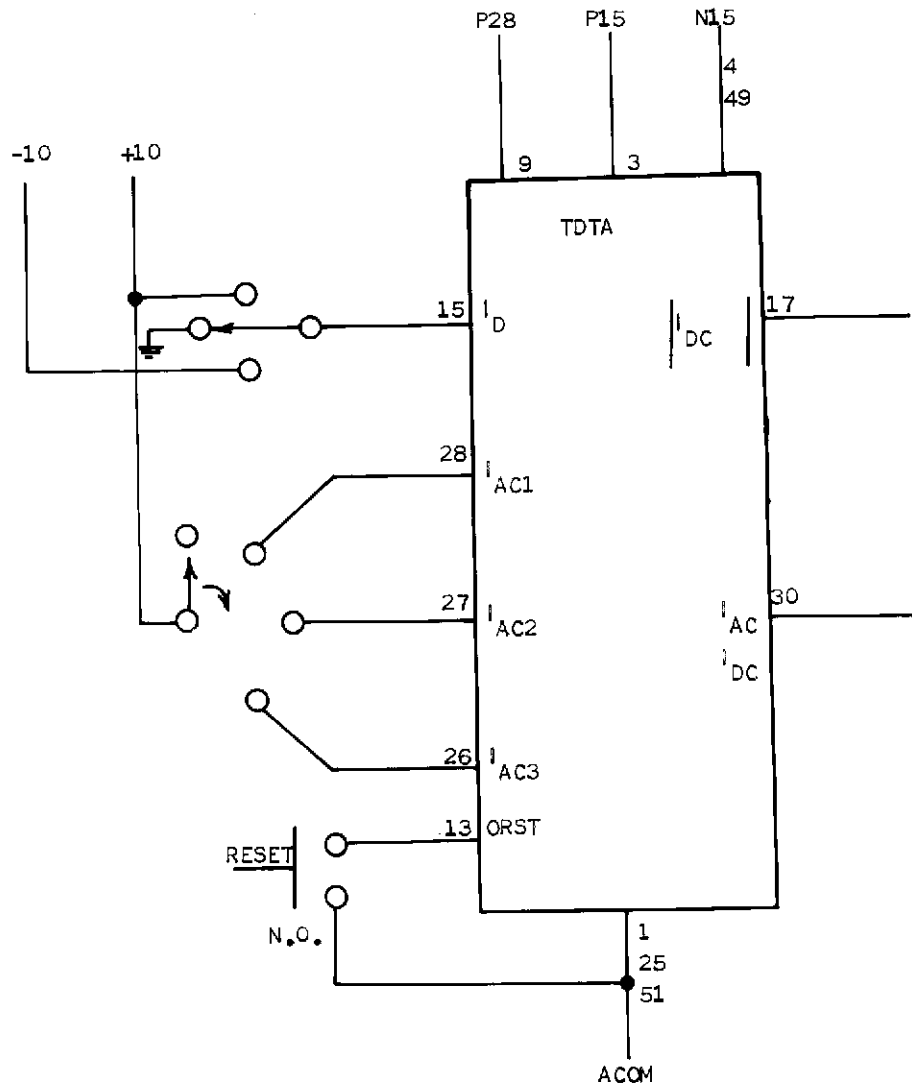
FIRST MADE FOR IC3600TDTA1

REVISIONS

INSPECTION

INSPECT THE TDTA FOR OBVIOUS ASSEMBLY ERRORS, MISSING COMPONENTS, AND PHYSICAL DAMAGE.

GAIN TEST CONFIGURATION



MADE BY

A. J. DELEO

ISSUED

6/13/76

APPROVALS

[Signature]

DRIVE SYSTEMS

SALEM, VA. U.S.A.

DIV OR DEPT.

LOCATION

2 7 7 A 3 6 6 6

CONT ON SHEET 2

SH NO. 1

CODE IDENT N

REV NO.
2 7 7 A 3 6 6 6
CONT ON SHEET 3 SH NO. 2

TITLE
TEST SPECIFICATIONS
FIRST MADE FOR IC3600TDTA1

REVISIONS

IC1 DC GAIN CHECK

A POSITIVE OR NEGATIVE SIGNAL AT INPUT PIN 15, WILL PRODUCE A POSITIVE OUTPUT AT PIN 17 WITH A RESULTANT GAIN OF 1/3. WITH R90 CW, A NEGATIVE SIGNAL IS PRODUCED AT PIN 30.

PINS 26, 27, 20 0 VOLTS R90 CW

V _{IN} (PIN 15)	V _{OUT} (PIN 17)	V _{OUT} (PIN 30)
5V ± .01V	1.63V TO 1.8V	-2.50V TO -3.1V
-5V ± .01V	1.63V TO 1.8V	-2.50V TO -3.1

TABLE 1

IC2 DC GAIN CHECK

WITH R90 FULL CCW, A POSITIVE VOLTAGE AT INPUT PINS 28 AND 27 WILL PRODUCE A POSITIVE OUTPUT AT PIN 30 WITH A RESULTANT GAIN OF 1. A POSITIVE VOLTAGE AT INPUT PIN 26 WILL PRODUCE A POSITIVE OUTPUT AT PIN 30 WITH A RESULTANT GAIN OF 0.55. EXERCISE IC2 WITH THE FOLLOWING TABLE 2 AND INSURE ITS PROPER OPERATION.

TABLE 2

PIN 15 0 VOLTS R90 CCW

INPUT	OUTPUT PIN 30
(PIN 28) +5V ± .01V	+7.36V TO +9.0V
(PIN 27) +5V ± .01V	+7.36V TO +9.0V
(PIN 26) +5V ± .01V	+4.03V TO +5.0V

1) 10/7/76 JVG

DL32
BZLVN

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REV NO.

TITLE

2 7 7 A 3 6 6 6

TEST SPECIFICATIONS

CONT ON SHEET 4 SH NO. 3

FIRST MADE FOR IC3600TDTA1

REVISIONS

STATIC L.E.D. AND LOGIC TESTS

THE R90 BALANCE POT CAN BE USED TO TEST THE AC > DC AND DC > AC INDICATORS..

1. APPLY +10 VOLTS TO PINS 15 AND 26.
2. NULL PIN 30 VOLTAGE TO 0 VOLTS BY ADJUSTING POT R90.
3. RESET INDICATOR CIRCUITS BY MOMENTARILY SHORTING PIN 13 ORST TO ACOM.
4. VERIFY THAT BOTH LED INDICATORS ARE OFF. ALSO VERIFY THE FOLLOWING TEST POINTS:
PIN 21, 13.5V \pm 1V; PIN 12, 1V \pm 1V; P31, 13.5V \pm 1V; PIN 50, 1V \pm 1V;
PIN 42, 1V \pm 1V.
5. ADJUST R90 TO OBTAIN +1.0 VOLT \pm .01V AT PIN 30.
6. FIND AC FAULT LIGHT ON. ALSO VERIFY PIN 21, -14V \pm 1V; PIN 12, 14V \pm 1V;
PIN 42, 14V \pm 1V; PIN 31, 13.5V \pm 1V.
7. REPEAT STEPS 2, 3, AND 4.
8. ADJUST R90 FOR -1V \pm .01V AT PIN 30.
9. FIND DC FAULT LIGHT ON. ALSO VERIFY PIN 31, -14V \pm 1V; PIN 50, 14V \pm 1V;
PIN 22, ADJUST R91 TO OBTAIN +5V \pm 1V; PIN 21, 13.5V \pm 1V.
10. REPEAT STEP 2. (THIS FIXES POSITION OF R90 FOR THE DYNAMIC TESTS).

4QA3

1RA2

4EF1

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6-12-76

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CONT ON SHEET 4

SH NO. 3

CODE IDENT NO.

REV NO.

TITLE

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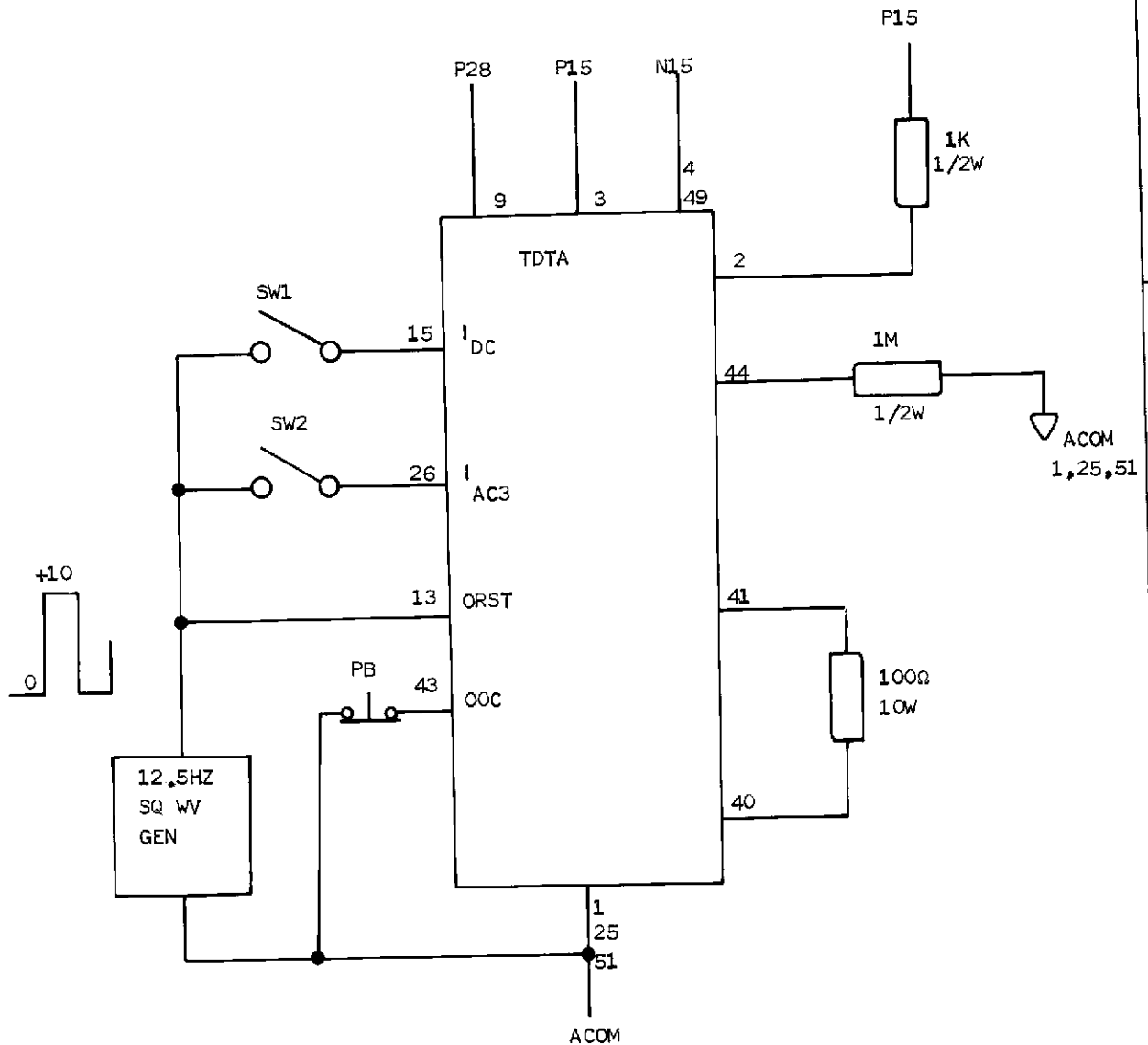
TEST SPECIFICATIONS

CONT ON SHEET 5 SH NO. 4

FIRST MADE FOR IC3600TDTA1

REVISIONS

DYNAMIC TESTING CONFIGURATION



PB SHOULD BE SPRING RETURN TO CLOSED POSITION. PROLONGED OPERATION WITH PB OPEN CAN CAUSE CARD HEATING.

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CONT ON SHEET 5

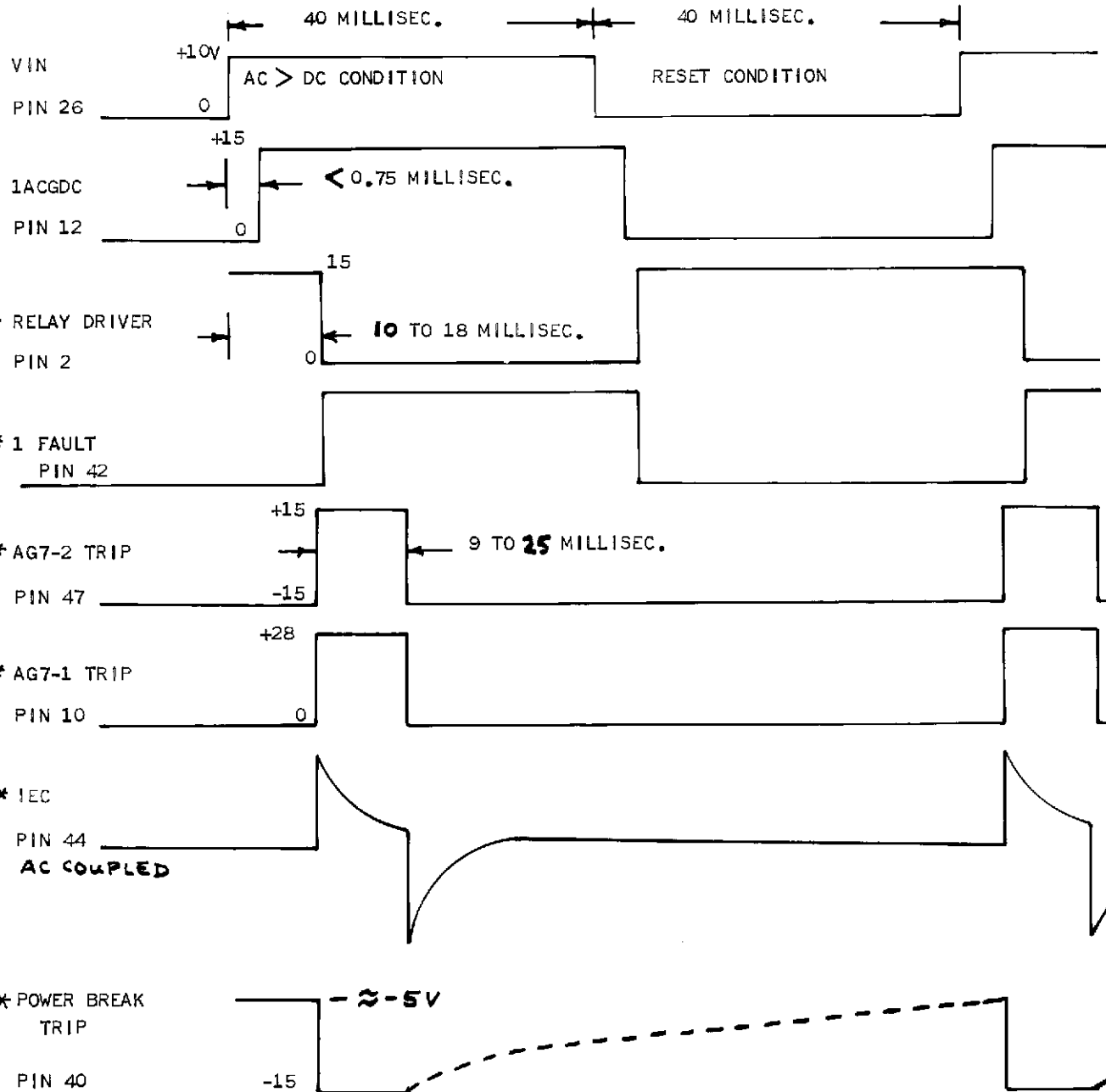
SH NO. 4

CODE IDENT

REV NO.	TITLE
2 7 7 A 3 6 6 C	TEST SPECIFICATIONS
CONT ON SHEET 6	SH NO. 5
FIRST MADE FOR IC3600DTA1	

AC > DC DYNAMIC TEST

1. CLOSE SWITCH SW2 TO PIN 26 AND OPEN SWITCH SW1.



* MOMENTARILY DEPRESS PUSHBUTTON TO OBSERVE THIS WAVESHAPES. USE 5 SEC. VIEWING PERIOD FOLLOWED BY 5 SEC. WITH PB RELEASED.

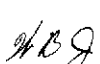
NOTE: SOME FLICKERING OF THE AC FAULT LIGHT MAY BE OBSERVED.

REVISIONS

REV	DESCRIPTION	DATE	BY	CHKD	APP'D	REASON
1	BU945JN JVG 3/2/77					
2	REV 12/2/98					

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				SH NO. 5

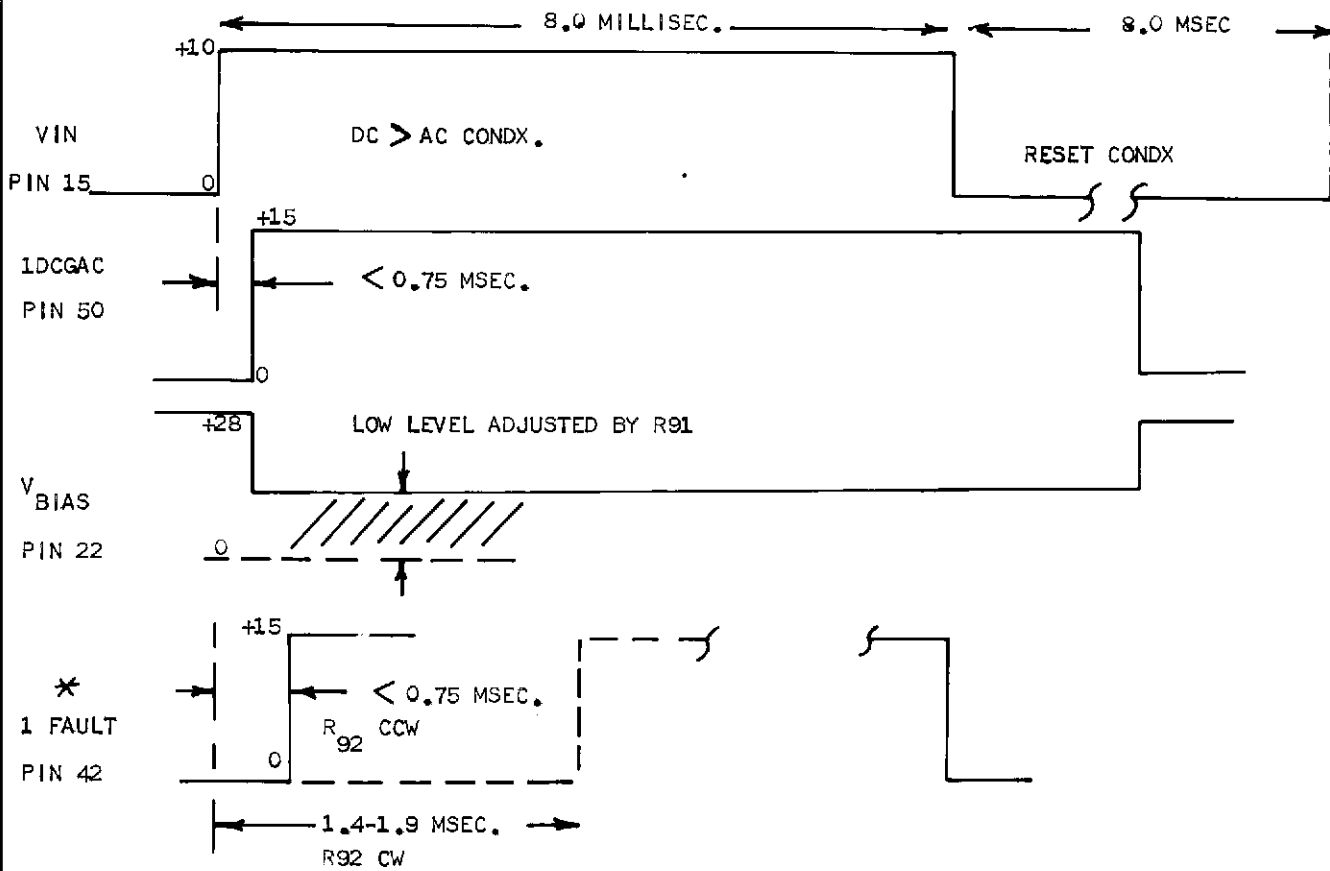
REV. NO. 2 7 7 A 3 6 6 6 CONT ON SHEET 7 SH NO. 6	TITLE TEST SPECIFICATIONS FIRST MADE FOR IC3600TDTA1
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DC > AC DYNAMIC TEST

1. CLOSE TEST SWITCH TO PIN 15 AND INSURE THAT SWITCH TO PIN 26 IS OPEN. CHANGE FREQUENCY TO .60 HZ.

REVISIONS

1) BU946JN JVG 3/2/77



* MOMENTARILY DEPRESS PB TO OBSERVE THIS WAVE SHAPE. USE 3 SEC. VIEWING PERIOD FOLLOWED BY 30 SEC. WITH PUSHBUTTON RELEASED.

NOTE: IN THE DC > AC CONDITION, FAULT TIME IS VARIABLE WITH R92 ADJUSTMENT. ALSO, DC > AC INDICATOR WILL LIGHT INTERMITTENTLY. AC > DC INDICATOR OFF.

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REV
NO.

TITLE

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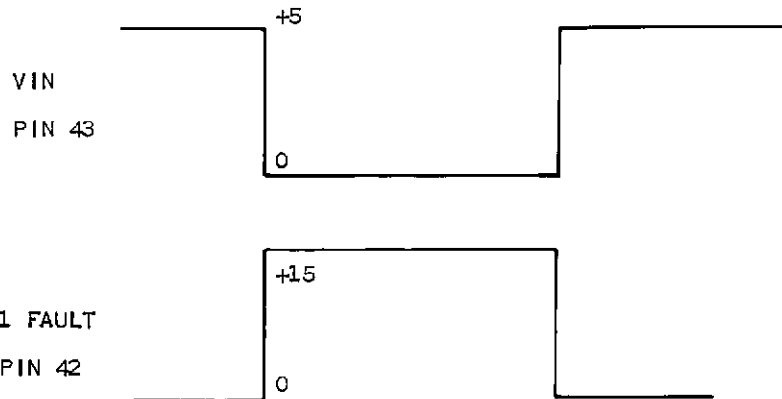
TEST SPECIFICATIONS

CONT ON SHEET FL. SH NO. 7

FIRST MADE FOR IC3600TDTA1

REVISIONS

A "1" FAULT SIGNAL (PIN 42) MAY BE INITIATED AT ANYTIME THAT PIN 43 IS LOW. ALL OTHER INPUTS AT 0 VOLTS.



P5 BUS CHECK (19)

MEASURE AND FIND BETWEEN 5.0V AND 6.8V.

4QA3

1RA2

4EF1

1338

B-2-L v2

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CONT ON SHEET FL

SH NO. 7

CODE IDENT NO.