GENERAL ELECTRIC

278A3070

REV NO. | TITLE: STANDING INSTRUCTIONS | CONT ON SHEET 2 SH NO. 1

CONT ON SHEET SH NO. | FIRST MADE FOR 1572K16G701

REVISIONS

STANDING INSTRUCTIONS

FOR

PRINTED CIRCUIT BOARD

1572K16G701

POSITION AMPLIFIER CHANNEL

FOR

T.S.I.

DL13

3EL1

Distribution Copies:

1 - QC Eng.
1 - QC Test
1 - Engineering

PRINTS TO

MADE BY	APPROVALS	DIV OR DEPT.	
K.A.Morris 790912	WLL	DRIVE SYSTEMS	278A3070
ISSUED 9-13-79		LOCATION SALEM, VA.	CONT ON SHEET 2 SH NO. 1

CODE IDENT NO.

FF-803 WF (11-77)
PRINTED IN U.S.A.

I. Test Equipment Required

A. Printed Circuit Board Test Stand - 44C931365

B. Adaptor - 9300K56

C. Cable - Power Supply

D. Patchboard - PB-4

E. Milliammeter - DC - 0 to 1/10 - Acc. 1%. Qty. 2

F. Drawings C3061K94 Elementary
Assembly
44C931365 Test Fixture

II. Wire Check

A. Resistance

			Resistance (Ohms)
Blue Test Jack	To	Pin E	0
Pin E	To	Pin H	1,998 to 2,002
Pin E	To	Pin C	1,998 to 2,002
Pin E (+)	To	Pin D (-)	Inf. (X 100 Scale)
Pin E (-)	To	Pin D (+)	50 to 100 (X 100 Scale)

B. Visual Check

C1901 1.0 mfd. 35 volt
C1902 1.0 mfd. 35 volt

III. Setup and Connections

A. Connect an AC/DC digital voltmeter to "BJ-1". Set for DC temporaril

B. Connect the DC milliammeter to "BJ-13". Red (+) and black (-). Set for 1 ma.

C. Turn all switches to OFF or Normal on the Universal Tester.

D. Turn all switches to OFF or Normal and all variacs to zero on the Universal Power Supply.

E. Connect adaptor 9300K56 to "PL-1" on the U.T.

F. Connect the Power Supply cable to the access plug on the U.P.S. and to PL-3 on the U.T. Connect leads to the DC power supplies.

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G. Set R1912 CCW R1934 CCW
R1914 CCW R1936 CCW

IV. Electrical Test

A. Close "SW-1" and depress "LPB-1". Increase PS-1 to 24 ± 1 volt DC at "BJ-1".

NOTE: Should it become necessary to remove all power from the PCB open "SW-1".

B. Depress "LPB-2" and increase PS-2 to 15 ± 0.01 VDC at "BJ-1". Red t jack (+) to black test jack (-) on the PCB shall read 15 ± 0.01 VDC. Readjust PS-2 if necessary.

C. Depress "LPB-3" and increase PS-3 to 15 ± 0.01 VDC at "BJ-1". Green test jack (+) to black test jack (-) shall read 15 ± 0.01 VDC. Read PS-3 if necessary.

D. Place "SW-18" and "SW-22" down.

E. Close "SW-2". Depress "LPB-6". Adjust PS4 for zero volts at "BJ-1"

R1914	VDC Blue Test Jack	R1934	VDC Brown Test Jack
CCW	0.92 ± 0.1	CCW	1 ~~0.6~~ ± ~~0.1~~ 0.2 ①
CW	4.00 ± 0.1	CW	3.6 ~~4.20~~ ± ~~0.1~~ 0.4
Set	2.00 ± 0.005	Set	2.0 ± ~~0.1~~ 0.005

F. Adjust PS-4 to -5 ± 0.05 VDC at "BJ-1".

R1912	VDC Blue Test Jack	R1936	VDC Brown Test Jack
CCW	9.0 ± 0.1	CCW	5.45 ± ~~0.1~~ 0.2
CW	13.7 ± 0.1	CW	7.65 ± ~~0.1~~ 0.2
Set	10.0 ± 0.005	Set	6.00 ± 0.005

G. Recheck Steps E and F until both ends of limits are met.

H. Place "RS-2" to position 6.

PS4 Volt "BJ-1"	MADC "BJ-13"	VDC Brown Test Jack
0	0.2 ± 0.02	2 ± 0.08
-2.5 ± 0.005	0.4 ± 0.02	4 ± 0.08
-5.0 ± 0.005	0.6 ± 0.02	6 ± 0.08

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I. Place "RS-2" to position 11.

PS4 Volt "BJ-1"	MADC "BJ-12"	VDC Blue Test Jack
0	1 ± 0.06	2 ± 0.08
-5 ± 0.005	5 ± 0.06	10 ± 0.08

J.

PS4 Volt "BJ-1"	MADC White Test Jack (+) to Green Test Jack (-)	VDC Blue Test Jack
0	1 ± 0.06	2 ± 0.08
-5 ± 0.005	5 ± 0.06	10 ± 0.08

K. Return "RS2" to zero. Return "PS-4"to zero.

"RS-1" position	"BJ-2"	VDC "PS4"Volt "BJ-1"
5	2 ± 0.08 VDC	0
11	8 ± 0.4 MVDC	0
11	40 ± 0.4 MVDC	-5 ± 0.005
5	10 ± 0.08 VDC	-5 ± 0.005

L. Open "SW-1". Return all switches to OFF or Normal on the U.T. Return all switches to OFF or Normal on the UPS. Return all variac to zero on the UPS. Return all DC power supplies to zero.

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PRINTS TO

REV NO. 278A3070

CONT ON SHEET 6 SH NO. 5

TITLE TEST SPECIFICATIONS

FIRST MADE FOR 1572K16G701

REVISIONS

1. Changed From SL 4 to SL6 Q.E.R 8-5-85

Addendum to TI# 278A3070. This portion is to be used when testing with the new CDO test console.

I. TEST EQUIPMENT

II. VISUAL CHECK

C1901 1.0uf 35V
C1902 1.0uf 35V
CR1910 Polarity

III. SET-UP

Connect +15VDC to Pin B
-15VDC to Pin U
Common to Pin N
Connect a power supply (0 to 5V) positive to pin N and negative to Pin J.

IV. ELECTRICAL

1. Apply ± 15VDC
2. Set power supply for 0 VDC on Pin E
   Adjust R1914 CCW 0.9 VDC or less on Pin E
   CW 4.0 VDC or more on Pin E
   Set 2.0 ± .005 VDC on Pin E

   Adjust R1934 CCW CCW 1.0 VDC or less @ Brown Jack
   CW 3.6 VDC or more @ Brown Jack
   SET 2.0 ± .005 VDC @ Brown Jack

   Set power supply for -5 + .05VDC at Pin J

   Adjust R1912 CCW 9.0 ± .5VDC @ Pin E
   CW 13.7 ± .5VDC @ Pin E
   SET 10.0 ± .005VDC @ Pin E

   Adjust R1936 CCW 5.45 ± .5VDC @ Brown Jack
   CW 7.65 ± .5VDC @ Brown Jack
   SET 6.00 ± .005VDC @ Brown Jack

Repeat steps 2-5 until limits for 0 and -5VDC input are met.

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PRINTS TO

MADE BY Gene Post

ISSUED 5/29/81

APPROVALS

DRIVE SYSTEMS DIV OR DEPT.

SALEM, VIRGINIA LOCATION

FF-803 WF (7-79-A-1)
PRINTED IN U.S.A.

REV NO.

2 7 8 A 3 0 7 0
CONT ON SHEET Fi. SH NO.

TITLE
TEST SPECIFICATIONS

FIRST MADE FOR 1572K16G701

REVISIONS

1. Changed from 5V5 to 5V6 DEK 5-5-81

Verify following chart:

Pwr Supply	Pin E VDC	Pin C VDC	Pin H VDC	Pin F MADC	Pin T(+) to V(-) MADC	Pin R(+) to P(-) MVDC
0	2.0±.01	2.0±.1	2.0±.1	.2±.02	1.0±.02	8 ± .4
-2.5±.01	6.0±.1	6.0±.1	6.0±.1	.4±.02	3.0±.02	24.± .4
-5.0±.01	10.0±.01	10.0±.1	10.0±.1	.6±.02	5.0±.02	40.± .4

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