

IC3600TRLE1

68A999193

GENERAL INFORMATION

38999

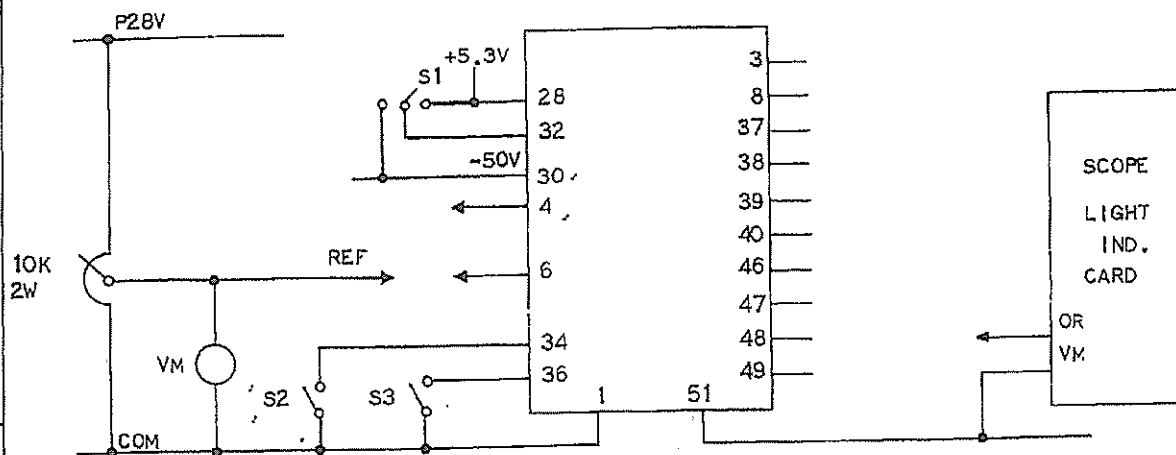
TITLE  
TEST SPECIFICATIONS

REV. LOGIC AND STATIC LOG

FIRST MADE FOR IC3600TRLE1 TRICK

POWER INPUT - P5.3V TO PIN 28  
COMMON TO PIN 1 AND 51  
N50V TO PIN 30  
P28V REF.

CONNECT CARD IN TEST CIRCUIT SHOWN BELOW.



1. TEST FOR CONFORMANCE WITH FOLLOWING TABLE

TOLERANCE ON ALL REF. VOLTAGES  $\pm 5\%$ 

ON SWITCH S1 - X = CLOSED TO PIN 28. O = CLOSED TO -50V CIRCUIT

X=CLOSED O=OPEN			REFERENCE INPUT		SET TRIMPOTS R1 AND R2 AS INDICATED BELOW	1 INDICATES VOLTAGE AT PIN $\approx 4.5V \pm 1V$ 0 INDICATES VOLTAGE AT PIN $\leq 1$ VOLT									
S1	S2	S3	VOLTS	TO PIN		3	8	37	38	39	40	46	47	48	49
O	X	X	0	4&6	FULLY CW	1	1	1	0	0	1	1	0	1	0
X	X	X	0	4&6	" "	1	1	0	1	1	0	1	0	1	0
X	X	X	.15	6	" "	1	1	1	0	0	1	1	0	1	0
O	X	X	.15	4	" "	1	1	0	1	1	0	1	0	1	0
O	X	X	.15	4&6	" "	1	1	1	0	1	0	0	1	1	0
O	X	X	18	4	ADJUST R1 TO OBTAIN 0 AT PIN 48	1	1	← O M I T →						0	1
O	X	X	18	6	ADJUST R2 FOR 0V AT PIN 48	1	1	← O M I T →						0	1
REMOVE REFERENCE VOLTAGE															
O	X	X	25	4&6	NO CHANGE	0	0	← O M I T →						0	1

1 WEM 10/28/72

46

13

24

PRII

MADE BY	APPROVAL	DATE	38999
DATE 10/19/72			
SALES	LOCATION	DATE OF SALE	
SEP 18 '78 16:51 GE DRIVES SALES			

TITLE

CONT. ON SHEET 371

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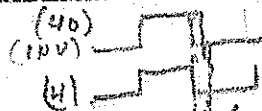
TEST SPECIFICATIONS

REV. LOGIC AND STATIC LOG

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FIRST MADE FOR IC3600TRLE1 TRK1



REVISI

7-11-82

6-1 ms LONGER

## 2. CHECK TIME DELAYS

A. WITH S1 TO NEG. REF AND S2 AND S3 CLOSED, APPLY 5V PEAK TO PEAK, 60 HZ SQUARE WAVE TO PIN 4 WITH RESPECT TO PIN 51. USE DUAL CHANNEL SCOPE TO MEASURE TIME DELAY BETWEEN TRAILING EDGE OF INPUT PULSE AND RISING EDGE OF OUTPUT PULSE AT PIN 40. PULSE AT PIN 40 MUST STAY NEGATIVE FOR 1MS  $\pm$  .3 MS. AFTER INPUT PULSE HAS GONE NEGATIVE.

B. CLOSE SWITCH S1 TO PIN 48 AND CONNECT WAVE TEK OUTPUT TO PIN 6. TIME DELAY BETWEEN FALLING PULSE AT PIN 6 AND RISING PULSE AT PIN 38 MUST BE 1 MS  $\pm$  .3 MS.

C. REMOVE WAVE TEK, SET DC REF. TO 20V AND MOMENTARILY APPLY REF. TO PIN 4. USE SCOPE TO DETERMINE THAT VOLTAGE AT PIN 48 REDUCES TO LESS THAN 1 VOLT WHEN REF. IS APPLIED AND STAYS LESS THAN 1 VOLT FOR AT LEAST .5 SEC., EVEN THOUGH REF. WAS ONLY MOMENTARILY APPLIED. REPEAT ABOVE TEST EXCEPT WITH INPUT REF. APPLIED TO PIN 6.

PIN 48 MOMENTARILY REDUCES

NOTE: TIME DELAY STARTS TIMING OUT AS SOON AS REF. IS APPLIED SO REF. MUST BE REMOVED IN LESS THAN .5 SEC.

NOTE: REMOVE  $\pm$ 67V POWER AND AC REFERENCER BEFORE INSERTING OR REMOVING CARD FROM TEST FIXTURE.

PR

1:

2:

DANIELS ACT

10/10/69

INDUSTRIAL

SALES

LOCATION

CONT. ON SHEET FL