

## **GE Power Generation Engineering**

PROCESS SPECIFICATION

P3K-AL-0360-A01

Materials and Processes Engineering Schenectady, NY 12345

TEST INSTRUCTIONS FOR PLANT COMMUNICATIONS LOGIC (MODE SELECTION)

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PREPARED BY:	P.R.	BUDKA
ORIG. ISSUE DATE:	-	-

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TITLE

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SH NO.

P3K-AL-0360-A01

SH NO. 2

TEST INSTRUCTIONS FOR PLANT COMMUNICATIONS LOGIC (MODE SELECTION)

FIRST MADE FOR

EHC MARK II (PLANT COMMUNICATIONS)

REVISIONS

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#### SCOPE

This instruction outlines the specifications for testing the Plant Communications Logic board 1PC2-BOO1.

CIRCUIT BOARD 117D9944 -- MODE SELECTION

#### CIRCUIT DESCRIPTION II.

The plant communications (PC) mode selection logic is designed to incorporate the following features:

- 1. When the power to the EHC control system is initially turned on, the unit will be in a TRIPPED condition. The PC mode selection will be in MANUAL (MAN).
- 2. When STANDBY (SB) mode is selected at the standby control on the EHC control panel, all other PC modes are automatically dropped out and the STANDBY light is lit.
- 3. MANUAL mode can be selected at the EHC control panel any time that STANDBY is not selected, and when in manual the MANUAL light is lit.
- REMOTE AUTO (RA) mode can be selected at the EHC control panel any time that STANDBY is not selected and the circuit breaker is closed. Opening the circuit breaker reverts this back to MANUAL mode.
- 5. REMOTE OPERATION (RO) (an extra cost accessory) mode can be selected at the EHC control panel any time that STANDBY is not selected and remote operation control power is available.
- Return to the MANUAL mode from REMOTE AUTO or REMOTE OPERATION modes can be done anytime the control is in one of these modes by inputs from --
  - Manual button on control panel
  - Power Load Unbalance (PU)
  - Loss of 125 VDC EHC power
  - d. Customer GO MANUAL for REMOTE AUTO
  - Customer GO MANUAL from REMOTE OPERATION
- 7. The REMOTE SPEED MATCHER (RSM) mode is selectable by customer input when the circuit breaker is open and rated speed is selected.
- The LAMP TEST diodes and resistors are included on the relay logic card. These logic circuits permit all of the lamps to be tested while the EHC system is in operation. A momentary pushbutton switch labeled LAMP TEST is located on the operating panel for checking the lamps.

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TEST INSTRUCTIONS FOR PLANT COMMUNICATIONS LOGIC (MODE SELECTION)

FIRST MADE FOR

EHC MARK II (PLANT COMMUNICATIONS)

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8 SH NO. 3

### II. CIRCUIT DESCRIPTION (continued)

- 9. The following outputs are provided for various reasons
  - a. Indication of IN STANDBY OPERATING MODE
  - b. Remote Speed Match Permission
  - c. Permission to go into REMOTE OPERATION control.
  - d. Permission for REMOTE OPERATION to increase or decrease load set
  - e. REMOTE AUTO or REMOTE OPERATION or remote speed match selected signal to load set
  - f. 24 VDC to loading rate logic when in REMOTE AUTO or REMOTE OPERATION or when circuit breaker is open, rated speed selected, and customer supplied remote speed matcher is selected
  - g. Permission for steam pressure limiter manual increase or decrease
  - h. 24 VDC to stage pressure feedback for permission to valve test when in REMOTE AUTO.
  - i. Permission for REMOTE AUTO to increase or decrease load set.
  - i. REMOTE AUTO mode selected

#### III. CIRCUIT SPECIFICATIONS

- 1. Before the board is coated, each diode of the 20 pairs should be checked (using an ohmmeter) both in the forward and reverse direction to insure that redundance is effective.
- 2. All functional tests shown in the Logic Testing Table can be made with the board coated as well as uncoated.
- 3. The symbols used in the Logic Testing Table are as follows:

#### TABLE SYMBOLS

- 0 = No voltage applied or absence of voltage at output pins and test points or light off.
- 1 = +24 VDC applied to power input pins or presence of +24 VDC at output pins and tests points or light on.
- M = +24 VDC momentarily applied.

Omission = No change from previous test condition.

- 4. Apply +24 VDC to pin 38 and common to pin 40 for all steps in the Logic Testing Table except step 1.
- 5. Before applying +24 VDC to circuit board, perform the following continuity checks:

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TEST INSTRUCTIONS FOR PLANT COMMUNICATIONS LOGIC (MODE SELECTION)

FIRST MADE FOR EHC MARK II (PLANT COMMUNICATIONS)

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III. CIRCUIT SPECIFICATIONS (continued)

5. (continued)

CONTINUITY: Pins 8 to 34 to 36 to 38 to TP1

16 to TP53

17 to 22 to TP52 to TP54

20 to 21 to TP55

24 to 25 to 26 .

-29 to 30

40 to TP2

3 to 38 OPEN CIRCUIT

13 to TP52

33 to TP55

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M Selection
Mode Select
Control Power RA Mode Select
RO Mode Salect
MANUAL Mode Sele
Loss of 125 VDC
PU Actuation AUG MANUAL Light RO Light SB Light RA Light gr. 1750 1751 1752 TP53 SB M 4 5 E Æ 1F1 + FUNCTION UNDER STEP TEST 3 8 17 18 20 21 22 30 31 36 2 6 11 13 19 29 33 16 23 24 27 32 2 4 28 35 1 50 51 52 53 54 55 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 ٥ 0 0 0 0 0 0 ŧ. 0 0 0 0 0 EHC Power Off 1 1 1 1 1 1 2 EHC Power On Ó 0 1 1 ı i Go to SB 1 M\* M\* M\* 4 Try other modes 5 Try RO 0 N 0 0 0 M. M 0 0 0 1 0 o 0 0 1 6, Go from SB 1 1 1 1 0 0 1 7 Go to RO , M 1 8 Try RSM 1 9 Go to SB 0 0 0 0 0 ø 1 0 1 0 1 1 1 0 l M 0 0 1 0 0 1 0 Ð 1 10 Go to RO 1 1 1 М 11 Remove RO Control Power 12 Replace RO 0 0 0 0 1 0 0 0 0 1 ī Control Power 13 Close Ckt. Bkr. 0. 1 H 14 Go to RA M 1 1 1 0 0 1 1 0 0 0 0 15 Open Ckt. Bkr. 0 0 Ð 0 0 1 1 0 1 M 1 16 Go to RA 0 M 1 0 Ö 17 Remove RA 0 0 0 o 0 0 0 ò 0 0 1 1 Control Power 18 Replace RA Control Power T 1 1 H 1 M Ł 0 ı 0 19 Go to RA 1 1 H H ò 20 Try GO MANUAL 0 0. 0 1 0 0 0 n 0 1 21 Lamp Test 1 1 1 0 22 Go to SB 1 ٥ 1 0 ٩l 1 0 1 1 23 Lamp Test 1 1 1 + Momentary power applied one at a time. en de la companya de PRINTS TO D.Mone APPROVALS Jan. 15, 1975 P3K-AL-0360-A01 Steam Turbine \_\_\_\_ RE ISSUED SH NO. Schenectady, N.Y. 5 LOCATION CONT ON SHEET CODE IDENT NO. FF-803-WA (2-75) PRINTED IN U.S.A.

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Test Instruction for Plant Communications Logic (Mode Selection)

FIRST MADE FOR

TITLE

EHC Mark II (Plant Communications)

REVISIO

TEST INSTRUCTIONS

1PC2-BOO1

Assembly

117D9944

Schematic 117D9805

#### PROCEDURE:

- 1. Refer to Fig. A for test circuit of Section I. Refer to Fig. B for test circuit of Section II.
- 2. Measure all test point voltages with respect to TP2.
- 3. The chart which follows outlines the procedure for testing the relay logic of this circuit.

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