g GE Canada Electronic Products Repair

Test Instructions for

<u>0621L0215 G001</u>

Device Number

Drive Excitation Control Card

Description of Device

Originated By:	Maher Albasel	Date: Ma	y 10, 2007
_	Typed Name		mm/dd/yy
Approved By:	Maher Albasel	Approval Date: Ma	y 10, 2007
	Signature		mm/dd/vy

PREVIOUS REVISION SHEET

621L0215 G001

Device Number

Drive Excitation Control Card

Description of Device

Originated By	Date mm/dd/yy Nov. 16, 2006	Description of change
Maher Albasel	Nov. 16, 2006	Created Test Instructions
17141101 1110 4001	1,0,1,10,2000	

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Location: Book or file File

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Date: May 10, 2007

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1. PURPOSE:

a. Static and dynamic test procedures for Drive Excitation Control Card

2. ELEMENTARY:

a. Drawing 266A9681 and 266A1737

3. EQUIPMENT:

- a. DVM TL#001033 or equivalent
- b. Oscilloscope TL#00172 or equivalent
- c. Resistor 1K 1/2 W 0177A1001 P049
- d. Resistor 100K 1/2 W 0177A1002 P001
- e. 6 phase isolated 20VAC bench supply (only needed if card came in without 0632L0504 unit)

4. SET UP:

- a. If 0632L0504 unit came with card disconnect card from unit and test unit separately first
- b. Install 1K resistor into R90
- c. Then reconnect card except do not connect the GPG wires (make sure they are isolated from everything) to 0632L0504 once unit has been tested.
- d. Perform these connections only if card came in alone
 - a. Connect 0° to CP30
 - b. Connect 180° to CP32
 - c. Connect CT to CP31
 - d. Connect 120° to CP10
 - e. Connect 300° to CP12
 - f. Connect CT to CP11

5. PROCEDURE:

a. Apply to AC power and check the incoming AC has a p-p voltage of approximately 21 VAC. Make sure the scope is connected to the CT of each set when checking.

b. Verify the following voltages

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Vdc	±Vdc		Betv	Ripple <xmvp-p< th=""></xmvp-p<>		
		1TB 1TB				
P15	1.67	14	(+)	16	(-)	300
N15	1.67	28	(-)	16	(+)	300
P10	0.1	15	(+)	16	(-)	30
N10	0.1	17	(-)	16	(+)	30

(ensure continuity between 1TB16, CP31, CP38)

		СР		СР		
P15	1.67	14	(+)	9	(-)	800
N15	1.67	16	(-)	9	(+)	800

(ensure continuity between CP9, CP11, CP19)

c. GPG Phasing

d. Verify the following (sync scope to line and use CP30 as reference point)

CP	Test Point	Amplitud	de (Vp-p)	Shift (X°)		
		min	max	min	max	
33	0°	27	34	3	6	
35	90°	15	22	85	90	
36	180°	27	34	183	186	
34	270°	15	22	265	270	

- e. Regulator
 - i. Remove CP3-CP4 and jumper I4-C4
 - ii. Connect the following points
 - iii. 1TB2 to 1TB8
 - iv. 1TB4-1TB6
 - v. 1TB3-1TB17
 - vi. 1TB22-1TB5
 - vii. 1TB13-1TB19
 - viii. 1TB11-1TB15
 - ix. Adjust 1TB2 for 0 VDC with R2 CCW should get 0Vdc ±30mVDC at CP5
 - x. Adjust 1TB2 for -1 VDC by turning R2 CW should get 7.5Vdc ±500mVDC at CP5
 - xi. With power off verify $4.7k\Omega$ between 1TB7 and CP4
 - xii. Check for 1μF and 470KΩ between CP1 and CP2

f. Aux Pots

i. Turn power back on.

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- 1TB12 = 0Vdcii. R4 $CCW \Rightarrow$ iii. R4 CW 1TB12 = +10Vdciv. R1 $CCW \Rightarrow$ 1TB21 = -1.8Vdc1TB5 v. R1 CW 1TB21 = -5.8Vdc \Rightarrow 1TB20 $CCW \Rightarrow$ 1TB2 = 0Vdcvi. R2 CW 1TB2 = -10Vdcvii. R2 \Rightarrow
- viii. Turn all pots back CCW
- ix. Feedback Level Detector
- x. Connect CP7 to 1TB12.
- xi. Verify the following:
- xii. R4 CCW 1TB9 should be 300 mVDC wrt 1TB10
- xiii. Turn R4 CW until 1TB12 is 2 VDC wrt COM
- xiv. 1TB9 should be 31 VDC
- xv. Increase 1TB12 and 1TB9 should remain 31 VDC
- g. Isolated Current Feedback Amplifier
 - i. Disconnect feedback wires to CP8 and CP9; make sure wires are isolated cover ends if needed (only needed if testing both card and base unit together)
 - ii. Jumper CP8 to CP9 and ensure R90 (1K Ω) is installed.
 - iii. Apply power and verify CP17 \leq 30 mVDC.
 - iv. Turn power off
 - v. Install 100K for R90
 - vi. Move jumper from CP8 to CP14 turn power on CP17 should be -1.5 VDC
 - vii. Move jumper from CP8 to CP16 turn power on CP17 should be +1.5 VDC
 - viii. Power down and reinstall $1k\Omega$ into R90 (reconnect the wires to CP8 and CP9 if using base unit)
 - ix. Feedback Circuit
 - x. Remove feedback wires marked CP21, CP22 (only do if testing card and base together)
 - xi. With no power applied, measure the following

CP	to	CP	R3	Value	
18	to	21	N/A	100k	
20	to	22	N/A	100k	
21	to	COM	CW	1050	
21	to	COM	CCW	1k	
22	to	COM	CW	1050	
22	to	COM	CCW	1k	

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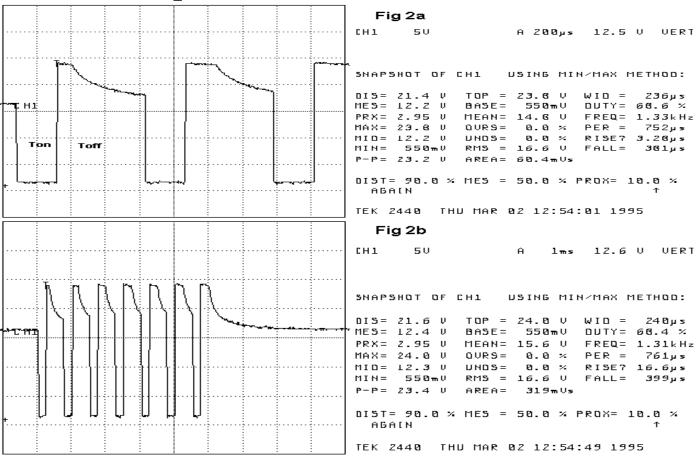
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C to ?	CP	CP	V I/P @ CP	O/P	@ CP	O/P @ C3	(±)	O/P @ CP3	(±)
open	i3	n/a	0	n/a	n/a	n/a	n/a	0mV	40mV
i	18	20	0	n/a	n/a	0mV	40mV	0mV	440mV
i	18	n/a	P10	21	100mV	(-)1V	5%	(+)10V	10%
i	20	n/a	P10	22	100mV	(+)1V	5%	(-)10V	10%
V	18	20	0	n/a	n/a	0mV	4mV	0mV	44mV
V	18	n/a	P10	21	100mV	(-)100mV	5%	(+)100mV	10%
V	20	n/a	P10	22	100mV	(+)100mV	5%	(-)100mV	10%

h. GPG circuits

- i. Connect CP5 to CP6 and adjust R2 fro 4.5 VDC at CP6
- ii. Observe fig 2a and fig 2b at both CP29's
- iii. $150 \mu s \le Ton \le 300 \mu s$
- iv. $300 \mu s \le Toff \le 600 \mu s$
- v. Vmax ≤25.2 VDC



- i. Dynamic Test (can only be done if base unit was sent with card)
 - i. Attach all GPG and feedback wires as per wire markers.

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ii. Ensure all pots are turned CCW and R90 (1K Ω) is installed.

- iii. Ensure that the jumpers and feedback wires are set for the same mode (ie. current voltage). Note that the wire markers are set up for the current mode. To change to Voltage mode use CP18, CP20 and change all C-I jumpers to C-V.
- iv. Monitor the O/P load with a scope in differential mode and 100X probes.
- v. Monitor CP29 "A" or "B".
- vi. Apply AC power and observe that the GPG LEDs are off. If one LED is on then adjusting R3 for a system balance MAY be the answer. The GPG static tests will have already proofed these circuits so that a feedback system balance is usually the problem.
- vii. Slowly turn R2 CW and see that both GPG circuits turn on at the same time and that the O/P load is phasing up evenly. Note that there may be some differences at the low end but once you start drawing more current the waveforms should stabilize.
- viii. Set current for about 2A O/P and let run with "capture" mode on the 'scope; the O/P should remain stable for the run.
- ix. Set card up in Voltage mode and verify regulation in that mode (ie. power up again and turn R2 slowly CW again).
- x. Leave R3 UNSEALLED since the card may require system balancing at customer's site.

6. UPGRADES:

- a. See revision page
- 7. END.