PFV .			_	68 4 9 9 3 4 5 9		1
REY HO. A	993459	TITLE TEST SPECIF OVERSPEED S		CONT ON SHEET 2 SH	NO.	•
CONT ON SH	еет 2 sн но. 1	FIRST MADE FOR	IC3600Q0XA,1,2,3,4,5,	6	1.	
						REVISIO
2.	CHECK THAT THE REED R3 ARE 100 OHMS 2W,			38A997168A03. CHECK R2 AND	13-73	
3.	APPLY POWER TO THE C	ARD AS FOLLOWS:			m	ا بو ب
	+12V (27) -50V (30)				CWZ.	18-48 JH
	A COM (2-5 P COM (25) D COM (1-5	· · ·	CETHER.		=	
		S (1), (2), (25) T 20 KC LOADS TO OT		٠.		ς ,
4.	PEAK TO PEAK. THEN	CONNECT THIS SIGNA BULD DROP TO 9 ± 2	L GENERATOR TO IN (:	EDANCE TO 3000 Hz, 24 V 7) AND A COM (2-50). SURE THE VOLTAGE ON TP3 (21)	725/75	DOED FORM
5,	TP1 (19) AND TP2 (11) THESE SIGNA DO OF 330 ± 50 MICRO	LLS SHOULD BOTH BE SQL	ND CHECK THE SIGNAL AT JARE WAVES OF AMPLITUDE ND FALL TIME OF THE SQUARE	COL 11	/ 69/L/
	ANTE 11001 DE 2200 11	MIN TOO MACO!			1	79 5
6.	WITH THE SIGNAL ON	N (7) AT 5V PE	K TO PEAK, AND AT THI OM TP4(49) TO TP5(48)	E <u>run</u> frequency shown on to This must be 9 to 14 vo	ET E	10 7 THE
6.	WITH THE SIGNAL ON CARD FRONT, MEASURE	N (7) AT 5V PE	OM TP4 (49) TO TP5 (48)	E <u>Run</u> Frequency Shown on Ti . This Must be 9 to 14 voi	LTS.	107 / 104 4 B/C
6.	WITH THE SIGNAL ON CARD FRONT, MEASURE CHECK THAT OUTPUT 48 THAN 10 VOLTS.	THE DC VOLTAGE FRO	THAN 0.4 VOLTS AND OUT	. THIS MUST BE 9 TO 14 VO	LTS.	19 + 1907 (18) (1907) 1816 (1907)
	WITH THE SIGNAL ON CARD FRONT, MEASURE CHECK THAT OUTPUT 48 THAN 10 VOLTS. NOW TIE TP6 (9) TO IS LESS THAN 3.5 VOL RELAY DROPOUT CHECK, REMOVE GND. FROM PIL TURN THE SLUG IN T2	THE DC VOLTAGE FRO CC (35) IS LESS TO COM (25) AND CHECOTS.	THAN 0.4 VOLTS AND OUT THAN THE VOLTAGE FI	. THIS MUST BE 9 TO 18 VOICEPUT OAEC (45) IS GREATER ROM TP4 (49) TO TP5 (48)	LTS	7 1704.13.1971 A
	WITH THE SIGNAL ON CARD FRONT, MEASURE CHECK THAT OUTPUT 48 THAN 10 VOLTS. NOW TIE TP6 (9) TO IS LESS THAN 3.5 VOI RELAY DROPOUT CHECK. REMOVE GND. FROM PIE TURN THE SLUG IN T2 SHOWN IN THE TABLE 6 CAT. NO. TI CARD IC3600Q0XA1	THE DC VOLTAGE FROM (25) IS LESS TO COM (25) AND CHECK, TS. N (9). FULLY COUNTERCLOCK BELOW. USE A COUNTRIP FREQ.	THAN 0.4 VOLTS AND OUT CK THAT THE VOLTAGE FIRE CWISE AND INCREASE THE FER TO INSURE THE FREE STEP 9A PICKUP UNDER 320HZ	E INPUT FREQUENCY TO TRIP QUENCY IS SET PROPERLY . STEP 9A DROPOUT OVER 145HZ	LTS	7 1704.13.1971 A
	WITH THE SIGNAL ON CARD FRONT, MEASURE CHECK THAT OUTPUT 48 THAN 10 VOLTS. NOW TIE TP6 (9) TO IS LESS THAN 3.5 VOLTS. RELAY DROPOUT CHECK, REMOVE GND. FROM PIL TURN THE SLUG IN T2 SHOWN IN THE TABLE 8 CAT. NO. IT CARD IC3600Q0XA1 CARD IC3600Q0XA3	THE DC VOLTAGE FROM (25) AND CHECK (35) IS LESS TO COM (25) AND CHECK (15). N (9). FULLY COUNTERCLOCK (15) FREQ. (15) HZ	THAN 0.4 VOLTS AND OUT CK THAT THE VOLTAGE FOR THAT THE VOLTAGE FOR THE FREE TO INSURE THE FREE STEP 9A PICKUP UNDER 320HZ UNDER 460HZ UNDER 585HZ	THIS MUST SE 9 TO 18 VOLUMENT OF THE PROPERTY OF TRIP QUENCY IS SET PROPERLY . STEP 9A DROPOUT OVER 145HZ OVER 266HZ	LTS	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
	WITH THE SIGNAL ON CARD FRONT, MEASURE CHECK THAT OUTPUT 48 THAN 10 VOLTS. NOW TIE TP6 (9) TO IS LESS THAN 3.5 VOLTS RELAY DROPOUT CHECK, REMOVE GND. FROM PII TURN THE SLUG IN T2 SHOWN IN THE TABLE BE CAT. NO. IT CARD IC3600Q0XA1 CARD IC3600Q0XA2 CARD IC3600Q0XA4 CARD IC3600Q0XA4 CARD IC3600Q0XA5	THE DC VOLTAGE FROM (25) IS LESS TO COM (25) AND CHECK, TS. N (9). FULLY COUNTERCLOCK BELOW. USE A COUNTRIP FREQ. 8960 HZ	THAN 0.4 VOLTS AND OUT THAN 0.4 VOLTS AND OUT THAT THE VOLTAGE FOR THAT THE VOLTAGE FOR THE FREE TO INSURE THE FREE STEP 9A PICKUP UNDER 320HZ UNDER 460HZ	E INPUT FREQUENCY TO TRIP QUENCY IS SET PROPERLY . STEP 9A DROPOUT OVER 145HZ OVER 205HZ	LTS	25.00 May 18.10
	WITH THE SIGNAL ON CARD FRONT, MEASURE CHECK THAT OUTPUT 48 THAN 10 VOLTS. NOW TIE TP6 (9) TO IS LESS THAN 3.5 VOLTS RELAY DROPOUT CHECK, REMOVE GND. FROM PII TURN THE SLUG IN T2 SHOWN IN THE TABLE 8 CAT. NO. II CARD IC3600Q0XA1 CARD IC3600Q0XA2 CARD IC3600Q0XA4 CARD IC3600Q0XA5	THE DC VOLTAGE FRO THE DC VOLTAGE FRO TO (35) IS LESS TO TO COM (25) AND CHECO TS. N (9). FULLY COUNTERCLOCK THE FREQ. THE THE FREQ. THE	THAN 0.4 VOLTS AND OUT CK THAT THE VOLTAGE FITTER TO INSURE THE FREE STEP 9A PICKUP UNDER 320HZ UNDER 460HZ UNDER 630HZ UNDER 440HZ	THIS MUST BE 9 TO 18 VOID TPUT O4EC (45) IS GREATER ROM TP4 (49) TO TP5 (48) E INPUT FREQUENCY TO TRIP QUENCY IS SET PROPERLY . STEP 9A DROPOUT OVER 145HZ OVER 205HZ OVER 280HZ OVER 195HZ	LTS	25.00 May 18.10

DIV OR INDUSTRY CONTROL 68 A 9 9 3 4 5 9 SALEM, VIRGINIA Fab. 15, 1968 LOCATION CONT ON SHEET 2

FF803-WF (2-66) PRINTED IN U.S.A.

SH NO. 1

CONT ON SHEET SH NO

TEST SPECIFICATIONS OVERSPEED SENSOR

CONT ON SHEET SH NO.

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FIRST MADE FOR IC3600QOXA 1,2,3,4,5,6

WITH AN OHMMETER ACROSS THE REED RELAY CONTACTS (PINS 46 AND 47), SLOWLY TURN THE SLUG IN T2 CLOCKWISE UNTIL RELAY K1 DROPS OUT REVISIONS (I.E. THE RESISTANCE SHOWN ON THE OHMMETER GOES TO INFINITY.

IF TRIP OCCURS WITH T2 SLUG FULL CCW (OUT AND LOOSE), DECREASE C2 TO THE NEXT STANDARD VALUE (OR REMOVE C2 ENTIRELY, IF NECESSARY) TO MOVE THE TRIP POINT OF THE SLUG DOWN INTO THE TURNING HOLE.

IF TRIP OCCURS WITH T2 SLUG FULL CW (DOWN INTO TURNING HOLE ALL THE WAY) INCREASE C2 TO THE NEXT STANDARD VALUE, TO MOVE THE SLUG UPWARD. CONTINUE TO INCREASE C2 UNTIL TRIP OCCURS WITH THE SLUG NEAR THE CENTER OF THE TURNING RANGE.

NOW REDUCE THE FREQUENCY UNTIL THE RELAY PICKS UP AGAIN AND SLOWLY INCREASE THE FREQUENCY UNTIL THE RELAY DROPS OUT. THIS FREQUENCY MUST BE WITHIN _2% HZ OF THE FREQUENCY SHOWN IN THE TABLE ABOVE. REPEAT THIS TEST AT LEAST ONCE MORE TO INSURE THAT THE RELAY WILL DROP OUT AT A FREQUENCY WITHIN .2% HZ OF THE STATED FREQUENCY. WITH THE SIGNAL GENERATOR 400HZ ABOVE THE STATED TRIP FREQUENCY, CHECK THAT THE DC VOLTAGE FROM TP4 (49) TO TP5(48) IS LESS THAN 3.5V. NOTE: WITH REV. H AND ABOVE INCREASE SIGNAL TO 10VP-P FOR THIS STEP ONLY.

- 8. CHECK OUTPUTS 4EC(35) AND 04EC(45) FOR PROPER OPERATION AT TRIP FREQUENCY. REDUCE THE FREQUENCY UNTIL 4EC(35) IS A O AND 04EC IS A 1. SLOWLY INCREASE THE FREQUENCY UNTIL 4EC (35) GOES TO A 1 AND 04EC(45) TO A 0. THIS FREQUENCY MUST BE WITHIN +.5% CYCLES OF THE STATED FREQUENCY IN THE TABLE IN STEP 7. IF IS NOT WITHIN ± .5% CYCLES, ADJUST POT.R60 TO BRING 4EC(35) AND 04EC(45) INTO SPECS.
- 9. VERIFY THAT DROPOUT FREQUENCY IS WITHIN .2% CYCLES OF THAT
- 9A.RELAY ALSO OPERATED BETWEEN 4 AND 9% OF OPERATING SPEED. PU AND DO PER TABLE IN STEP 7. CHECK
- 10. WITH INPUTS 04ECA(31) AND 04A(33) A 1 AND 014HM (44) A 0,0TP (37) SHOULD BE A O. REMOVE AND RE-APPLY POWER SEVERAL TIMES AND CHECK THAT EACH TIME POWER IS APPLIED, OTP COMES ON A \underline{o} .

CONNECT 04ECA(31),04A(33) AND 014HM (44) TO COM, ONE AT A TIME. OTP SHOULD REMAIN A O.

CONNECT A VOLTAGE DIVIDER CONSISTING OF TWO 1.5K RESISTORS BETWEEN P12 AND COM. CONNECT THE MIDPOINT OF THE DIVIDER TO

CONNECT 014HM (44) AND 04ECA (31) TO COM. OTP SHOULD BE A 1. DISCONNECT BOTH INPUTS FROM COM AND OTP SHOULD REMAIN A 1. PUSH

CONNECT 014HM (44) AND 04A (33) TO COM. OTP SHOULD BE A $\underline{1}$. DISCONNECT BOTH INPUTS FROM COM AND OTP SHOULD REMAIN A 1.

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MADE BY J.SMITH 2-15-68 R E APPROVALS PRINTS TO DRIVE SYSTEMS 2-13-80 RE 68A99 DIV OR 2-16-80 Rashla SALEM, VA. FF-803 WF (7-79-A-1) PRINTED IN U.S.A. LOCATION CONT ON SHEET 3 2 SH NO. CODE IDENT NO

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FF-803 WF (7-79-A-1) PRINTED IN U.S.A. FL

LOCATION CONT ON SHEET

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