

REV. NO. 1	TITLE TEST INSTRUCTIONS FOR VOLTAGE COMPARATOR LOW HYSTERESIS	CONT ON SHEET 2 SH. NO. 1
P3K-AL-0390-A01	FIRST MADE FOR	

I. SCOPE

This instruction outlines the test specifications for circuit board 125D4413 Groups 1, 2 and 3 (Schematics 125D3221, 137D2100 and 145D3821). *

II. CIRCUIT DESCRIPTION

This circuit is used for special voltage comparison functions in the Mark II system which require a small hysteresis or deadband between pickup point and dropout point.

Each circuit board contains two identical Voltage Comparator functions. The circuit, in general, looks at two input voltages and picks up a relay when one voltage exceeds the other in accordance with the following rules:

- * TYPE A Relay (and LED) picks up when the voltage on Input #1 is more positive than the reference voltage which is connected on Input #2.
- * TYPE B Relay (and LED) picks up when the voltage on Input #2 is more negative than the reference voltage which is connected on Input #1.
- * TYPE C Relay (and LED) picks up when the voltage on Input #1 is more positive than the voltage on Input #2. (This internal reference voltage is not used in this case).

The relay contacts available per voltage comparator are two single pole double throw dry circuit contacts with 3.0 amp capacity at 28 VDC and resistance load.

CIRCUIT DESCRIPTION

This circuit, in general, consists of a high input impedance discrete component differential amplifier; an integrated circuit differential comparator; a transistor relay driver; a relay with bifilar coil and dry contacts; and a temperature compensated adjustable reference voltage with plus and minus capability.

A balance potentiometer is provided in the differential amplifier section so that the firing point can be adjusted exactly in spite of small component differences in each half of the amplifier. A hysteresis potentiometer is provided around the integrated circuit comparator to allow some adjustment of the difference between the pick up point and drop-out point of the circuit. Having some hysteresis also prevents the relay from chattering if the input voltage is holding near the reference voltage. In order to improve noise immunity and to prevent false triggering on narrow pulses, two R-C filter networks have been included. One is on the input of the differential amplifier and the other is on the input of the integrated circuit differential comparator.

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III. CIRCUIT SPECIFICATIONS

A. Power Supply Requirements

- Power Supply 1: $+22.000 \pm 0.002$ VDC
Pin 37 at 150 ma
- Power Supply 2: -22.000 ± 0.002 VDC
Pin 41 at 60 ma

B. Input Signal Levels

- VC1
 - + Input (Pin 29) ± 15.0 VDC max.
 - Input (Pin 26) ± 15.0 VDC max.
- VC2
 - + Input (Pin 4) ± 15.0 VDC max.
 - Input (Pin 3) ± 15.0 VDC max.

C. Outputs

- VC1
 - Reference Voltage (Pin 24) adjustable by changing VR4 over the range ± 11.7 VDC with tolerance $\pm 5\%$.
 - Relay K1 Contacts
 - K1-1 Pin 35 Common 34
Pin 38 Normally Closed 36
Pin 36 Normally Open 35
 - K1-2 Pin 34 Common 33
Pin 32 Normally Closed
Pin 30 Normally Open
- VC2
 - Reference Voltage (Pin 2) adjustable by changing VR2 over the range ± 11.7 VDC with tolerance $\pm 5\%$.
 - Relay K2 Contacts
 - K2-1 Pin 12 Common
Pin 16 Normally Closed
Pin 14 Normally Open

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III. CIRCUIT SPECIFICATIONS (continued)

C. Outputs (continued)

2. VC2 (continued)

b. (continued)

2. K2-2 Pin 10 Common
Pin 8 Normally Closed
Pin 7 Normally Open

D. Performance Specifications

1. VC1

a. Set Balance Potentiometer (VR3)

1. Set up the following conditions:

Hysteresis adjust pot VR50 to max. CCW, + Input (Pin 29) to ground, - Input (pin 26) to ground. Monitor K1 relay contacts (Pin 35) and (pin 36) for continuity.

2. Adjust VR3 until the relay K1 is just on the edge of picking up. (It may take several tries in order to get the exact point).

b. Check Operation Mode with Removed Inputs

1. Without inputs connected to pins 26 and 29, K1 relay should be energized.

2. Apply +250mV to pin 26: K1 should de-energize.

3. Remove the +250mV from pin 26: K1 should energize again.

4. Apply -250mV to pin 29: K1 should de-energize.

5. Remove the -250mV from pin 29: K1 should energize again.

6. If steps 1 or 2 or 5 fail to realize, turn VR3 slightly CW until K1 energizes. If steps 2 or 4 fail to realize, turn VR3 slightly CCW until K1 de-energizes. Repeat steps 1 through 5.

c. Check Reference Voltage (Pin 24)


1. Monitor Pin 24 with a digital voltmeter. Adjust VR4 over its entire range and verify that the voltage output is in accordance with Section III. C. 1. a. above.

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III. <u>CIRCUIT SPECIFICATIONS</u> (continued)			
D. Performance Specifications (continued)			
1. VC1 (continued)			
g. Check Hysteresis - High End			
1. Set up conditions for Step III. D. 1. d. above.			
2. Set potentiometer VR50 to max. CW.			
3. Pick up and drop out relay K1. Monitor the external voltage on (Pin 29) where the relay picks up and drops out. The difference in voltage between pickup and drop out must be greater than 120 mv.			
h. Check Pickup Time			
1. Set up the following conditions for Type A operation: Hysteresis adjust pot VR50 to max. CCW, + Input (Pin 29) to a voltage source and switch (S1) such that this input can be switched from 0V to +5V, - Input (Pin 26) to reference voltage (Pin 24). Set reference voltage (Pin 24) to 1.0 V. Monitor DC voltage source of nominally 24 VDC through the K1-1 normally open relay contacts with an oscilloscope. Trigger the oscilloscope with the output of external switch S1.			
2. Close S1 and observe on the oscilloscope the time it takes for relay contacts K1-1 to close. The pickup time must be less than 10 ms.			
i. Check Drop Out Time			
1. This test can be performed at the same time as III. D. 1.h. above with the same set up.			
2. Open switch S1 and observe on the oscilloscope the time it takes for relay contacts K1-1 to open. The drop out time must be less than 34 ms.			
2. VC2			
a. Set Balance Potnetiometer (VR1)			
1. Set up the following conditions:			
Hysteresis adjust pot VR51 to max. CCW, + Input (Pin 4) to ground, - Input (Pin 3) to ground. Monitor K2 relay contacts (Pin 12) and (Pin 14) for continuity.			
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III. CIRCUIT SPECIFICATIONS (continued)

D. Performance Specifications (continued)

2. VC2 (continued)

a. (continued)

2. Adjust VR1 until the relay K2 is just on the edge of picking up. (It may take several tries in order to get the exact point).

b. Check Operation Mode with Removed Inputs

1. Without inputs connected to pins 3 and 4, K2 relay should be energized.
2. Apply +250mV to pin 3: K2 should de-energize.
3. Remove the +250mV from pin 3: K2 should energize again.
4. Apply -250mV to pin 4: K2 should de-energize.
5. Remove the -250mV from pin 4: K2 should energize again.
6. If steps 1 or 3 or 5 fail to realize, turn VR1 slightly CW until K2 energizes. If steps 2 or 4 fail to realize, turn VR1 slightly CCW until K2 de-energizes. Repeat steps 1 through 5.

c. Check Reference Voltage (Pin 2)

1. Monitor Pin 2 with a digital voltmeter. Adjust VR2 over its entire range and verify that the voltage output is in accordance with Section III. C. 2. a. above.

Voltage range from +11.7 to -11.7V

d. Check Pick Up Point

1. Set up the following conditions for Type A operation:

Hysteresis adjust pot VR51 to max. CCW, + Input (Pin 4) to some adjustable external voltage, - Input (Pin 3) to reference voltage (Pin 2). Set reference voltage (Pin 2) to some convenient voltage between 0 and +10 VDC. Monitor K2 relay contacts (Pin 12) and (Pin 14) for continuity. Monitor the external voltage (Pin 4) with a digital voltmeter.

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III. CIRCUIT SPECIFICATIONS (continued)

D. Performance Specifications (continued)

2. VC2 (continued)

d. (continued)

2. Slowly increase the external voltage (Pin 4) from some value below VREF and note its value where relay K2 picks up. This pick up voltage should be equal to the reference voltage on (Pin 3) within ± 30 mv. Several tries may be necessary to verify the pick up point. The relay must stay picked up as long as the external voltage (Pin 4) is more positive than the reference voltage on (Pin 3). To drop out the relay, the external voltage (Pin 4) must be reduced below the reference voltage (Pin 3).

3. Repeat steps 1 and 2 with the reference voltage (Pin 2) set to some convenient voltage between 0 and -10 VDC.

e. Check Relay Contacts

1. Set up conditions for Step III. D. 2. d. above.
2. Monitor K2-1 and K2-2 relay contacts for continuity.
3. Pick up and drop out relay K2 and verify that all contacts will open and close.

f. Check Hysteresis - Low End

1. Set up conditions for Step III. D. 2. d. above.
2. Set potentiometer VR51 to max. CCW.
3. Pick up and drop out relay K2. Monitor the external voltage on (Pin 4) where the relay picks up and drops out. The difference in voltage between pick up and drop out must be less than 28 mv.

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III. CIRCUIT SPECIFICATIONS (continued)

D. Performance Specifications (continued)

2. VC2 (continued)

g. Check Hysteresis - High End

1. Set up conditions for Step III. D. 2. d. above.
2. Set potentiometer VR51 to max. CW.
3. Pick up and drop out relay K2. Monitor the external voltage on (Pin 4) where the relay picks up and drops out. The difference in voltage between pick up and drop out must be greater than 120 mv.

h. Check Pick Up Time

1. Set up the following conditions for Type A operation:
Hysteresis adjust pot VR51 to max. CCW, + Input (Pin 4) to a voltage source and switch (S1) such that this input can be switched from 0V to +5V, - Input (Pin 3) to reference voltage (Pin 2). Set reference voltage (Pin 2) to +1.0V. Monitor a DC voltage source of nominally 24 VDC through the K2-1 normally open relay contacts with an oscilloscope. Trigger the oscilloscope with the output of external switch S1.
2. Close S1 and observe on the oscilloscope the time it takes for relay contacts K2-1 to close. The pick up time must be less than 18 ms.

i. Check Drop Out Time

1. This test can be performed at the same time as III. D. 2. h. above with the same set up.
2. Open switch S1 and observe on the oscilloscope the time it takes for relay contacts K2-1 to open. The drop out time must be less than 34 ms.

IV. SET POINTS

A. VC1

1. Set Hysteresis Band

a. Use method outlined in Step III. D. 1. e. above.

Do not do this test unless set points are given. just 11/1/88.

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IV. SET POINTS (continued)

A. VC1 (continued)

1. (continued)

- b. Vary the setting of potentiometer VR50 to obtain any desired hysteresis band between min. and max. Turning VR50 CW will cause the hysteresis band to increase. Repeated tries will be necessary to obtain the desired hysteresis band.

2. Set Balance Potentiometer (VR3) - Final

- a. After desired hysteresis band has been set, a check or reset of balance pot should be done as outlined in section D. 1. a. and D. 1. b. above.

B. VC2

1. Set Hysteresis Band

- a. Use method outlined in Step III. D. 2. e. above.
- b. Vary the setting of potentiometer VR51 to obtain any desired hysteresis band between min. and max. Turning VR51 CW will cause the hysteresis band to increase. Repeated tries will be necessary to obtain the desired hysteresis band.

2. Set Balance Potentiometer (VR1) - Final

- a. After desired hysteresis band has been set, a check or reset of the balance pot should be done as outlined in section D. 2. a. and D. 2. b. above.

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P. C. Allen

DATE _____

9-6-77

P.C. Callan - MANAGER
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TEST PROCEDURE

REVIEWED BY

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DATE _____

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R. Debertolis
EHC TEST ENGINEER

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