68A989773 CONT ON SHEET 2 SH NO. TITLE PULSE GENERATOR TEST SPECIFICATIONS 68A989773 1C3600TPGA1 2 sh No. 1 FIRST MADE FOR CONT ON SHEET REVISIONS IC3600TPGA1 SH. 3.0 ELEMENTARY GENERAL THIS TEST SPEC REQUIRES THAT TEST SELECT AND SOLDER FOUR RESISTORS TO THE CARD. THE RESISTOR SELECTION IS MADE SUCH THAT THE TEST CARD WILL TRACK A MASTER CARD WITHIN SPECIFIED LIMITS. THE MASTER CARD WILL BE SUPPLIED AND PERIODICALLY RETESTED BY SPC DEPT. IN PHILADELPHIA. COVERS THE PROCEDURE TO MAKE A MASTER CARD BUT REQUIRES A THEIR DRAWING MORE COMPLICATED SETUP THAN THAT LISTED BELOW. TEST EQUIPMENT 1 - MASTER CARD TO BE FURNISHED BY SPC 1 - 5" DUAL CHANNEL OSCILLOSCOPE 1 - AC POWER SUPPLY PER FIGURE 1 1 - DC POWER SUPPLY CENTERTAPPED WITH QUITPUT POTENTIOMETER AND VOLTMETER TO SUPPLY ±20V ADJUSTABLE, 50 MA. RIPPLE TO BE UNDER 100 MV. 1 - DC BIAS SUPPLY ISOLATED FROM OTHERS, 80V 25MA, MAX. 5% RIPPLE 2 - DECADE RESISTOR BOXES 10 TO 1500 OWMS, 2 WATTS OR EQUIVALENT. 1 - MISC RESISTORS, ETC. PER FIGURE 2. 1 - SUPPLY OF 1/2 WATT CARBON RESISTORS TYPE 68A7000 10 OHM. 1 - SUPPLY OF 1/2 WATT 1% RESISTORS TYPE 68A7030 51.1, 82.5, 121, 150, 182, 221, 274, 332, 392, 475, 562, 681, 825, AND 1000 OHMS. AC SUPPLY 1 **60CY** 260/400n SHOP 115V 100W SUPPLY (OR TRANSFORMER) LIMIT a + SIGNAL 1338 115V 2300 R2 C1 3**60**/50**0**Ω P6A FIGURE 1 10MFD ħ 50W 600V ÇQM WITH TWO CARDS CONNECTED PER FIGURE 2, ADJUST R1 AND R2 SO THAT SIG TO COM AND LIM TO COM ARE SAME VOLTAGE WITHIN 2% AND SIG TO COM LAGS BY 30 ELECTRICAL DEGREES +2 DEGREES.

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MADE BY

V.M. STOCKTON

APPROVALS

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DEFT. 68A 98 9 7 7 3

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GENERAL ELECTRIC 68A989773 2 CONT ON SHEET 3 SH NO. REY A TITLE GATE PULSE GENERATOR TEST SPECIFICATIONS 68A9B9773 IC3600TPGA1 CONT ON SHEET 3 2 SH NO. FIRST MADE FOR REVISIONS TEST CONNECTIONS LIM 22 MASTER AC CARD 44 4 SUPPLY SIG 50 270 30 COM 18 270 807 2 8 DC 32 SUPPLY 6 38 POLARITY 40 SIDE (+ OR 100MFD NON-VAR. POLAR ±20V COM SUPPLY 22 TEST CARD DECADE 44 BOX #1 18 2 38 32 6 270 מלבר 40 46 DECADE BOX 48 30 #2 1338 SCOPE TEST 2 15W P6A 4 31DE 30 SIDE 25W MASTER FIRST EXT , TEST FIRST **GND**• FIGURE 2 PRINTS TO DIV OR V.M. STOCKTON INDUSTRY CONTROL 68 A 9 8 9 7 7 3

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LOCATION CONT ON SHEET 3

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CONT ON SHEET 4 SH NO. 3

68 A 9 B 9 7 7 3

TITLE

GATE PULSE GENERATOR TEST SPECIFICATIONS

CONT ON SHEET 4 SH NO. 3

FIRST MADE FOR

IC3600TPGA1

REVISIONS

TEST STEPS

- 1. SET UP. CHECK ONCE PER RUN OF CARDS.
 - A. SET DECADE BOX #1 AT 400 OHMS.
 - . SET DECADE BOX #2 AT 650 OHMS.
 - B. CHECK MAGNITUDE AND PHASE OF THE TWO AC VOLTAGES.
 - C. CHECK FIXED DC POWER SUPPLY FOR BO VOLTS AND UNDER 4 VOLT PEAK TO PEAK RIPPLE.
 - D. CHECK VARIABLE POWER SUPPLY AT APPROXIMATELY 20V POSITIVE AND NEGATIVE FOR MAXIMUM OF 100 MV PEAK TO PEAK RIPPLE.
- 2. OUTPUT PULSES.
 - A. SET DECADE BOXES AS IN 1A ABOVE.
 - B. SCOPE CHANNEL A ON 30 AND B ON 4 OF TEST CARD. (TOP POS. OF 15W). TRIGGER ON LINE SYNCH AND DISPLAY APPROXIMATELY ONE CYCLE. (2 MS/CM IS OK)
 - C. PULSE TO MOVE FROM RIGHT TO LEFT APPROXIMATELY 1/2 CYCLE AS VM IS CHANGED FROM +20V TO -20V. SEE FIGURE 3. [MIST CARDS HAVE PULSES THAT DISAPPEAR AT -12V, AND STOP MOVING TO LEFT AT +15V. THESE VALVES ARE NOT IMPORTANT AS CARD IS TRACKED IN STEP 3).

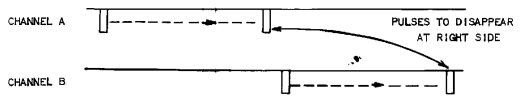


FIGURE 3

D. CHANGE SCOPE TRIGGER TO INTERNAL MINUS AND CHANNEL A ONLY. PULSE TO HAVE WAVE SHAPE SHOWN IN FIGURE 4. INPUT VM AT O.

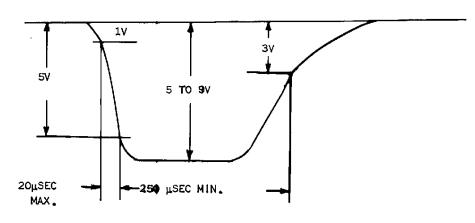


FIGURE 4

E. CHANGE TO CHANNEL B AND CHECK PER FIGURE 4.

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5 CONT ON SHEET SH NO.

BOTH ZEROES AT

TOP OF SCREEN

FIGURE 5

TITLE 68A989773

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CONT ON SHEET

PULSE GENERATOR TEST SPECIFICATIONS

FIRST MADE FOR IC3600TPGA1

REVISIONS

- 3. TRACKING THE MASTER CARD.
 - A. SCOPE CHANNEL A ON 4 OF MASTER AND B ON 4 OF TEST CARD. (MID POS OF 1SW) SCOPE IN EXTERNAL MINUS DC TRIG AND ALTERNATE MODE. USE 25W TO TRIGGER ON FIRST PULSE AND NAMEPLATE ON 25W TO INDICATE WHICH PULSE IS FIRST.
 - B. SET VM AT ZERO AND ADJUST DECAME BOX #2 TO OBTAIN FIGURE 5. IF MASTER IS FIRST DECREASE CHAIS IF TEST IS FIRST IN INCREASE CHAS

EXT (-) TRIGGER ON FIRST PULSE (2SW) . 3 TO 4V LEVEL MAKE FINAL ADJUSTMENT ON 10 LSEC/CM SCALE

- * TO ALLOW MARGIN FOR RESISTOR TOLERANCES TRY TO HOLD UNDER 35 MICROSECONDS IN THIS STEP. HOWEVER 60 IS OK AS LONG AS CARD IS STILL UNAFF 60 IN STEP 3G.
- C. TURN VM POSITIVE UNTIL PULSES STOP SHIFTING WITH RESPECT TO EACH OTHER. (SHOULD BE APPROX. 15V). REDUCE VM CHIE VOLT AND ADJUST DECADE BOX #1 TO OBTAIN FIG. 5. IF MASTER IS FIRST INCREASE CHARS IF SLAVE IS FIRST DECREASE OHMS
- D. REPEAT B AND C UNTIL TRACKING WEETS FIGURE 5 AT ANY POINT WHILE VM IS SWEPT FROM PLUS 14V TO -11V. (NOTE: SEPARATION AT +15V IS SET BY FORWARD CLAMP AND DECADE BOXES CANNOT CHANGE THIS).
- E. SCOPE CHANNEL A ON 30 OF MASTER, B ON 30 OF TEST CARD (BOTTOM POS OF 1SW) AND CHECK TRACKING OVER SAME RANGE, IF PULSES ARE WITHIN 30 MSEC OF MEETING FIGURE 5 THEN REPEAT STEPS B AND C TO BARELY BRING THESE IN AND RECHECK TRACKING OF ON TERMINAL 4. (IF TRACKING ON 4 AND 30 ARE WIDELY DIFFERENT, A NEW TRANSFO IS PROBABLY THE ONLY SOLUTION). F. AFTER BOTH PULSES 4 AND 30 ARE TRACKING PER FIGURE 5, READ THE DECADE BOXES (IF TRACKING ON 4 AND 30 ARE WIDELY DIFFERENT, A NEW TRANSFORMER

AND SELECT RESISTORS PER FOLLOWING TABLE AND SOLDER IN CARD.

FOR DECADE BOX #1 RES A IS RES ROL RES B IS R2 FOR DECADE BOX #2 RES A IS R5: RES B IS R6

| TOTAL OHMS | RES A | RES 8 | TOTAL OHMS | RES A | RES B |
|---------------|-------|--------------|---------------|-------|-------|
| OIPIO | | | | | |
| 20 | 10 | 10 | 3 6 4 | 182 | 182 |
| 61 | 51.1 | 10 | 403 | 221 | 182 |
| 102 | 51.1 | 51.1 | 4 4 2 | 221 | 221 |
| 133 | 82.5 | 51.1 | 474 | 392 | 82.5 |
| 165 | 82,5 | 82.5 | 495 | 274 | 221 |
| 203 | 121 | 82. 5 | 514 | 332 | 182 |
| 242 | 121 | 121 | 5 48 | 274 | 274 |
| 271 | 150 | 121 | · 574 | 392 | 182 |
| 300 | 150 | 150 | 606 | 332 | 274 |
| 332 | 182 | 150 | 613 | 392 | 221 |

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CONT ON SHEET FL. **SH NO.** 5 TITLE PULSE GENERATOR 68 A 9 8 9 7 7 3 TEST SPECIFICATIONS IC3600TPGA1 CONT ON SHEET FL. SH NO. FIRST MADE FOR + REVISIONS TOTAL RES A RES B OHMS 82.5 G. OPEN OR SET DECADE BOXES ON INFINITE AND RECHECK THAT TRACKING MEETS FIGURE 5 ON BOTH 4 AND 30 OVER RANGE OF +16V TO -11V, EXCEPT CHANGE TIME TO 80 µSEC. ON AN OCCASIONAL CARD, DUE TO RESISTOR TOLERANCES, IT MAY BE NECESSARY TO CHANGE A RESISTOR, USE RULES OF 3B AND 3C. 5 2 + P6A + PRINTS TO

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