

g GE Canada

Electronic Products Repair

Test Instructions for

0621L0461 G002

Device Number

Reversing Armature Interface Card

Description of Device

Originated By: Omar Zawistowski
Typed Name

Date: 5/19/1987
mm/dd/yyyy

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Approval Date: 5/19/1987
mm/dd/yyyy

TEST INSTRUCTIONS
PREVIOUS REVISION SHEET

0621L0461 G002

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TEST INSTRUCTIONS

gg

Location: Book or file File

Device Name Reversing Armature Interface Card

Device Number 0621L0461 G002

Date: January 8, 2013

Pg.: 3/11

1. PURPOSE:

- a. To test and repair the Reversing Armature Interface Card 0621L0461 G002

2. ELEMENTARY:

- a. None at this time

3. EQUIPMENT:

- a. Digital Multimeter
- b. Anatek Dual Regulated Power Supply
- c. Power supplies (+/-15VDC, +5VDC, +65VDC)
- d. Tektronix Oscilloscope
- e. 51 Pin Universal Test Jig
- f. 50 Pin Flat Ribbon Cable to 51 Pin Jig Converter Jig
- g. Variac
- h. Sencore Capacitor Tester
- i. Isolation Transformer TL #847 TM002 or equivalent
- j. 115VAC Isolated (2 phases of 3 phase connected to bench is O.K.)
- k. 3 Phase 115VAC
- l. 1 - Semi-Pack SCR or a 100 Ω , 5%, ½Watt resistor resistor for “loading”
- m. 1 – 20k Ω , 1%, ½W resistor 0177A1032 P022
- n. 1 – 200 Ω , 5%, 5W resistor 0177A1029 P021
- o. 3 – 1k Ω , 1%, ½W resistor 0177A1024 P026
- p. **Note: parts are in 621L0461 test bag**

4. SET UP:

- a. Measure 20 Ω with a DMM across R260 through R265
- b. Measure 0.5 μ F with a DMM or a capacitor tester across C120 through C125
- c. Connect 51 Pin Universal test Jig to bench power supplies, insert 50 Pin Flat Ribbon to 51 Pin Jig converter Jig in any slot and connect to JA on the card.
- d. Connect +15VDC on JA25 and JA1
- e. Connect –15VDC on JA37
- f. Connect GND on JA31

TEST INSTRUCTIONS

g

Location: Book or file File

Device Name Reversing Armature Interface Card

Device Number 0621L0461 G002

Date: January 8, 2013

Pg.: 4/11

5. PROCEDURE:

- i. Power up ± 15 VDC
- ii. Measure +5VDC on C76

B. SCR FIRING CIRCUITS:

- i. Measure 0Ω between 115VA, J115V-1, J115V-3, J115V-5, J115V-7, J115V-9
- ii. Measure 0Ω between 115VB, J115V-2, J115V-4, J115V-6, J115V-8, J115V-10
- iii. Apply 115V to 115VA and 115VB
- iv. Measure ≈ 26 VDC across the positive and negative of the bridge rectifier for each circuit.
- v. Measure ≈ 8.2 VDC across the zener for each circuit
- vi. Load each circuit one at a time and pull the corresponding JA pin to GND.
- vii. Observe that the LED for that circuit is on.
- viii. Place 100 Ohm Resistor across G to K (P1F) and Measure ≈ 12 VVDC across the gate and cathode of the SCR. For each Firing circuit as in Chart below.
(if using 100 ohm load see note and skip steps ix, x, xi). Do Not keep AC input to I/P too Long Transformers will over heat.
- ix. If using Semi-pack SCR Measure @1.6V across the gate and cathode of SCR.
- x. Measure ≈ 12 VDC across the positive and negative of the bridge rectifier for each circuit.
- xi. Measure ≈ 6.5 VDC across the zener for each circuit.

Circuit	I/P	AC I/P	G	K	ZA	XFM R	LED
P1F	JA2	J115V-1/J115V-2	1	2	Z1	T1	LED1
N1F	JA 4	J115V-1/J115V-2	1	2	Z3	T3	LED3
P2F	JA 6	J115V-1/J115V-2	1	2	Z5	T5	LED5
N2F	JA 8	J115V-1/J115V-2	1	2	Z7	T7	LED7
P3F	JA10	J115V-1/J115V-2	1	2	Z9	T8	LED9
N3F	JA12	J115V-1/J115V-2	1	2	Z11	T11	LED11
P1R	JA14	J115V-1/J115V-2	1	2	Z4	T4	LED4
N1R	JA16	J115V-1/J115V-2	1	2	Z2	T2	LED2
P2R	JA18	J115V-1/J115V-2	1	2	Z8	T8	LED8
N2R	JA20	J115V-1/J115V-2	1	2	Z6	T6	LED6
P3R	JA22	J115V-1/J115V-2	1	2	Z12	T12	LED12
N3R	JA24	J115V-1/J115V-2	1	2	Z10	T10	LED10

- xii. Disconnect AC at 115A and 115B before proceeding to next step.

TEST INSTRUCTIONS

89

Location: Book or file File

Device Name Reversing Armature Interface Card

Device Number 0621L0461 G002

Date: January 8, 2013

Pg.: 5/11

c. CONDUCTION STATE SENSORS:

- i. Using the DMM, measure the comparator thresholds by measuring across C80 and C81 should read +0.54 and -0.54 respectively +/- 2%. Monitor the outputs as stated with respect to COM, with the following inputs as according to this chart. NOTE: Input and Output Values are +/-5% and jumpers are removed.
- ii. DC Power Supply set for @70.3VDC connect + lead to CP49 (input Voltage) and -(com) to Acom CP91 as to cart below monitor CP49 DMV and check voltages at C50 for @49V and CP51 for @29V. At Sensor TP voltage will be at 5V (STATE1) when 70.3V is off or Voltage goes below @68V into CP49 and -0.61V (State2) when 70.3V is applied to input High CP49. You can vary Power Supply below 70.3V to change State or remove Input Voltage.
- iii. Continual test by changing input voltage to CP52, CP52, CP58, CP61 follow Cart below for test point voltages.

INPUT VOLTAGE POINTS			INPUT VOLTAGE		OUPUT VOLTAGE		SENSOR
<i>HIGH</i>	<i>MED</i>	<i>LOW</i>	<i>STATE1</i>	<i>STATE2</i>	<i>STATE I</i>	<i>STATE2</i>	<i>TP</i>
CP49			0V	~70.3V	5V	-0.61V	1,2,3
	CP50		0V	~49V	5V	-0.61V	1,2,3
		CP51	0V	~29V	5V	-0.61V	1,2,3
CP52			0V	~70.3V	5V	-0.61V	1,4
	CP53		0V	~49V	5V	-0.61V	1,4
		CP54	0V	~29V	5V	-0.61V	1,4
CP55			0V	~70.3V	5V	-0.61V	2,5
	CP56		0V	~49VV	5V	-0.61V	2,5
		CP57	0V	~29V	5V	-0.61V	2,5
CP58			0V	~70.3V	5V	-0.61V	3,6
	CP59		0V	~49V	5V	-0.61V	3,6
		CP60	0V	~29V	5V	-0.61V	3,6
CP61			0V	~70.3V	5V	-0.61V	4,5,6
	CP62		0V	~49V	5V	-0.61V	4,5,6
		CP63	0V	~29V	5V	-0.61V	4,5,6

TEST INSTRUCTIONS

09

Location: Book or file File

Device Name Reversing Armature Interface Card

Device Number 0621L0461 G002

Date: January 8, 2013

Pg.: 6/11

d. MOTOR VOLTAGE FEEDBACK:

- i. Apply +50.00VDC to CP66, with Digital multimeter, read U8 pin 1, should read -.984 VDC +/- 2%. Install the 20k Ω resistor between SC5 and SC6 and again apply +50.00VDC to CP66, measure JA41 with digital multimeter, and observe +1.968VDC +/-2%. Measure the same voltage again at TP15, JA42 and JA49. Now apply the +50.00VDC input to CP69. Observe the same voltages as above but with the opposite polarity except for TP15 and JA42. The voltage at TP15 and JA42 should always be positive because of the absolute value circuit.

e. BRIDGE VOLTAGE FEEDBACK:

- i. Apply +50.00VDC to CP51 and COM to CP63. Observe -.984 VDC on U12 pin7 with digital multimeter. Install the 20k Ω resistor between SC1 and SC2 and again apply the +50.00VDC to CP51 as before. Observe +1.968VDC +/- 2% at the following points: SC2, TP14, JA40 and CP92. Now move the +50.00VDC to CP63 and COM to CP51 and observe the same as above but with opposite polarity.

TEST INSTRUCTIONS

gg

Location: Book or file File

Device Name Reversing Armature Interface Card
Device Number 0621L0461 G002
Date: January 8, 2013

Pg.: 7/11

f. LINE VOLTAGE FEEDBACK:

- i. Attach 3Ø 115VAC to circuit card as follows: Line 1 to CP54, Line 2 to CP57 and Line 3 to CP60. Apply power and observe that NL1 and NL2 are illuminated. Observe TP8 and TP9 with oscilloscope and observe the following waveform (FIGURE 1). TP9 may be slightly higher; both have the same amplitude of approximately 6.8Vp-p to 6.9Vp-p. Also observe the same waveforms on JA45 and JA46.

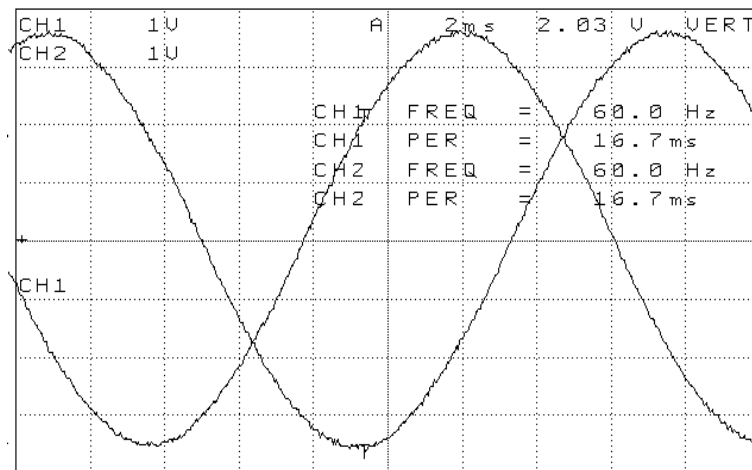


Figure 1.

Note; Use 240AC Variac and Fuse board in Parts Bag.

3 Phase: Line 115V to CP54, Line2 115V to CP57, Line3 115V to CP60, N or Com to CP91 on board.

TEST INSTRUCTIONS

gg

Location: Book or file File

Device Name Reversing Armature Interface Card
Device Number 0621L0461 G002
Date: January 8, 2013

Pg.: 8/11

G. CONDUCTION STATE SENSORS PART II:

- i. With 30 still connected apply power and observe TP1 with channel 1 and TP2 with channel 2 of the oscilloscope and observe the waveforms in FIGURE 2. The waveform present on TP1 will also be on TP4 and the waveform on TP2 will also be on TP5. Now move Channel 2 from TP2 and place on TP3, observe the waveforms of FIGURE 3. The waveform present on TP3 will also be present on TP6.

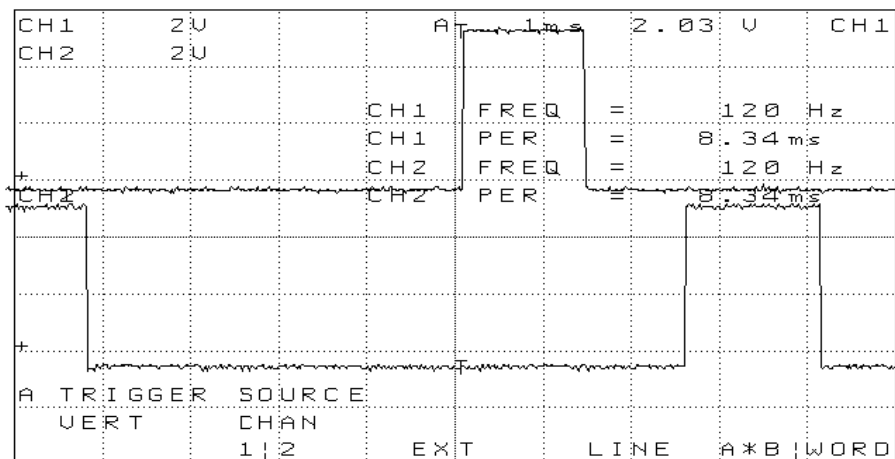


Figure 2.

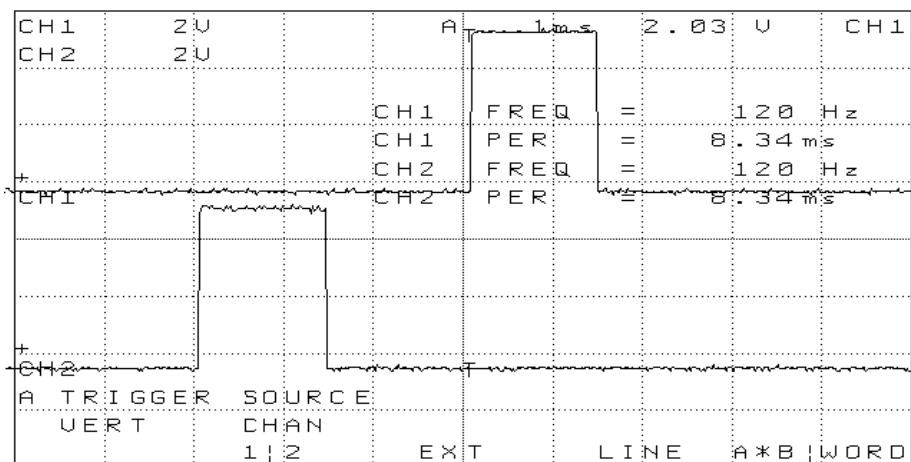


Figure 3.

TEST INSTRUCTIONS

gg

Location: Book or file File

Device Name Reversing Armature Interface Card
Device Number 0621L0461 G002
Date: January 8, 2013

Pg.: 9/11

h. PHASE LOCKED LOOP INPUT FILTER:

- i. With 3 phase still connected apply power and observe TP12 with channel 1, TP13 with channel 2 and observe the waveforms in FIGURE 4. The square wave on TP13 should be from -0.6 VDC and +5.0VDC, this waveform will also be present on JA38.

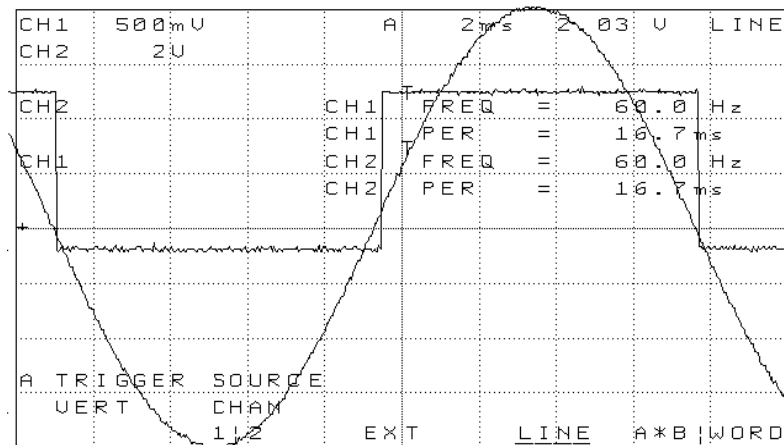


Figure 4.

i. SPARE METER DRIVER:

- i. Using the Diode Test function on the digital multimeter, observe 0.740 when forward biased and infinity when reverse biased across Z13 and Z14. Check for continuity between CP89 and SC11; CP90 and SC12; and CP91 and COM.

j. TEMPERATURE SENSORS:

- i. Using the 1k Ω resistors connect one resistor between JTEMPS1 and JTEMPS2, one resistor between JTEMPS3 and JTEMPS4, and one resistor between JTEMPS5 and JTEMPS6 make sure none of the leads are touching one another. Observe TP7 with digital multimeter, should read +1.9VDC +/- 5%. Attach JTEMPS2, with resistor still connected to +5VDC and TP7 should go to +5VDC as well. Repeat this for JTEMPS4 and then JTEMPS6 with the same results.

TEST INSTRUCTIONS

89

Location: Book or file File

Device Name Reversing Armature Interface Card
Device Number 0621L0461 G002
Date: January 8, 2013

Pg.: 10/11

k. MOTOR CURRENT FEEDBACK:

- I. With CP76 jumpered to CP77 (COM), observe that CP88, TP11, JA47 and JA48 are at 0VDC with digital multimeter. Remove jumper and apply 10VACp-p (3.5VAC on DVM) to CP76 with respect to COM with variac and isolation transformer. Observe JA47 and CP88 with oscilloscope, should see the input waveform on these points. Observe JA48 and TP11 with oscilloscope, should see a 5Vp, fully rectified waveform. Replace factory jumper CP76 - CP77 after test.

l. BRIDGE CURRENT FEEDBACK:

- i. Connect variac to isolation transformer and set so that the secondary of the isolation transformer is 35V P-P (12.36VAC on DVM). Connect the secondary leads to JACCT1 and JACCT2. Install the 200 Ω , 5W resistor between SC7 and SC8. Apply power and observe 15V peak full wave rectification at SC7 and observe squared off rectification at TP16 with channel 1 and channel 2 respectively of oscilloscope (see FIGURE 5.). Observe TP16 waveform at CP93, now touch JA50 to COM and observe that the waveform at CP93 inverts. Repeat the above for the secondary leads connected to JACCT3 & JACCT4 and then again for JACCT5 & JACCT6. Check for continuity between CP80 & SC10 and CP81 & COM.

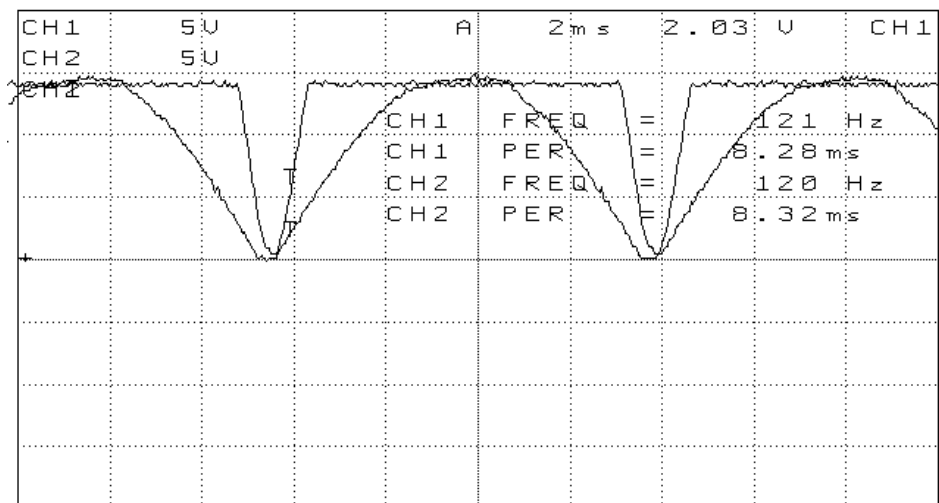


Figure 5.

TEST INSTRUCTIONS

gg

Location: Book or file File

Device Name Reversing Armature Interface Card

Device Number 0621L0461 G002

Date: January 8, 2013

Pg.: 11/11

6. UPGRADES:

- a. Rev0 to Rev1
 - a. Add a jumper from CP90 to CP95.
- b. Rev1 to Rev2
 - a. Replace R260 to R265 with 0177A1504P083. **(Do not do will change again).**
- c. Rev2 to Rev3
 - a. Add 0177A1127P037 for TP17 and TP18. qty change from 59 to 61.
 - b. Add 0177A1235P002 R282.
- d. Rev3 to Rev4
 - a. Change R80-R91 to 0177A1001P049
 - b. Change C62 to 0177A1283P009(0.01 μ F 50V).
 - c. Change R145 to 0177A1460P385(475k Ω \pm 1% 1/8W).
 - d. Remount C62 at U4-pin4 (Acom) to C62 mounting hole at U4-pin6. **(Do not do will change again).**
- e. Rev4 to Rev5
 - a. Add 0186B6379DLP002 insulator.
 - b. Add 0186B6379DLP001 metal shield.
 - c. Remount C62-pin1 to U4-pin6, and C62-pin2 to R34-pin2 (Acom).
- f. G1 Rev5 to G2 Rev0
 - a. Remove R260 to R265.
 - b. Remount per 0233B3975
 - c. Using 0293A1816P001
 - d. Change R260 to R264 to 0177A1504P053.

7. END.