

2 7 8 A 3 0 6 6

CONT ON SHEET 2 SH NO. 1

REV  
NO.

2 7 8 A 3 0 6 6

CONT ON SHEET 2 SH NO. 1

TITLE

TEST SPECIFICATIONS

FIRST MADE FOR 44C372689-G01

REVISIONS

STANDING INSTRUCTIONS

For

AUTO REGULATOR

PRINTED CIRCUIT BOARD

For

GENERREX SYSTEM

44C372689-G01

Dist:

1 QC Test  
1 QC Engr  
1 Engr

3EL1

4QA3

1RA2

4EK1

DL13

PRINTS TO

MADE BY

R.K.Gerlitz 790605

ISSUED

6/7/79

APPROVALS

*WdL*

Drive System

Salem, Va.

DIV OR  
DEPT.

LOCATION

2 7 8 A 3 0 6 6

CONT ON SHEET 2 SH NO. 1

CODE IDENT NO.

REV NO.  2 7 8 A 3 0 6 6 CONT ON SHEET 3 SH NO. 2	TITLE TEST SPECIFICATIONS FIRST MADE FOR 44C372689-G01
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## I. Test Equipment Required

- A. Printed Circuit Board Test Setup - 44C931365
- B. Adaptor - Amp. Mod. II 30 pin
- C. Power Supply Cable
- D. Patchboard PB-8
- E. Drawings 44C308257 Elementary Rev. 0  
                   44C372689 Assembly  
                   44C931365 Elementary of Test Table

## II. Connection

- A. Connect the adaptor cable to "PL1" on the Universal Tester (U.T.).
- B. Connect the Amp. Mod. II Power Supply cable to "PL1" on Universal Power Tester (U.T.) and to Power Supplies per marking.
- C. Insert Patchboard PB-8 in carrier of Universal Tester and close.
- D. Connect a DC digital voltmeter to "BJ-1". Red (+) and black (-).
- E. Connect a DC digital voltmeter to "BJ-11". Red (+) and black (-).

## III. Wire Check

Pin	To	Test Point	Resistance (Ohms)
15		9 TP	9.5K to 10.5K
16		9 TP	9.5K to 10.5K
12		9 TP	31.3 to 34.7

### Visual Check

	Resistance (Ohms)	Adjust 1P	CCW
41R	5.6K	2P	CCW
48R	15K	3P	CCW
52R	100K	6P	CCW
42R	51K	7P	CCW
44R	51K	8P	CCW
46R	51K		
50R	5.6K		
53R	5.1K		
54R	5.1K		
55R	91K		
56R	2.7K		
57R	6.8K		

## REVISIONS

1) BU941NX DGJ 830308

3EL1

4QA3

1RA2

4EK1

DL13

## PRINTS TO

MADE BY R.K.Gerlitz 790605 ISSUED 6/7/79	APPROVALS 	Drive Systems Salem, Va.	DIV OR DEPT. LOCATION 2 7 8 A 3 0 6 6 CONT ON SHEET 3 SH NO. 2
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REV NO.  2 7 8 A 3 0 6 6 CONT ON SHEET 4 SH NO. 3	TITLE TEST SPECIFICATIONS FIRST MADE FOR 44C372689-G01
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## IV. Setup *SEE SECTION 3 PRESET POTS*

- A. Turn all switches to OFF or NORMAL on both the UT and UPS.
- B. Turn all power supplies to zero on the UT.
- C. Apply power to test stand.
- D. Insert board under test (B.U.T.) into test adaptor.

## V. Electrical Test

*MAIN POWER*

- A. Close "SW-1". Depress "LPB-1" and adjust power supply #1 (PS-1) to  $24 \pm 0.2$  VDC at "BJ-1". *(READ DC PWR AT TABS BT010)*  
 Note: Should it become necessary to remove all power, open "SW-1".  
*(CONNECTS +15V TO PATCH BOARD TO CARD BEING TESTED)*

- B. Close "SW-10", depress "LPB-2" and adjust PS-2 to  $15 \pm 0.005$  VDC at "BJ-1". Measure the D.C. voltage 1VR pin 2 (+) to OTP (-) =  $10 \pm 0.5$  VDC. *TABS 244 METER FOR +15V*

- C. Depress "LPB-3" and adjust PS-3 to  $15 \pm 0.005$  VDC at "BJ-1".  
 1TP (+) to OTP (-) =  $2.85 \pm 0.2$  VDC. *TABS 6704 METER FOR -15*  
 Measure the D.C. voltage OTP (+) to anode 3ZD (-) =  $10 \pm 0.5$  VDC.

- D. Adjust 2P CW.  
 1TP (+) to OTP (-) = 6.33 to 7.00 VDC. *6.64*

- E. Close "SW-29". *CONNECT PIN 29 TO PIN 5*  
 Adjust 2P for  $3.75 \pm 0.005$  VDC, 1TP (+) to OTP (-).  
 2TP = Neg saturation ( $< -12$  VDC). *-12.78*  
 Close "SW-2" CLOSING 2RL RELAY SHEET 9A (CONNECTS PS 5 TO SW 17TH CIRCUIT)

- F. Open "SW-29". *OPEN 29 TO 5.*  
 Place "RS2" to position 2 *METER IS HOOKED + TO PIN 30 - TO PIN 3*  
 Close "SW-9". *(PS-5 negative to pin 29). PS 5 - TO PIN 29 PS 5 + TO PIN 3*  
 Increase PS-5 until 1TP to OTP =  $1 \pm 0.005$  VDC  
 "BJ-11" =  $61.4 \pm 3.0$  VDC *-57.5 (-58.1)*

- G. Return PS-5 to zero.  
 Open "SW-9". *REMOVE DC FROM PIN 30 + PIN 3*  
 Close "SW-29". *CONNECT PIN 29 TO PIN 5*  
 Place "RS2" to position 1 *METER PIN 20 TO PIN 3*  
 Close "SW-19". *(Connect PS4 positive to pin 19). PS 4 + TO PIN 19 - TO PIN 3*  
 Increase PS4 until 1TP =  $7.5 \pm 0.005$  VDC  
 "BJ-11" =  $37.5 \pm 0.5$  VDC *37.6 38.5*  
 Open "SW-19". *REMOVE PS-4 FROM 19 + 3*  
 Place "SW-25" down. Place "RS2" to position 10. *PS 4 - TO 25 + TO 3 METER PIN 26*  
 "BJ-11" =  $37.5 \pm 0.5$  VDC. *-37.6 (-38.5)*  
 1TP =  $1.87 \pm 0.1$  VDC. *1.873 (1.724)*

### REVISIONS

2. ECR860409-CD02  
RV - 5/20/86

1. BU116030P  
ML800627

3EL1  
4QA3 6  
1RA2  
4EK1  
DL13 3

### PRINTS TO

MADE BY R.K.Gerlitz 790605 ISSUED 6/7/79	APPROVALS WLL	Drive Systems Salem, Va.	DIV OR DEPT. LOCATION	2 7 8 A 3 0 6 6 CONT ON SHEET 4 SH NO. 3
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REV NO.	TITLE
2 7 8 A 3 0 6 6	TEST SPECIFICATIONS
CONT ON SHEET 5 SH NO. 4	FIRST MADE FOR 44C372689-G01

- H. Return PS-4 to zero.  
 Open "SW-25" REMOVE PS4 FROM 25 + 3  
 Place "SW-19" down. (Connect PS-4 negative to pin 21) PS4 - TO PIN 21 + TO 3  
 Place "RS2" to position 6 METER PIN 22 + PIN 3  
 Increase PS4 until 2TP to OTP = 0 to 0.005 VDC.  
 "BJ-11" =  $-37.5 \pm 0.5$  VDC -37.94 (-37.0)
- I. Increase PS4 until 2TP (+) to OTP (-) =  $5 \pm 0.005$  VDC  
 "BJ-11" =  $42.5 \pm 0.5$  VDC -43.1 (-42.3)  
 Return PS4 to zero.  
 2TP (+) to OTP (-) = Neg. saturation ( $< -12$  VDC) -12.79
- J. Open "SW-19" REMOVE PS4 - FROM PIN 21  
 Close "SW-3". (Connects pin 27 to +15V) PIN 27 TO +15V (PIN 1)  
 2TP (+) to OTP (-) =  $12.5 \pm 0.5$  VDC 12.45 (11.42)  
 Place "SW-23" down. (Connects PS4 negative to pin 23) PS4 - TO PIN 23 + TO 3  
 Place "RS2" to position 7 METER PIN 24 TO PIN 3  
 Increase PS4 until 2TP (+) to OTP (-) =  $10 \pm 0.005$  VDC  
 "BJ-11" =  $-10 \pm 0.1$  VDC -10.00  
 3TP (+) to OTP (-) =  $10 \pm 0.1$  VDC +10.00
- K. Return PS4 to zero  
 Open "SW-23" REMOVE - PS4 FROM PIN 23  
 Open "SW-3" REMOVE PIN 27 FROM PIN 1  
 Adjust 2P full clockwise BIAS ADJ  
 Open "SW-29" REMOVE PIN 23 TO 10 ADD PIN 29 TO PIN 10 THRU 2.6 K RESISTOR  
 1TP (+) to OTP (-) = 6.33 to 7.00 VDC 6.67 (6.64)
- L. Close "SW-27" + PS4 TO PIN 27 - TO PIN 3  
 Place "RS2" to position 8 METER PIN 28 TO PIN 3  
 Adjust PS4 for  $10 \pm 0.005$  VDC at "BJ-11"  
 Adjust 3P  
 CW 1TP (+) to OTP (-) VDC  
 CCW -11  $\pm 0.3$  -10.98 (-11.2)  
 Set -9  $\pm 0.3$  -9.09 (-9.16)  
 -10  $\pm 0.05$  -10.00 SET
- M. Place "SW-27" down - PS4 TO PIN 27 + TO PIN 3  
 1TP (+) to OTP (-) = 6.33 to 7.00 VDC 6.67v (6.64v)  
 Return PS4 to zero  
 Open "SW-27" REMOVE PS4 FROM 27 + 3
- N. Place "SW-13" down + PS4 TO PIN 3 - PS4 TO PIN 13  
 Place "RS2" to position 4 METER PIN 14 TO PIN 3  
 Increase PS4 until 3TP (+) to OTP (-) =  $0 \pm 0.1$  VDC  
 "BJ-11" =  $-7.5 \pm 0.15$  VDC -7.51v (-7.55)

REVISIONS

1. ECR860409-CD02  
 RV - 5/20/86

3EL1  
 4QA3 6  
 1RA2  
 4EK1  
 DL13 3

PRINTS TO

MADE BY R.K.Gerlitz 790605	APPROVALS WdL	Drive Systems	DIV OR DEPT.	2 7 8 A 3 0 6 6
ISSUED 6/7/79		Salem, Va.	LOCATION	CONT ON SHEET 5 SH NO. 4

REV NO.

TITLE

2 7 8 A 3 0 6 6

CONT ON SHEET 6 SH NO. 5

TEST SPECIFICATIONS

FIRST MADE FOR 44C372689-G01

REVISIONS

O. Adjust PS4 until 3TP (+) to OTP (-) =  $-2 \pm 0.005$  VDC

"BJ-11" =  $-7.4 \pm 0.15$  VDC  $-7.31$  ( $-7.34$ )

Adjust 4P 4TP (+) to OTP (-) VDC GAIN CONTROL

CCW  $-2 \pm 0.05$   $-2.02$  ( $-2.05$ )

Set  $-1.0 \pm 0.005$  VDC  $-1.000$

5P must be CCW With 6P fully CCW, 7TP should be  $+1.9 \pm .01$  V.  $1.95$

With 6P fully CW, 7TP should be  $+5.00 \pm .02$  V.  $5.05$  CREEPS UP TO THIS

Then adjust 6P back fully CCW.

Adjust 5P for  $.5 \pm 0.005$  VDC 5TP (+) to OTP (-). LEAD CONTROL

If oscillations appear at 6TP adjust 6P slightly CW until oscillations disappear.

P. Adjust PS4 until 6TP (+) to OTP (-) =  $2.5 \pm 0.05$  VDC

"BJ-11" =  $-7.3 \pm 0.15$  VDC.  $-7.31$

8TP (+) to OTP (-) =  $-5 \pm 0.1$  VDC  $-4.86$

9TP (+) to OTP (-) =  $10 \pm 0.3$  VDC  $9.7$

Q. Adjust PS4 for 3TP (+) to OTP (-) =  $0 \pm 0.01$  VDC

Connect an oscilloscope to 9TP to OTP

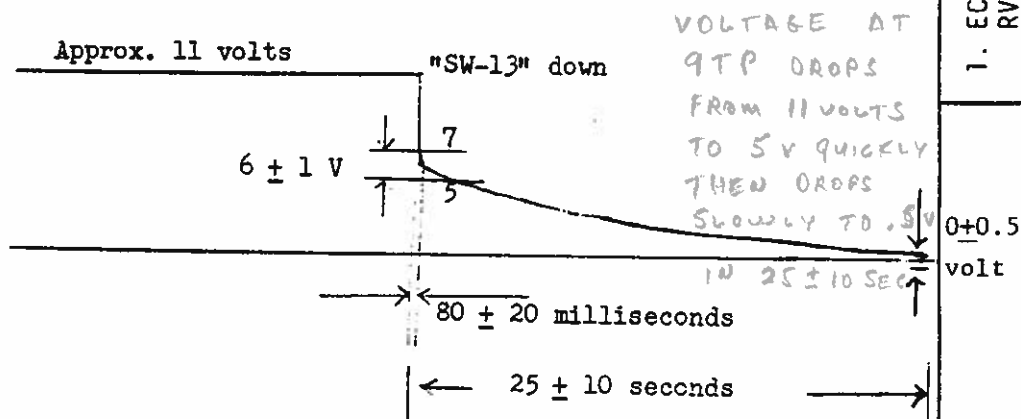
Open "SW-13" REMOVE PS4 FROM PIN 13 9TP GOES TO 11V

R. Place "SW-13" down - PS4 TO PIN 13

Adjust 6P fully CW

Allow trace to stabilize then open "SW-13"

Wave form shall appear as follows:



S. Place "SW-13" down - PS4 TO PIN 13

Increase PS4 to  $-10 \pm 0.005$  VDC at "BJ-11" PIN 14

Adjust 7P for 9TP (+) to OTP (-) =  $-4.85 \pm 0.2$  VDC  $-4.85$

Open "SW-13"

Adjust 8P for 9TP (+) to OTP (-) =  $7.9 \pm 0.2$  VDC  $7.90$

1. ECR860409-CD02  
RV - 5/20/86

0 ± 0.5 volt

3EL1

4QA3<sup>6</sup>

1RA2

4EK1

DL13<sup>3</sup>

PRINTS TO

MADE BY R.K.Gerlitz 790605

APPROVALS

Drive Systems

DIV OR DEPT.

ISSUED

6/7/79

WdL

Salem, Va.

LOCATION

2 7 8 A 3 0 6 6

CONT ON SHEET 6

SH NO. 5

CODE IDENT NO.

REV  
NO.  
2 7 8 A 3 0 6 6  
CONT ON SHEET FL SH NO. 6

TITLE  
TEST SPECIFICATIONS  
FIRST MADE FOR 44C372689-G01

REVISIONS

- T. Place "SW-13" down -PS 4 TO PIN 13  
Adjust PS4 for 0 + 0.05 VDC 9 TP to OTP  
Adjust 6P fully CCW then turn slightly CW.  
(Note this will cease oscillations at 9TP if they exist)
- U. Close "SW-21." + PS5 TO PIN 17, -PS5 TO PIN 3  
Place "RS2" to position 9 METER PIN 18 TO PIN 3  
Increase PS5 until 9TP (+) to OTP (-) = +2.5 ± 0.005 VDC  
above the 9TP reading in step "T".  
"BJ-11" = +0.3 ± 0.1 VDC .305  
Adjust 6P midpoint.
- V. Open "SW-1," then open or return to Normal all remain-  
ing switches. Turn all power supplies to zero.

1 BUL16030P M1.800627  
2-ECR860409-C002-RV-5/20/85

3E1.

4QA36

1RA2

4EK1

DL133

PRINTS TO

MADE BY  
R. K. Gerlitz 790605  
ISSUED  
6/7/79

REAPPROVALS  
WJL

Drive Systems  
Salem, VA USA

DIV OR  
DEPT.

LOCATION

2 7 8 A 3 0 6 6

CONT ON SHEET FL SH NO. 6