

G3 +2.25 +0.07 VDC.



5.0 GAIN CHECK (Cont). 5.13 SET R88 FULL CCW, READ TP8 TO TP11: G1 -2.67 +0.1 VDC. VDC VDC G2 -5.22 +0.15 VDC. G3 -2.25 +0.07 VDC. SET R88 FOR 0.0 +0.01 VDC AT TP8 TO TP11. 5.15 APPLY 1.00 VDC TO PIN 6, READ TP8 TO TP11: VDC GI = +5.93 +0.15 VDCVDC G2 = +11.60 + 0.35 VDCVDC $G3 = -5.00 + \overline{0}.15 \text{ VDC}.$ 5.16 SET INPUT AT PIN 6 TO 0.00 VDC. SET TRANSDUCER FOR +1.00 + 0.01 VDC AT TP6 TO TP11. READ TP8 TO TP11: 5.17 VDC G1 = -5.91 + 0.15 VDCVDC G2 = -11.56 + 0.35 VDCG3 = -4.98 + 0.15 VDCVDC OUTPUT CURRENT CHECK 6.0 SET TRANSDUCER FOR 0.00 ±0.01 VDC AT TP8 TO TP11. 6.I 6.2 NOTE M1 READS 0.0 +1 MA. 6.3 SET INPUT FOR +5.00 +0.01 VDC AT TP8 TO TP11. Dependingon Gr VDC READ PIN 28 TO TP11, -1.4 +0.1 VDC. **16.**4 See 1280389 VDC 6.5 - READ PIN 24 TO TP11, -6.2 +0.2 VDC. Sh. 5 For Resistor Across Plu 28424 NOTE MI READS -37 +1 MA. 6.6 VP-I 6.7 READ PIN 24 TO TP11 LESS THAN 20 MV P-P. RESISTOR INSTALLED is for G1. Put parallel Recistor in Pin 28 TO 287 6.8 SET INPUT FOR -5.00 +0.01 VDC AT TP8 TO TP11. VDC. 6.9 READ PIN 28 TO TP11, +1.4 +0.1 VDC. For 62 4 62 VDC READ PIN 24 TO TP11, +6.2 +0.2 VDC. 6.10 NOTE M1 READS +37 +1 MA. 6.11 VP-I READ PIN 24 TO TP11, LESS THAN 20 MV P-P. 6.12 7.0 FINAL ADJUSTMENT 7.1 SET TRANSDUCER AT TOP STOP, SET INPUT AT PIN 6 FOR -5.00 +0.01 VDC. VD(TP5 MUST BE +5.00 +.1 VDC. 7.2 ADJUST R86 CCW FOR 0.0 MA ON MI. 7.3 SET TRANSDUCER AT BOT. STOP, SET INPUT AT PIN 6 TO 0.00 VDC. 7.4 ADJUST R67 FOR 0.0 MA ON MI. 7.5 ADJUST R91 FOR 0% ON M2. VD 7.6 READ PIN 35 TO TP11, 0.0 +0.2 VDC. 7.7 SET TRANSDUCER AT TOP STOP, ADJUST R84 FOR 100% ON M2. VD 7.8 READ PIN 35 TO TP11, +5.00 +0.1 VDC.

