P3K-AL-0701-A01

CONT ON SHELT 2 5

10£2- TITLE

TEST INSTRUCTIONS - COMB. VLV. LOGIC

P3K-AL-0701-A01

1F2-D3 (CB Assembly 18609144)

CONTON SHEET 2 SH NO.]

FIRST MADE FOR EHC MARK ITA

Schematic # 148043275 Group 3 2 DALLY BENEV

I. DEFINITIONS

A. CB Powerup

This CB can only be replaced while the unit is either unpowered or tripped; when the unit is powered-up, it assumes the tripped state.

B. Inputs

+

- 1. High \equiv "1" \equiv 24 \pm 1 VDC at specified CB pin. CB performance should be satisfactory for 23 \leq V input \leq 25 VDC.
- 2. Low \equiv "0" \equiv 0 VDC which is obtained by absence or removal of signal at specified pin.

C. Outputs

- 1. High Ξ "1" Ξ 24 \pm 1 VDC at CB pin.
- 2. Low Ξ "0" Ξ 0 to 0.2 VDC at CB pin.

SAT 12/2/92

II. PRELIMINARY

- \checkmark 1. Connect resistive loads to CB pins as in Figure 1. (470Ω to simulate industrial relays, 1300Ω for Hg-wetted).
- 2. Apply 24V return to pin 40.
- 3. Apply +24 VDC simultaneously to resistor loads and to pin 38.

4. Observe:

-a. DS5 is ON.

b. $4.75 \le V_{TP1} \le 5.25$.

NOTE: A blank entry into the Input/Output State Table assumes the previous entry made in that column.

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Steam Turbing | DIV OR | P3K-AL-0701-A01

NO CHC THIS ...

22.2. 2.2.5. NET 5.1 C

273-2 273-12

273-12

273-1

273-201 273-201

PRINTS 1

FIGURE 1: Preliminary Test Setup

J.Aulisi 9/3/82

Steam Turbine

DIV OR
P3K-AL-0701-A01

Schenectady, N.Y. Location cont on Sheer 3 Sh No 2

PRINTS

ONT ON SMEET 4 IN NO. 3 FIRST MADE FOR EIIC MARK IIA DO PAL IVATS RV10S RV20S IVATS PAL IVATS PAL IVATS PAL IVATS PAL IVATS PAL IVATS PAL IVATS IVATS PAL IVATS IVA	T	TITLE TEST INSTRUCTIONS - 1F2-D3 (CB Assembl	COMB. VLV y 186C8144	CONT ON SHEET	14 SH NO 3
PROSE SE S	NT ON SHEET 4 SH NO. 3	FIRST MADE FOR EHC	ARK IIA		4 50
SV1 T,F IV1FAS T,F W W W W W W W W W W W W W W W W W W W			0 0 0 0 0 0 1 1 1 1 1 1	IVITS RV10S RV20S RV20S IV2TS PB2 IV20SHP IV10SHP IV10SH	21 33 25 7 13 29 10 2 36 34 1
DS DS 1 2 NEMARKS 1 2 Output State Independent of Input t = (sec.) T = Test F = Fast Close W = Warming SE = Speed Zrror PB = Test Button Sel. Spd. Sel. Vive. Cl. Trip Res. Sel Vive. Cl. IVOSH Op. Sel Vive. Sel Vive. Cl. IVOSH Op. Sel Vive. Sel Vive. Sel Vive. Sel Vive. Sel Vive. Sel Vive. IVOSH Op. Sel Vive. Sel V	Ho	Trip Rcs. IVOSH Op. Sel Vlv Cl. Trip Trip	1 1 1 0 0 0 Power up. Res. Sel. Spd. Sel. Vive.	IVIFAS T,F RVITS/FAS T,W W RV2TS/FAS T,W H IV2FAS T,F N SV2 T,F FH/Ana SE SE,T SE,T	DS D

432.	GENERAL (1)	PELECINIC	PBK-AL-(1701-A)	()] -\$H_N
P3K-AL-0701-A01	TEST INSTRUCTIONS - 1F2-D3 (CB Assembl			
NT ON SHEET 5 24 30. "	FIRST MADE FOR EIIC	EVACTION COD		
Tampering	WZ Teat	. (cont.)		
X OIL	01	PB1	pr ===	_
· ·	(n)	LV1TS RV10S	= कि	
		O RV20S	7 2 4	
2 5	H H	O IV2TS	2 2 2	
3 A 1 0 0	o	PB2	6 21/2 70/2	
40		o IVZOSH	A.a. anul	
		o se	PAST CL.	
3.		r o IVHS		
Respo	0	L O IVOSH	SEQ.	
7		o SB	₩ 2 ×	
		o T/H	25	
Po	· · ·	o - SV1	T.F Z C	
· · · · · · · · · · · · · · · · · · ·	<u> </u>	► IV1FAS	<u>*</u>	
O' F.O	F 0	₩ RVITS		
	Ti o ×××	→ IV2FA	77 W. w	
рно н <u>-</u>	•	F N SV2	T,F CH	
		O FH/An	SE 22	
o'x	0 ×	0	SE,T - 5	
О×	o××	•	SE,T ∾ 🖔	_
RV20S Op E 0.43 E>9.45, IV2IS Op; **Kinit. Cond.(\$2b) See P.6 Frip Push P81 Re1. P81; t>0.35 Reset Reset Sel. Was.	IVITS C Rel. PB RVITS O C>3.35 See P.6 V40 - I Push PB IV2TS C RV2OS C RR2OS C	Rel.	+r	
Trip Push PB1: Rel. PB2: Reset PB2: Sel. Wmg.	IVITS C1. (#1) See P.6 EVITS C1. Rel. PB1 RVITS Op. r< 0.35 ** L>).35 (#2a) Init. Cond. See P.6 V40 - Init. Cond. Push PB2 IV2TS C1. RV2OS C1. Rel PB2 ** ** ** ** ** ** ** ** **	H 0 (****		
V2IS Op: cod.(#2b)	(A1) t < 0 2a) 1 t. Co	Fast Close Warming Speed Error Test Button PB1, Push PB1 SHP Not Op. SHP Not Op. PB2, Push PB1	REWARKS US lit US lit Output State Independent of Input (sec.)	
45* ***	* 1 APPROVALS * * *	Close id Error Button Button Push PB2; Push PB1;	Gare	
United in the	APPROVALS #6 7F 6	rbine Div of	P3K-AL-0701-A	01

0701-A01	TITLE TEST INSTRUCTIONS	COMB 191 **	CONF ON SHEET	7 6 SH NO.
0701-A01	1 1117 57 / 77 .		:IC	
	1F2-D3 (CB Assemb	ly 186C8144)		
SH NO. I. Speed Erro	FIRST MADE FOR EHC M	ARK IIA F: (cont.) 9.		:
	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	PLU O SE	OS IN SOCIETY OF THE PROPERTY	1 2 3 4 3 6 8 9 1 8 1 18 18 18 18 18 18 18 18 18 18 18
9000	0 0,	O T/H	T,P	S 8 E 15 15 DS
0 0 P	0 X	→ V3 RV2	TS/FAS T,W TS/FAS T,W FAS T,F	200 200 100 200 100 200 100 200 100 100
NA O		O FIL	Ana SE SE,T	\$ 227 - DS .
O K'X		•	SE,T	~8
VOSH OP-	Sel. SB. Off Sel. Wmg. Off Sel. Spd WWO Apply PLU/EVA Rel. PLU, IVOSM C1; C 0.35 % 0.35 % C 4 7.45 %	Speed Teach	# DS Linder	REMARKS
	Error 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	VA O O O O O O O O O O O O O	By 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	## 1

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P3K-AL-070L-A01

CONT ON SHEEF 7

CONT ON SHEET 7

TITLE TEST INSTRUCTIONS - COMB. VLV. LOCIC 1F2-D3 (CB Assembly 186C8144)

P3K-AL-0701-A01

6 SH NO.

EHC MARK IIA FIRST MADE FOR

(continued) PRELIMINARY II.

REMARKS

- (#1) RVTS/RVFAS tested via SE circuits.
- (#2a) First adjust VR53 so that DS3 drops out at 350 < t < 400 ms after removal of 24V @ pin 9. Observe TP63 (IC3).
- (#2b) Then adjust VR54 so that t_{DS4} drop out = t_{DS3} + 100 ms (Nominal range 450 < t < 500 ms) after removal of 24V from pin 9. Observe TP64.
- Adjust VR51 (VR52) so that DS1 (DS2) picks up at 35 < t < 45 ms. after application of 24V to pin 2. Observe TP61 (TP62).

REVISION

2. 20 April 2 MAR 19 ..

PRINTS T

J.Aulisi 9/3/82

APPROVALS

Steam Turbine

P3K-AL-0701-A01

148 D 4327 Wireing in + PUT ALL P3K-AL-0701-A01 15 SWE TO O. MAY have TO Adj. POTS TO GET POLSE STAFTED ON SOME CITCUITE.

Timer Adjustments

5HL 8 3

To PIN 9 (SET To .5 HZ)

- Trig scope on reg. going edge of input

- Adjust R53 FOR A .40 sec. debay between -going input AND Pos. going outpu

AT Pin 15

- Adjust R54 FOR A .50 sec delay between - going input and pos. going output AT Pin 19

2 - Move SQ. WAVE imput to Pin 2 and increase.

Freq. To 5 HZ.

- Trig. on Pos going input

- Adj R52 FOR 50 MS de LAY AT TP 62 (Red)

- Adj RSI FOR SO MS delay AT TPGI (white

see Fig 2 54,2

5h Z of =

12 PIN 2 P0152 TP62 TP61