

REV NO. 01

TITLE

P3K-AL-0355 -A01

TEST INSTRUCTIONS FOR LOAD LIMIT AND LOAD SET RUNBACK
ANALOG LOGIC CIRCUIT BOARD 1L3-B001
FIRST MADE FOR (Ass'y Drawing 117D7726 G-1) O/C

CONT ON SHEET 2 SH NO. 1

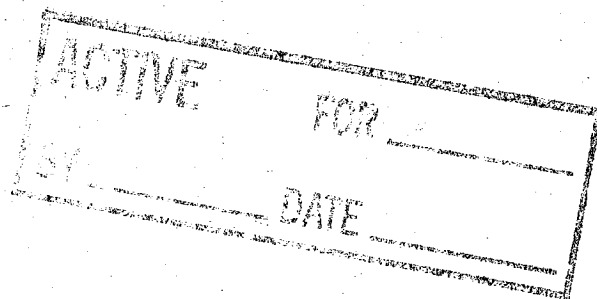
REVISIONS

I. CIRCUIT DESCRIPTION

This circuit is used to consolidate all of the analog to logic relays that provide switching signals to the Main Steam Pressure Limiter and the Load Limit and Load Set Runback Analog Circuits, except for FA/PA transfer relays. The board contains six (6) 24V mercury wetted relays.

II. CIRCUIT SPECIFICATIONS

1. Connect +24 VDC to pin 38 and ground to pin 40. A +24V should appear between TP1 and TP2. Connect 24V lamps to pins 4, 5, 6, 7, 8, 9, 10, 11, 12, and 14.
2. The logic and path continuity should be checked according to the following table:



1 0.077MAG APR 28 1976

No chg this y89

273-221

273-227

273-2

273-12^a

273-138

273-71^a

PRINTS TO

MADE BY D.Mone Oct. 28, 1974

APPROVALS

Steam Turbine
Schenectady, N.Y.

DIV OR DEPT.

P3K-AL-0355-A01

ISSUED OCT 28 1974

LOCATION CONT ON SHEET 2 SH NO. 1

REV NO. 01

TITLE

TEST INSTRUCTIONS FOR LOAD LIMIT AND LOAD SET RUNBACK
ANALOG LOGIC CIRCUIT BOARD 1L3-B001
FIRST MADE FOR (Ass'y Drawing 117D7726 G-1)

P3K-AL-0355 -A01

CONT ON SHEET 3 SH NO. 2

REVISIONS

INPUTS AT PINS

CORRECT OUTPUTS AT PINS

20	26	19	21	22	23	24	25		4	5	6	7	8	9	10	11	12	14
----	----	----	----	----	----	----	----	--	---	---	---	---	---	---	----	----	----	----

0	0	0	0	0	0	0	0		0	0	0	0	0	0	0	0	0	0
1	0	0	0	0	0	0	0		0	0	0	0	0	0	0	0	0	0
1	1	0	0	0	0	0	0		0	1	0	0	0	0	0	0	0	0
1	1	1	0	0	0	0	0		1	1	0	0	0	0	0	0	0	0
1	1	0	0	0	0	0	0		0	1	0	0	0	0	0	0	0	0
1	1	0	1	0	0	0	0		0	0	0	0	0	0	0	0	0	0
1	1	0	0	0	0	0	0		0	1	0	0	0	0	0	0	0	0
1	1	0	0	1	0	0	0		0	1	0	0	0	0	0	0	0	0
1	1	0	0	0	0	0	0		0	1	1	1	0	0	0	0	0	0
1	1	0	0	0	0	0	0		0	1	0	0	0	0	0	0	0	0
1	1	0	0	0	0	1	0		0	1	0	0	1	1	0	0	0	0
1	1	0	0	0	0	0	0		0	1	0	0	0	0	0	0	0	0
1	1	0	0	0	0	1	0		0	1	0	0	0	0	1	1	0	0
1	1	0	0	0	0	0	0		0	1	0	0	0	0	0	0	0	0
1	1	0	0	0	0	0	0		0	1	0	0	0	0	0	0	1	1
1	1	0	0	0	0	0	0		0	1	0	0	0	0	0	0	0	0
0	0	1	0	0	0	0	0		0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0		0	0	0	0	0	0	0	0	0	0

0: OV or lamp off

1: 24V or lamp on

1/04/76 APR 28 1976
1/04/76 APR 28 1976

1/04/76 APR 28 1976
1/04/76 APR 28 1976

PRINTS TO

MADE BY D. Mone Oct. 28, 1974

APPROVALS

Steam Turbine

DIV OR DEPT.

P3K-AL-0355 -A01

ISSUED OCT 28 1974

Schenectady, New York LOCATION

CONT ON SHEET 3 SH NO. 2

REV
NO. 1

TITLE

CONT ON SHEET 4

SH NO. 3

P3K-AL-0355-A01

TEST INSTRUCTION FOR LOAD LIMIT AND
LOAD SET RUNBACK.

CONT ON SHEET 4

SH NO. 3

FIRST MADE FOR

MK II

REVISIONS

TEST INSTRUCTIONS 1L3-B001

Assembly 117D7726
Schematic 117D7723

PROCEDURE:

- A. Refer to P3K-AL-0494-A02 for test circuits, equipment list, and set up instructions.
- B. Table A provides the step by step procedure to follow in testing this circuit.
- C. Table B provides a step by step description of input voltages (to use as a troubleshooting aid).

1 D. Mone APR 28 1974

Rev. 9 Schenectady

PRINTS TO

MADE BY D. Mone Oct. 28, 1974

APPROVALS

Steam Turbine

DIV OR
DEPT.

P3K-AL-0355-A01

Schenectady

LOCATION

CONT ON SHEET 4

SH NO. 3

[illegible]

CODE IDENT NO.

REV NO. 01
P3K-AL-0355 -A01
CONT ON SHEET --- SH NO. 6

TITLE
TEST INSTRUCTIONS FOR LOAD LIMIT AND LOAD SET RUNBACK
ANALOG CIRCUIT BOARD 1L3-B001
FIRST MADE FOR (Ass'y Drawing 117D7726 G-1)

REVISIONS

1 D. Mone - APR 28 1976
Sup. to Numbered - was sh. 7

PREPARED BY

D. Economou

DATE

4-17-1973

D. Economou
EHC DESIGN ENGINEERING

APPROVED BY

P.C. Callan

DATE

10-25-74

P.C. Callan - MANAGER
EHC DESIGN ENGINEERING

TEST PROCEDURE

PREPARED BY

C. Bugg

DATE

8/5/74

C. Bugg
EHC TEST ENGINEER

TEST PROCEDURE

APPROVED BY

M.J. Sindoni

DATE

10/24/74

M.J. Sindoni
EHC DESIGN ENGINEERING

PRINTS TO

MADE BY
D. Mone Oct. 28, 1974

ISSUED
OCT 28 1974

APPROVALS

Steam Turbine

DIV OR
DEPT.

Schenectady, N.Y.

LOCATION

P3K-AL-0355 -A01

CONT ON SHEET --- SH NO. 6

CODE IDENT NO.



GE Power Generation Engineering

Materials and Processes Engineering
Schenectady, NY 12345

PROCESS SPECIFICATION

P3K-AL-0494-A02

TEST INSTRUCTIONS FOR GENERAL CURCUIT CARD TEST PROCEDURES-LOGIC

DOCUMENT REVISION STATUS: DETERMINED BY THE LAST ENTRY IN THE "REV" AND "DATE" COLUMN

REV.	AN NO.	DESCRIPTION	SIGNATURE	REV. DATE
A	YA00096	SPECIFICATION LISTED IN STEAM TURBINE/GENERATOR INDEX AS "INACTIVE" HAS BEEN FORMALLY REVISED AS "INACTIVE FOR NEW DESIGN". (PR BUDKA)	C.R. Tripp	DEC 02 1991
<div>INACTIVE FOR NEW DESIGN AS OF 12/02/91</div>				

© COPYRIGHT 1991 GENERAL ELECTRIC COMPANY

PROPRIETARY INFORMATION - THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION OF GENERAL ELECTRIC COMPANY AND MAY NOT BE USED OR DISCLOSED TO OTHERS. EXCEPT WITH THE WRITTEN PERMISSION OF GENERAL ELECTRIC COMPANY.

PREPARED BY: P.R. BUDKA

ORIG. ISSUE DATE: --

