

1266J49 REV 0
3 KHZ OSCILLATOR
MBD 39A
DISK: GE5

MARK III
PC BOARD TEST

REV. B
SHEET 1 of 1

ASSEMBLY DRAWING
4116J89G001

PC BOARD DRAWING
4116J39-001

SCHEMATIC DRAWING
4112J92

TEST KIT
UNIV. MK II

1.0 INSPECTION

- | | | |
|-------------------------|----------------------|-------------------|
| .1 IDENTIFICATION _____ | .3 SOLDER/WIRE _____ | .5 KEY SLOT _____ |
| .2 COMP./ CONN. _____ | .4 TEMP CYCLE _____ | .6 _____ |
| | | .7 _____ |

REMARKS:

2.1 APPLY +30 VDC, -22 VDC, AND POWER SUPPLY COMMONS TO UNIVERSAL TEST KIT.

2.2 SET 4 R15 FULL CW.

2.3 SET 2 R11 AND 3 R2 FULL CCW. *also R17*

2.4 PLUG PC BOARD INTO TEST KIT.

3.0 POWER SUPPLY CHECKS

3.1 READ VOLTAGE TP5 (+14.80 TO +15.20 VDC).

3.2 READ VOLTAGE TP6 (-14.80 TO -15.20 VDC).

4.0 OSCILLATOR TEST

4.1 ADJUST 4 R15 FOR +2.70 VDC AT TP4.

4.2 ADJUST 2 R11 AND 3 R2 CW IN EQUAL INCREMENTS UNTIL THE FREQUENCY AT TP1 IS 2997 TO 3003 HZ.

4.3 ADJUST 1 R17 FOR 6.00 VRMS AT TP1. *Pin 35* USE TP8 AS GROUND REFERENCE.

4.4 READ OSCILLATOR OUTPUT VOLTAGE PIN 35 (6.00 VRMS).

4.5 READ OSCILLATOR OUTPUT FREQUENCY PIN 35 (2997 TO 3003 HZ).

4.6 LOAD PIN 35 WITH 15 Ω 10W.

4.7 CHECK THAT OUTPUT VOLTAGE HAS NOT CHANGED MORE THAN 6 MVRMS FROM THE READING TAKEN IN STEP 4.4 (V4.4 - V4.7 \pm 6 MVRMS). USE TP8 AS GROUND REFERENCE.

4.8 CHECK THAT THE OUTPUT FREQUENCY HAS NOT CHANGED MORE THAN 2 HZ FROM THE READING TAKEN IN STEP 4.5 (F4.5 - F4.8 \pm 2 HZ).

4.9 WITH A SCOPE, CHECK THAT THE WAVEFORM AT TP1 IS NOT BEING MODULATED. (SEE SKETCH)

Sine wave with no oscillation ridge on it.

all changes made 3/12/88

*TROUBLESHOOTING
OVER*

VDC
VDC

HZ

VRMS
HZ

FHZ