



GE Power Generation Engineering

Materials and Processes Engineering
Schenectady, NY 12345

PROCESS SPECIFICATION

P3K-AL-0358-A01

TEST INSTRUCTIONS FOR LOAD CONTROL UNIT MSV/IV AMPLIFIERS ANALOG LOGIC CIRCUIT BOARD 1L3-E001

DOCUMENT REVISION STATUS: DETERMINED BY THE LAST ENTRY IN THE "REV" AND "DATE" COLUMN

REV.	AN NO.	DESCRIPTION	SIGNATURE	REV. DATE
A	YA00096	SPECIFICATION LISTED IN STEAM TURBINE/GENERATOR INDEX AS "INACTIVE" HAS BEEN FORMALLY REVISED AS "INACTIVE FOR NEW DESIGN". (PR BUDKA)	C.R. Trippi	DEC 4 2 1991
<div>INACTIVE FOR NEW DESIGN AS OF 12/02/91</div>				

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PREPARED BY: P. R. BUDKA

ORIG. ISSUE DATE: --

REV
NO.

TITLE

P3K-AL-0358 -A01

CONT ON SHEET

SH NO.

TEST INSTRUCTIONS FOR LOAD CONTROL UNIT MSV/IV AMPLIFIER
ANALOG/LOGIC CIRCUIT BOARD 1L3-E001
FIRST MADE FOR (Ass'y Drawing 117D7743 G-1)

REVISIONS

I. CIRCUIT DESCRIPTIONS

This circuit board is used to consolidate all of the Analog to Logic Relays that provide switching signals to the Main Stop Valve Amplifier and Intercept Valve Amplifier Analog Circuits.

The board contains six (6) 24V mercury wetted relays.

II. CIRCUIT SPECIFICATIONS

1. Connect +24 VDC to pin 38 and ground to Pin 40. A +24V should appear between TP1 and TP2. Connect 24V 20MA lamps to pins 5, 6, 7, 8, 9, 10, 11, 12, and 13.
2. The logic and path continuity should be checked according to the following table:

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273-2

273-12

273-138

273-71

PRINTS TO

MADE BY
D.Mone Oct. 28, 1974

APPROVALS

Steam Turbine

DIV OR
DEPT.

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ISSUED
OCT 28 1974

Schenectady, N.Y.

LOCATION

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SH NO.

REV NO. A	
P3K-AL-0358- A01	TITLE
CONT ON SHEET 4 SH NO. 3	TEST INSTRUCTIONS FOR LOAD CONTROL UNIT MSV/IV AMPLIFIER ANALOG/LOGIC CIRCUIT BOARD 1L3-E001 FIRST MADE FOR (Ass'y Drawing 117D7743 G-1)

CONT ON SHEET 4	SH NO. 3
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REVISIONS

INPUTS AT PINS

CORRECT OUTPUTS AT PINS

19	20	22	23	24	25	26	18	21	27		5	6	7	8	9	10	11	12	13
----	----	----	----	----	----	----	----	----	----	--	---	---	---	---	---	----	----	----	----

0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
1	1	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0	0	0
1	1	0	0	0	0	0	1	0	0	0	1	0	0	1	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0
0	0	1	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0
0	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0
0	0	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	1
0	0	0	0	1	1	1	0	0	1	0	0	0	0	0	0	0	0	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

0: 0V or lamp off

1: 24V or lamp on

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Test Instructions for Load Control Unit MSV/IV Amplifier
Analog/Logic Circuit Board 1L3-E001
FIRST MADE FOR (Assy. dwg. 117D7743 G-1)

REVISIONS

TEST INSTRUCTIONS

1L3-E001

Assembly 117D7743

Schematic 117D7742

PROCEDURE:

1. Refer to Table A for test circuit.
2. Measure +24 VDC from TP1 to TP2.
3. The chart which follows outlines the procedure for testing the relay logic of this circuit.

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SH NO.

GENERAL ELECTRIC

P3K-AL-0358-A-1

REV NO. **A**
P3K-AL-0358-A-1
CONT ON SHEET **6** SH NO. **5**

TITLE TEST INSTRUCTIONS FOR LOAD CONTROL UNIT MSV/IV AMPLIFIER
ANALOG/LOGIC CIRCUIT BOARD 1L3-E001
FIRST MADE FOR

REVISIONS

L14					X														
L13					X														
L12																			
L11																			
L10																			
L9			X																
L8																			
L7																			
L6		X		X		X													
L5		X		X		X													
L4		X		X		X		X											
L3		X		X		X		X											
L2		X		X		X		X											
L1		X		X		X		X											
PROCEDURE																			
1 Set SSW to pos. 0																			
2																			
3																			
4																			
5																			
6 End of Test																			

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REV. NO. **A**

TITLE

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TEST INSTRUCTIONS FOR LOAD CONTROL UNIT MSV/IV AMPLIFIER
ANALOG/LOGIC CIRCUIT BOARD 1L3-E001

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SH NO. **6**

FIRST MADE FOR

REVISION

Table A

Pin 27	SSW Pos. 1
21	" " 2
18	" " 3
11	- L1
13	- L2
10	- L3
9	- L4
7	- L5
6	- L6
12	- L9
8	- L13
5	- L14
38	+24 VDC
26	"
25	"
24	"
23	"
22	"
20	"
19	"
40	24 V COM

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CONT ON SHEET — SH NO. 7

REVISIONS

PREPARED BY

D. Economou

DATE

9-15-73

D. Economou
EHC DESIGN ENGINEERING

APPROVED BY

P.C. Callan

DATE

10-25-74

P.C. Callan - MANAGER
EHC DESIGN ENGINEERING

TEST PROCEDURE
PREPARED BY

C. Bugg

DATE

8/5/74

C. Bugg
EHC TEST ENGINEER

TEST PROCEDURE
APPROVED BY

M.J. Sindoni

DATE

10/24/74

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SH NO. 7

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