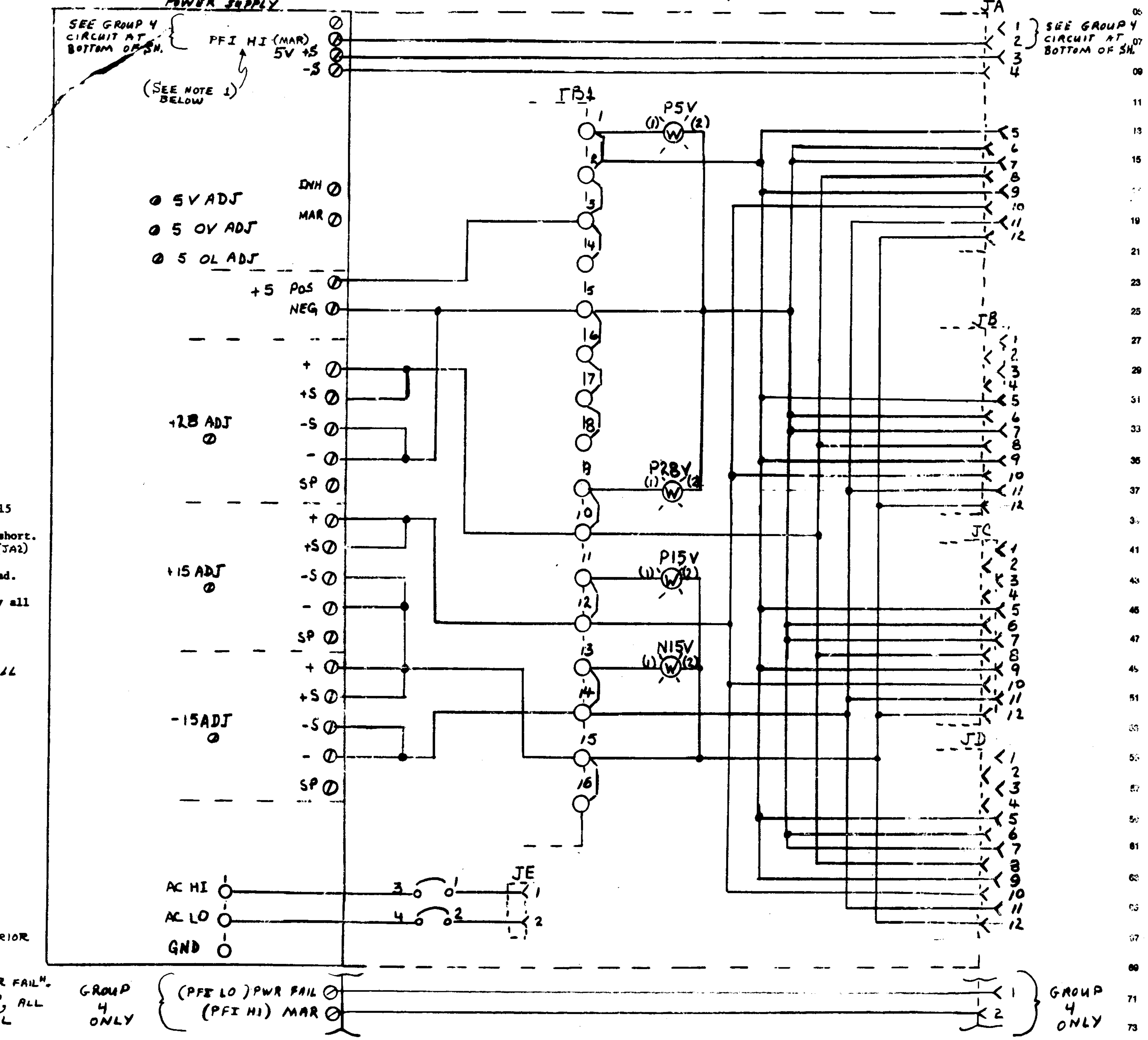
POWER SUPPLY ELEMENTARY



TEST INSTRUCTIONS

1.0 - 1.4

Verify output connector wiring.

1.5 Input Voltage = 120 ± 2
Load power supply 5V - 30 amp, +15V - 1.5A, -15V, -1.5A, 28V
Monitor PS outputs and adjust if necessary - 8V, ± .2 + 15 ± .1
-15 ±.1 +5 ± .1. Remove load.

1.6 Verify all lights ON

1.7 Monitor ripple
+15 50 mv P-P
-15 50 mv P-P
+28 50 mv P-P (WITH 1 AMP LOAD ON P5 V)
5V 50 mv P-P

1.8 Load power supply same as 1.5. Verify all output voltages. Input voltage +90 ±2. Load power supply same as 1.5. Verify all output voltages. Input voltages = 132 ± 2. Load power supply same as 1.5. Verify all output voltages.

2.0 Input voltage = 120 ± 2.

2.1 Load power supply same as 1.5. Short out P28 supply. P28 light goes out - all others stay on. Remove P28 short and short-out + 15 - P15 light goes out - all others stay on. Remove +15 short and short -15. N15 light goes out - all others stay on. Remove N15 short. Increase P5 load to 33A. Verify P5 is 5 ± .1 volts. *Power fail (JA2) signal should be logic '0'. Short out P5. All lights should be out. Power fail signal should be logic '1'. Remove short and load.

2.2 Power supply power cycled. Load power supply same as 1.5. Verify all output voltages. Remove power. Test complete.

* GROUP 4 INSTRUCTION ONLY:
POWER FAIL SIGNAL (JA1) SHOULD BE LOGIC '1', SHORT OUT P5, ALL LIGHTS SHOULD BE OUT AND (JA1) SHOULD BE LOGIC '0'
(JA2) LOGIC '0', SHORT OUT P5. ALL LIGHTS SHOULD BE OUT AND (JA2) SHOULD BE LOGIC '1'

NOTE 1 (FOR ENGINEERING REFERENCE): GROUPS 1-3 BUILT PRIOR TO 5/20/88 USING VENDOR "AC/DC" POWER SUPPLY MODEL RQ304-101 HAD JA(2) CONNECTED TO TERMINAL MARKED "PWR FAIL". TO STANDARDIZE WITH NEW POWER SUPPLY VENDOR "ELPAC", ALL ELPAC & AC/DC POWER SUPPLIES WILL HAVE POWER FAIL SIGNALS AS PER TABLE BELOW: (REF. BU974HR)

AC/DC SIGNAL NAME	PWR FAIL	MAR-
ELPAC SIGNAL NAME	PFI LO (PWR FAIL)	PFI HI (MAR)
POLARITY	(high-to-low step)	(low-to-high step)
JA CONNECTOR	JA (1)	JA(2)
BACKPLANE SIGNAL	L-VSH	L-VSL

REV. 6 - CHG JA(2) FROM "PWR FAIL" TO "PFI HI (MAR)", ADDED NOTE 1
REV. 3 BU# 927LY A STEVENS 2-13-86
REV. 4 A STEVENS 3-4-86
REV. 5 A STEVENS 5-12-88
REV. 2 BU# 927LR PLL 10-6-86
REV. 1 GKG 11/8/84 T.I.
ISSUED 5-2-84
MADE BY G.K. GARRIDO
PRINTS TO
APPROVALS G.K.G.
FIRST MADE FOR REQ. T-A608
LA NO. 4870
GENERAL ELECTRIC
ELEMENTARY DIAGRAM 173C5190
CONT. ON SH. FL
SH. NO. 2