

2 7 7 A 3 9 9 4

REV
NO.

TITLE

CONT ON SHEET 2 SH NO. 1

2 7 7 A 3 9 9 4

TEST INSTRUCTIONS

CONT ON SHEET 2 SH NO. 1

FIRST MADE FOR 304A8465

REVISIONS

STANDING INSTRUCTIONS
FOR
TRIGGER GENERATOR II
PRINTED CIRCUIT BOARD
FOR
GENERREX SYSTEM
304A8465

1) ALB/GSW 11/30/82
Board re-design

DL13

3EL1

4ME1

4QA3

4AA1

Distribution:

- 1 Test Area
- 1 QC Engineering
- 1 Engineering

PRINTS TO

MADE BY
KA Morris 091479

APPROVALS

ISSUED 12-3-79

Wdl

Drive Systems

DIV OR
DEPT.

Salem, Virginia

LOCATION

2 7 7 A 3 9 9 4

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CODE IDENT NO.

CAR3

REV NO.
2 7 7 A 3 9 9 4
CONT ON SHEET 3 SH NO. 2

TITLE
TEST INSTRUCTIONS
FIRST MADE FOR 304A8465

REVISIONS

I. POWER SUPPLY REQUIREMENTS

NOMINAL VOLTAGE	MAXIMUM CURRENT	MAXIMUM VOLTAGE	PINS
P15	100ma	+17	1-2
N15	100ma	-17	5-6
N18	100ma	-20	25-26
COM	-	-	3-4

II. INPUT SIGNAL REQUIREMENTS

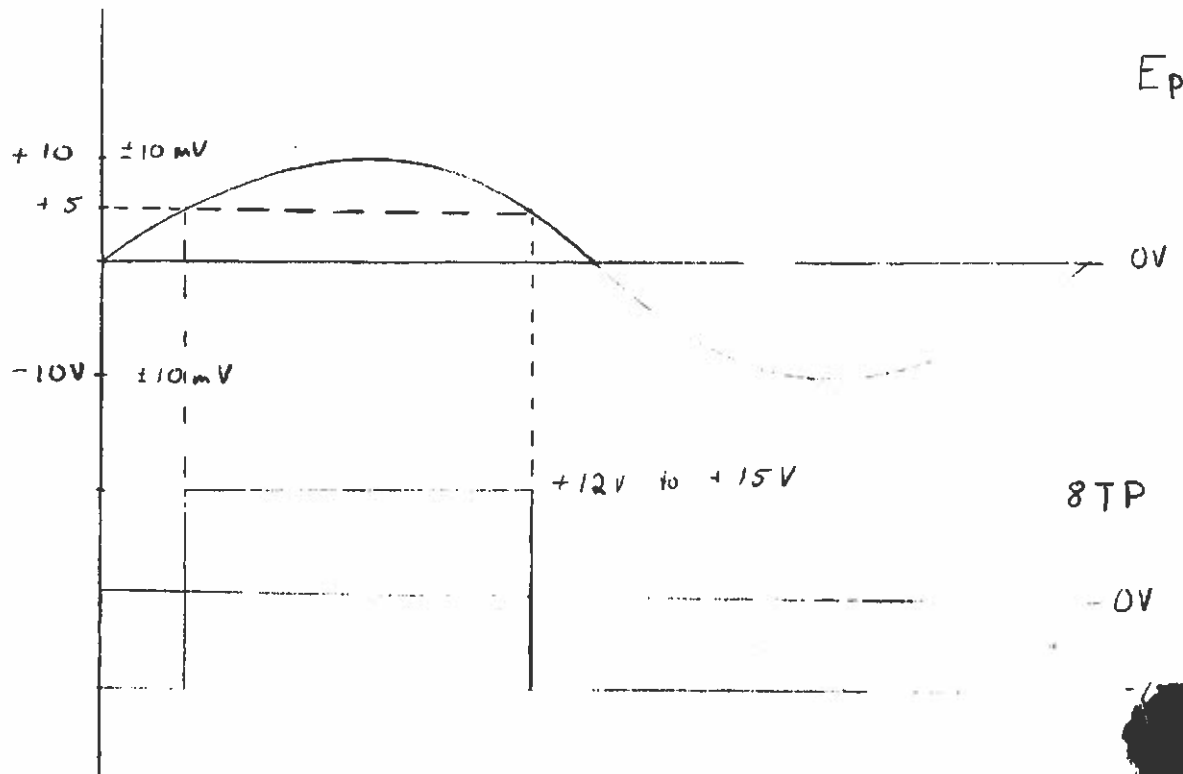
- Two regulated, variable, D.C. supplies (0 to 10VDC).
- One sine wave generator capable of 20 volts P-P at 60Hz.

III. TEST EQUIPMENT REQUIRED

- One dual channel oscilloscope
- One digital volt meter.

IV. ELECTRICAL TESTS

- Apply power supplies per Section I.
- Apply a +5 VDC signal to pins 15 and 16 (Q).
Connect a sine wave generator (set at 60Hz. and 20 volts P-P) to pins 13&14 (EP).
- Verify the following waveforms.



1) MR 010519 010519-6X
2) ALB/GSW 11/30/82
Board re-design

DL13
3EL1-
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MADE BY D. C. Nolan
RE ISSUED 5-21-84

APPROVALS
D.C. Nolan

DRIVE SYSTEMS

DIV OR
DEPT.

2 7 7 A 3 9 9 4

SALEM, VIRGINIA

LOCATION

CONT ON SHEET

3

SH NO.

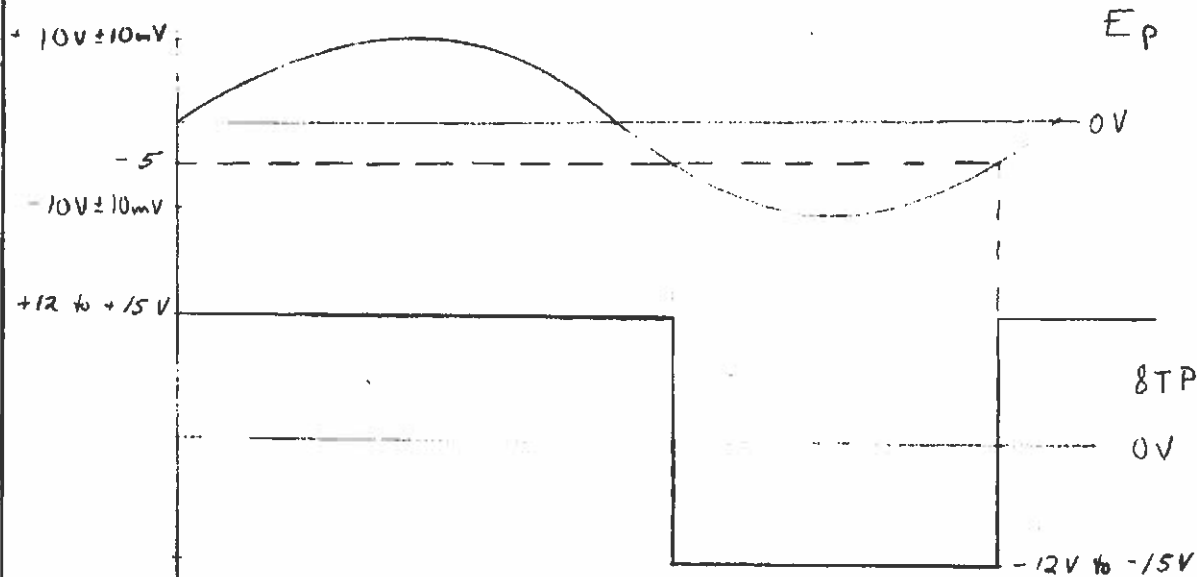
2

REV NO.
2 7 7 A 3 9 9 4
CONT ON SHEET 4 SH NO. 3

TITLE
TEST INSTRUCTIONS
FIRST MADE FOR 304A8465

IV. ELECTRICAL TESTS (Cont'd)

- d. Change Q to -5V. (pins 15 & 16)
- e. Verify the following waveforms:



- f. Disconnect the sine wave generator and change Q to +5 volts.
- g. Connect a square wave generator (set at 60 Hz and 10V. P-P) to pins 17 and 18 (-I).

REVISIONS

1) mlf 01/01/82 849465
2) ALB/GSW 11/30/82
Board re-design

DL13

3EL1

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4QA3

4AA1

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DRIVE SYSTEMS
SALEM, VIRGINIA

DIV OR DEPT.
LOCATION

2 7 7 A 3 9 9 4

CONT ON SHEET 4

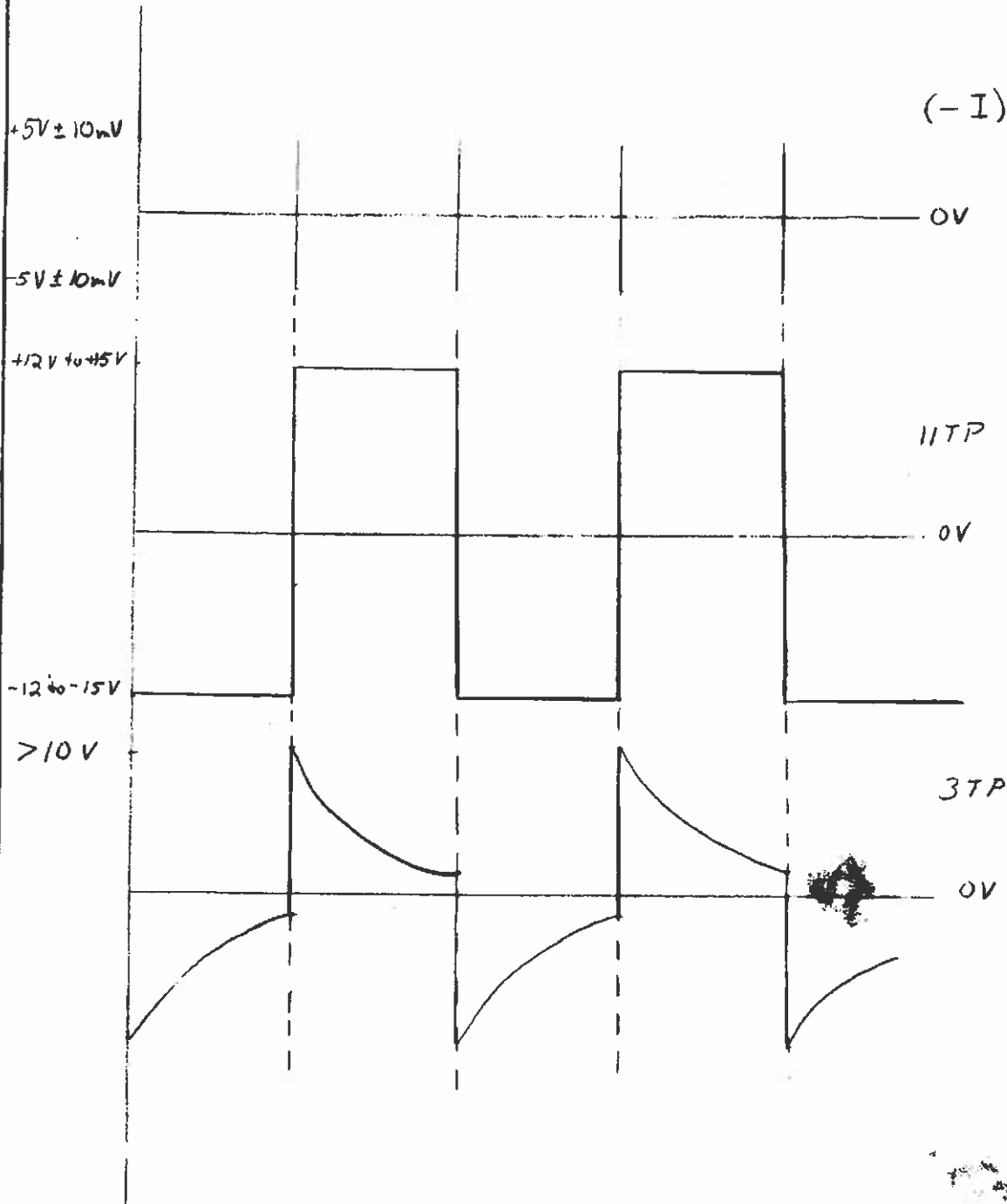
SH NO. 3

REV NO.	2 7 7 A 3 9 9 4
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TITLE	TEST INSTRUCTIONS
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IV. ELECTRICAL TESTS (Cont'd)

H. Verify the following waveforms:



REVISIONS

- 1) FILE 8105M BLANK-6X
- 2) ALB/CWS 11/30/82
Board re-design

3EL1

DL13

4AA1

4ME1

4QA3

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APPROVED	<i>D.C. Nolan</i>
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DRIVE SYSTEMS	SALEM, VIRGINIA
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DIV OR DEPT.	LOCATION
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REV NO.

2 7 7 A 3 9 9 4

TITLE

TEST INSTRUCTIONS

CONT ON SHEET 6

SH NO. 5

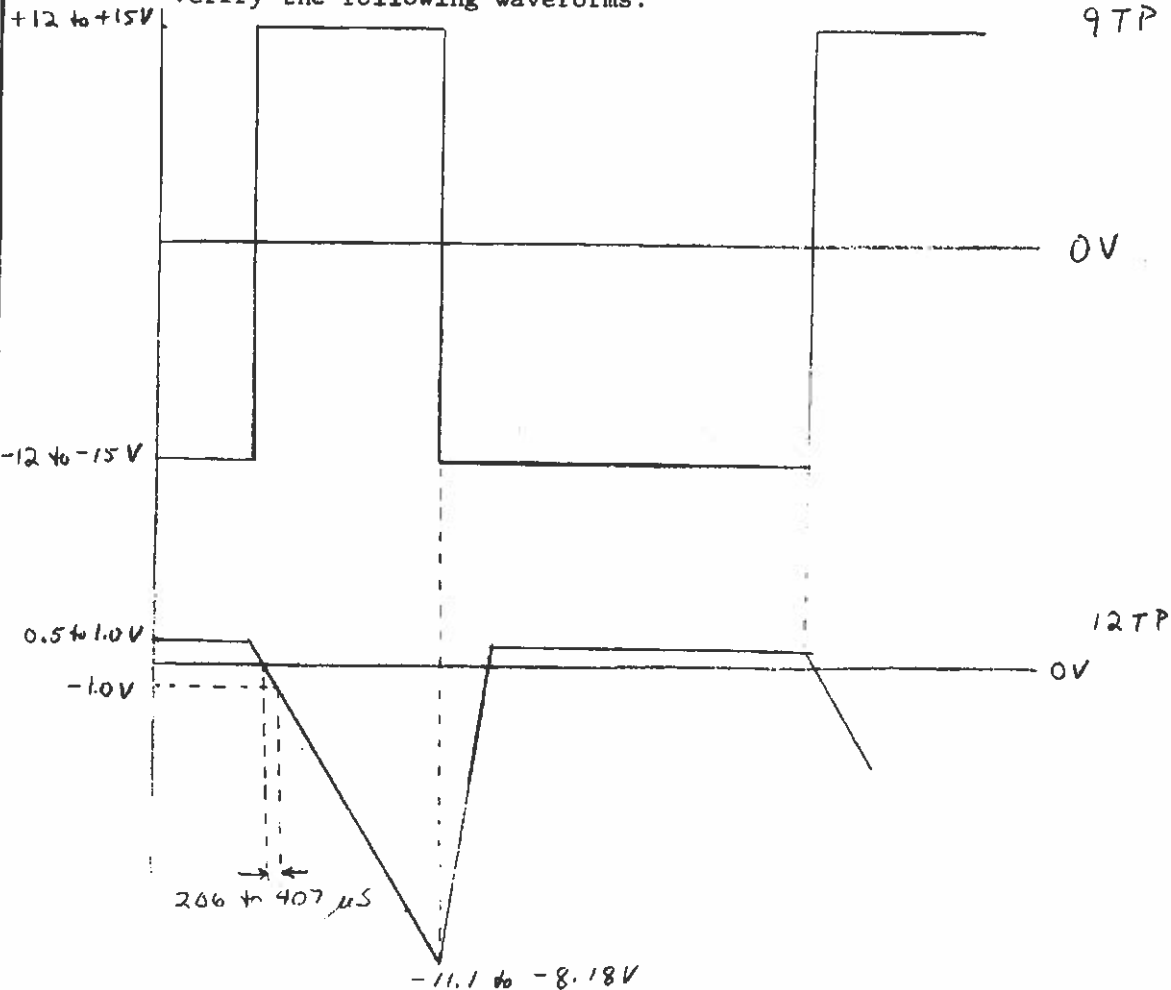
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REVISIONS

IV. ELECTRICAL TESTS (Cont'd)

J. Apply a +8.0VDC + 10mv signal to pins 11 & 12 (EX) while maintaining +5VDC at 15/16.

Verify the following waveforms:



k. Connect a 10K ohm, $\frac{1}{2}$ W resistor from pins 27 and 28 to ground.
l. Apply -18 VDC to pins 25 and 26 (maintain previous signals).

1) MRP 8/20/79 8465-6x
2) ALB/GWS 11/30/82
Board re-design

3EL1

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4AA1

4ME1

4QA3

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DRIVE SYSTEMS

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SALEM, VIRGINIA

LOCATION

CONT ON SHEET 6

SH NO. 5

REV NO.

2 7 7 A 3 9 9 4

TITLE

TEST INSTRUCTIONS

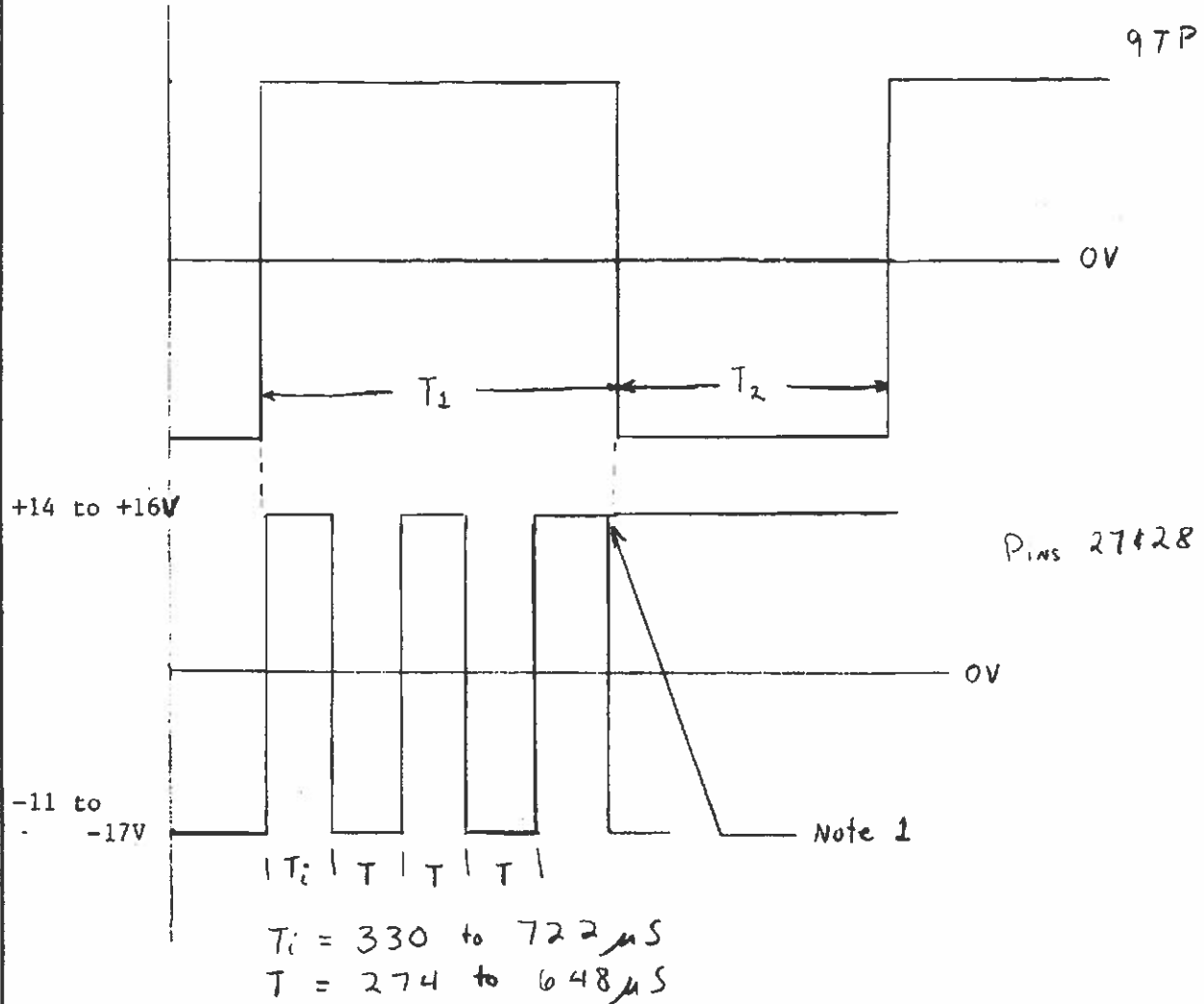
CONT ON SHEET Fl. SH NO. 6

FIRST MADE FOR 304A8465

REVISIONS

IV. Electrical Tests (Cont'd)

m. Verify the following waveforms:



Note 1: Verify only that the pulse train stops at this point. The pulse may be either high or low at this point.

Note 2: T_1 and T_2 need not be equal.

Note 3: The number of pulses in the pulse train of Pins 27&28 may be other than shown.

n. END OF TEST

ALB/GWS 11/30/82
Board re-design

DL13

3EL1

4AA1

4QA3

4ME1

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2 7 7 A 3 9 9 4

LOCATION

CONT ON SHEET

Fl.

SH NO.

6

CODE IDENT NO.