

<b>g</b> <b>GE Electronic Services</b>	<b>TEST and OPERATING PROCEDURE</b>	
	<b>REVISION: 1.0</b>	<b>DATE: 06/17/94</b> <b>PAGE 1 OF 2</b>
<b>DISTRICT MGR:</b> <i>Michael A. Hodge</i> <b>QUALITY REP:</b> <i>Robert J. Duvall</i>		
<b>TITLE:</b> <b>PROCEDURE:</b>		
<b>AS-884A-XXX CPU</b>		

## 1. INTRODUCTORY DESCRIPTION

- A. This procedure establishes the methods for testing a AS-884A-XXX.
- B. Environmental ranges: 72+/-10 Deg. F. with 20-80% R.H.
- C. Unit warm-up/stabilization period requirement: 1 Minute
- D. Personnel using this procedure are expected to have a high degree of confidence and expertise in related testing and calibration procedures.
- E. Procedures not explained here are considered to be understood as common practice.

## 2. TEST EQUIPMENT VERIFICATION

- A. Verify the accuracy of the standard(s) used in the repair/calibration process by evidence of recent calibration labeling affixed to the test equipment.
- B. All measurement standards used in this procedure shall be traceable to the NATIONAL INSTITUTE of STANDARDS and TECHNOLOGY (N.I.S.T.) and shall have the accuracy, stability, range and resolution required for the intended use.
- C. Unless otherwise specified, the collective uncertainty of the Measurement Standard(s) shall not exceed twenty five percent of the acceptable tolerance for each characteristic being calibrated.
- D. All deviations shall be documented.

## 3. EQUIPMENT CLEANING

- A. All equipment clean will be performed as instructed in the GEES SOP Sec. 14.0

## 4. EQUIPMENT INSPECTION

- A. The following criteria should be used as a guideline or basis for the inspection process of the this unit:
  - 1. Wires broken or cracked.
  - 2. Terminal strips broken or cracked.
  - 3. Loose wires.
  - 4. Components visually damaged.
  - 5. Capacitors leaking.
  - 6. Solder joint, cold.
  - 7. Circuit board discolored or burned.
  - 8. Printed wire runs burned or damaged.

## 5. THEORY OF OPERATION

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A. Refer to the AS-884A-XXX CPU information Bulletin(s) for theory of operation.

## 6. TEST EQUIPMENT TO BE USED

- Micon 884 system
- P190 Programmer
- Test Tape or Disk
- Digital Multimeter

## 7. FINAL TEST AND OPERATION PROCESS

✍ NOTE: Technician must have working knowledge on the Modicon 884 system.

- Insert board into the appropriate rack and apply power.
- A ready light should illuminate, along with the battery OK light.
- Attach the P190 or Computer to the system and load the operations program to configure the board.
- Verify memory protect switch is in the "OFF" position and clear memory
- Place memory protect in the "ON" position and attempt to clear memory.
- An ERROR should be received indicating memory protect or user error.
- Clear error and place memory protect switch is in the "OFF" position.
- Load test program via the programmer.
- Use the programmer to start P/C logic solving operations.
- The run light should illuminate.
- Verify operation of the board by watching the test pattern of the I/O cards in the test system. (Test pattern in standard rotating bit test on a 1 second time base)
- Let the board run for (4) four or more hours.
- Turn off power and let board set for at least 5 minutes.
- Turn power on and board should resume logic solving.
- Use the programmer to stop PLC logic solving operations.
- Use the programmer to clear the boards memory.
- Remove board from test system.
- End of test.

**TEST WRITTEN BY:** \_\_\_\_\_

**DATE:** \_\_\_\_\_