

REV NO.	A
6 8 A 9 9 9 2 9 8	
CONT ON SHEET 2	SH NO. 1

TITLE	
TEST INSTRUCTIONS NOZZLE CONTROL	
FIRST MADE FOR	IC3600SNCB1

REVISIONS

1. VISUALLY CHECK THE FOLLOWING COMPONENTS:

R44	39K	} CHECK POLARITY
C11	1MFD	
C15	100MFD	
C13	50 MFD	

2. MAKE THE FOLLOWING RESISTANCE CHECKS:

SJA ( 14 ) TO TX ( 7 )	80K TO 85K
SJA ( 14 ) TO AB ( 3 )	320K TO 340K
SJA ( 14 ) TO B ( 15 )	460K TO 490K
SJA ( 14 ) TO HR ( 21 )	480K TO 510K
NCE ( 22 ) TO NM ( 31 )	9.5K TO 10.5K

3. ATTACH THE FOLLOWING LOADS:

TC ( 35 )	7 -20KC LOADS
SC ( 49 )	7 -20KC LOADS
OSC ( 48 )	A 1.2K RESISTOR TO +12V
OTC ( 32 )	A 1.2K RESISTOR TO +12V
NCE ( 22 )	A 3.3K RESISTOR TO COM.

TO SPEED UP READINGS CONNECT A 1K TO 2K RESISTOR FROM C15 (PLUS SIDE) TO ACOM.  
C15 STILL STABILIZES CKTS.

4. INPUT LEAKAGE. IN TURN, CONNECT EACH INPUT BELOW TO +12V THROUGH AN AMMETER.  
CHECK THAT FOR EACH CASE, CURRENT IS LESS THAN 5  $\mu$ A.

NL ( 45 )  
OFF ( 46 )

5. INPUT LOADING. IN TURN SHORT EACH INPUT BELOW TO COM THROUGH AN AMMETER. CHECK  
THAT FOR EACH CASE, CURRENT IS BETWEEN 1.35 AND 1.65 MA.

NL ( 45 )  
OFF ( 46 )

6. CHECK THAT OUTPUT SR ( 39 ) IS CORRECT FOR THE FOLLOWING CONDITIONS:

<u>VOLTAGE SR ( 39 )</u>	<u>CONDITIONS</u>
- 9 TO -11	R90 CW, R92 CW
0 TO +1.2	R90 CW, R92 CCW
- 9 TO -10.5	R90 CW, R92 CCW, AB ( 3 ) TIED TO +12V
- 2 TO -3.	R90 CCW, R92 CCW, AB ( 3 ) TIED TO +12V

7. CHECK THAT OUTPUTS TC ( 35 ) AND OTC ( 32 ) ARE CORRECT FOR THE FOLLOWING  
CONDITIONS:

<u>VOLTAGE</u>	<u>CONDITIONS</u>
TC ( 35 )	OTC ( 32 )
6 TO .8V	0 TO 0.4V AB ( 3 ) TIED TO +12V
0 TO 0.4V	11 TO 12.2V AB ( 3 ) OPEN

5) SBH REVISED 4/11/75

3) 4-29-77 JCH  
4 BAB 9/7/72

1 10-9-70 MAC  
2 DLP 12.2.70

2520  
DL22

PRINTS TO

MADE BY	DARLENE ALLIE
ISSUED	APRIL 22-1970

APPROVALS

*RFP*

DRIVE SYSTEMS

DIV OR  
DEPT.

6 8 A 9 9 9 2 9 8

SALEM, VIRGINIA

LOCATION

CONT ON SHEET 2

SH NO. 1

CODE IDENT NO.

REV  
NO. A

6 8 A 9 9 9 2 9 8

CONT ON SHEET FL. SH NO. 2

TITLE

TEST INSTRUCTIONS  
NOZZLE CONTROL

FIRST MADE FOR IC3600SNCB1

REVISIONS

8. CHECK THAT OUTPUTS SC ( 49 ) AND OSC ( 48 ) ARE CORRECT FOR THE FOLLOWING CONDITIONS:

<u>VOLTAGE</u>		<u>CONDITIONS</u>		
SC ( 49 )	OSC ( 48 )	<u>OFF ( 46 )</u>	<u>NL ( 45 )</u>	<u>TX ( 7 )</u>
0 TO 0.4V	11 TO 12.2V	TO ACOM	TO ACOM	SHORTED TO +12V
0 TO 0.4V	11 TO 12.2V	TO ACOM	4.25V INPUT	OPEN
0 TO 0.4V	11 TO 12.2V	4.25V INPUT	TO ACOM	OPEN
6 TO 8V	0 TO 0.4V	TO ACOM	TO ACOM	OPEN

9. CHECK THAT THE VOLTAGE AT NCE ( 22 ) IS  $9.8 \pm 0.2$  VOLTS.

10. SHORT SR ( 39 ) TO COM AND CHECK THAT THE VOLTAGE ON SJSC ( 38 ) IS -6.0 TO -7.0 VOLTS.

REMOVE THE SHORT FROM SR ( 39 ) TO COM AND SHORT SJSC ( 38 ) TO COM. CONNECT AN AMMETER FROM EACH OF THE POINTS LISTED BELOW TO N12 AND CHECK THAT THE CURRENT IS WITHIN SPEC.

<u>POINT</u>	<u>CURRENT</u>
MF ( 44 )	2.5 TO 3.1 MA
OST ( 43 )	50 TO 70 $\mu$ A
N ( 41 )	190 TO 230 $\mu$ A

11. REMOVE C15 TEST RESISTOR.

1. DLP 12-1-70  
2. ACW 4-20-71

3 BAB 9/7/72  
4 RBA-3-29-12

DL22

2520

PRINTS TO

MADE BY DARLENE ALLIE	APPROVALS <i>RKD</i>	DIV OR DEPT. DRIVE SYSTEMS	6 8 A 9 9 9 2 9 8
ISSUED APRIL 22 1970		LOCATION SALEM, VIRGINIA	CONT ON SHEET FL. SH NO. 2