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P3K-AL-0355 -A01

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TITLE

TEST INSTRUCTIONS FOR LOAD LIMIT AND LOAD SET RUNBACK ANALOG LOGIC CIRCUIT BOARD 1L3-BOO1

FIRST MADE FOR (Ass'y Drawing 117D7726 G-1)

CIRCUIT DESCRIPTION

This circuit is used to consolidate all of the analog to logic relays that provide switching signals to the Main Steam Pressure Limiter and the Load Limit and Load Set Runback Analog Circuits, except for FA/PA transfer The board contains six (6) 24V mercury wetted relays.

CIRCUIT SPECIFICATIONS II.

- Connect +24 VDC to pin 38 and ground to pin 40. A +24V should appear between TP1 and TP2. Connect 24V lamps to pins 4, 5, 6, 7, 8, 9, 10, 11, 12, and 14.
- The logic and path continuity should be checked according to the following table:

273-221 273-227

273-2

273-129 273-138

273-71°

PRINTS TO

D.Mone Oct. 28, 1974 ISSUED

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Steam Turbing __ Schenectady, N.Y.

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TEST INSTRUCTIONS FOR LOAD LIMIT AND LOAD SET RUNBACK ANALOG LOGIC CIRCUIT BOARD 1L3-BOO1 $\,$ FIRST MADE FOR

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(Ass'y Drawing 117D7726 G-1)

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TEST INSTRUCTION FOR LOAD LIMIT AND LOAD SET RUNBACK.

FIRST MADE FOR

TITLE

MK II

TEST INSTRUCTIONS 1L3-B001

SH NO.

Assembly

117D7726

Schematic

117D7723

PROCEDURE:

- Refer to P3K-AL-0494-A02 for test circuits, equipment list, and set up instructions.
- Table A provides the step by step procedure to follow in testing this circuit.
- Table B provides a step by step description of input voltages (to use as a troubleshooting aid).

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STEP 2 APP P 2	В	В									MFR 28 1976
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STEP 7 APP P 20			Ø					110			
STEP 8 APP P 32											
STEP 9 APP P 36			A								
STEP 10 REM P 32		4.3	A	A							
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STEP 12 REM P 34											
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GENERAL MELECTRIC P3K-AL-0355-A01 HEV NO. CONT ON SHEET 6 SH NO. 5 TITLE TEST INSTRUCTION FOR LOAD LIMIT AND P3K-AL-0355-A01 LOAD SET RUNBACK CONT ON SHEET 6 **s**н No. 5 MK II FIRST MADE FOR ACTION REVISIONS INPUT AT PINS 1975 2 10 20 32 34 36 38 00 STEP 1 STEP 2 APP P 2 Χ STEP 3 REM P 2 STEP 4 APP P 38 X STEP 5 APP P 10 X X STEP 6 REM P 10 X STEP 7 APP P 20 Χ X STEP 8 APP P 32 X X X STEP 9 APP P 36 X X X X STEP 10 REM P 32 X X X STEP 11 APP P 34 Χ X Х X STEP 12 REM P 34 X X Х STEP 13 APP P 32 Х X X X STEP 14 REM P 36 Х Х X STEP 15 REM P 20 X X STEP 16 REM P 32 Χ'n STEP 17 REM P 38 PRINTS TO

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D.Mone Oct. 28, 1974

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Steam Turbine Schenectady

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LOCATION CONT ON SHEET 6

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GE Power Generation Engineering

PROCESS SPECIFICATION

Materials and Processes Engineering Schenectady, NY 12345 P3K-AL-0494-A02

TEST INSTRUCTIONS FOR GENERAL CURCUIT CARD TEST PROCEDURES-LOGIC

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PREPARED BY:	Ρ.	R.	BUDKA	-
ORIG. ISSUE DATE:		-		-

TEST INSTRUCTIONS FOR GENERAL P3K-AL-0494-A02 CIRCUIT CARD TEST PROCEDURES-LOGIC CONT ON SHEET SH NO. FIRST MADE FOR EHC MK II REVISIONS EQUIPMENT LIST DVM Digital Voltmeter 5 digit AC/DC/Ohms SD7110A В. CT Counter Timer SD6150 C. Scope Oscilloscope (Storage) TEK R5103N D. WT Function Generator WAVETEK 154 E. FPS 1,2 DC Power Supply 0-65V Fluke 4265A PS 1 DC Power Supply 0-320V Trygon RS320 PS 2, PS3, PS4 DC Power Supply 0-32V Lambda LMF28 II. SETUP PROCEDURES A. All logic circuits are tested under a 50K load, tolerance B. All signals are measured and applied with respect to pin 40 unless otherwise noted. INTERPRETATION OF TEST TABLES III. The logic test tables which provide the step by step procedure apply the following code: 1. Init Cond - Initial conditions of the test App Pnn - Apply 24VDC to pin nn Rem Pnn - Remove 24VDC from pin nn The logic test tables apply the following code for measured voltages. Indicates a DC voltage - level not important ø OVDC A 24.0 VDC Tolerance 0.1 VDC B 22.9 VDC 11 ·C 22.5 VDC Ď 21.8 VDC E 21.4 VDC F 20.7 VDC PREPARED BY 273-138 273-71**-**PRINTS TO MADE BY D.Mone Steam Turbine P3K-AL-0494-A02 DIV OR Mar. 30. 1976 ISSUED MAR 3 0 1976 Schenectady SH NO. Z CONT ON SHEET -LOCATION CODE IDENT NO 670

TITLE

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