GENERAL ELECTRIC

277A3792

CONT ON SHEET 2 SH NO. 1

REV NO.	TITLE
277A3792	Test Specifications
CONT ON SHEET 2 SH NO. 1	FIRST MADE FOR 44C331837

REVISIONS

STANDING INSTRUCTIONS

FOR

IMPEDANCE COMPENSATOR

PRINTED CIRCUIT BOARD

FOR

GENERREX SYSTEM

44C331837

Distribution:

1 QC Eng.
1 Test Area
1 Engineering

PRINTS TO
3EL1
4QA1
1RA2
4EK1
DL13

MADE BY	APPROVALS		
RK Gerlitz 790109	F R Parsons 1/10/79	Drive Systems DIV OR DEPT.	277A3792
ISSUED 1-12-79		Salem, VA USA LOCATION	CONT ON SHEET 2 SH NO. 1

CODE IDENT NO.

FF-803 WF (11-77)
PRINTED IN U.S.A.

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I. Test Equipment Required

A. Printed Circuit Board Test Setup - 44C931365.

B. Adaptor - Amp. Mod. II 30 Pin

C. Power Supply Cable

D. Patchboard PB-1

E. Drawings 44C306932 Elementary
44C331837 Assembly
44C931365 Elementary of Test Table

II. Connection

A. Connect the adaptor cable to 1PL and 3PL per lead markings.

B. Insert Patchboard # 1 in carrier of Universal Tester and close.

C. Connect DC voltmeter to BJ-1 Red (+) and Black (-).

III. Wire Check

Pin	To	TP	Resistance (Ohms)
25		2TP	95K to 105K
23		2TP	0
3		1TP	0

Visual Check

3R	8.2K
6R	8.2K
14R	4.3K
39R	10K
31R	5.6K
22R	6.2K
9R	4.99K
25R	7.5K
37R	10K
38R	5.6K
ⓘ 18R	10.K

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IV. Setup

A. Turn all switches to OFF or normal on both the UT and UPS.

B. Turn all power supplies to zero on the UT and all variacs to zero on the UPS.

C. Apply power to test stand.

D. Install board under test to adaptor.

V. Electrical Test

A. Remove LDC-DROOP jumper on Printed Circuit Board (PCB).

7-2-02 Q

MAIN 24 VOLTS NOT USED ON CARD ONLY ON FACTORY FIXTURE

B. Close "SW-1" and "SW-34". Depress "LPB-1" and adjust PS-1 to 24 ± 0.5 VDC at "BJ-1".
NOTE: Should it become necessary to remove all power from the PCB, open "SW-1".

+15 to Pin 1

C. Depress "LPB-2" and adjust PS-2 to 15 ± 0.1 VDC at "BJ-1".

-15 to Pin 5

D. Depress "LPB-3" and adjust PS-3 to 15 ± 0.1 VDC at "BJ-1".

E. Apply main power to Universal Power Supply (U.P.S.).

F. On the U.P.S. close 3Ø Power SW, Depress PB-1 and PB-20 and adjust Ø1 Balance variac to 17 ± 0.17 volts AC at L-N jacks.

G. Close "SW-10" (applies 17 volts AC to pins 11-3). Place RS-2 to Pos. 4 (connects 10K resistor pin 23-4). Place "RS-1" to position 1. Readjust Ø1 Balance variac if necessary for 17 ± 0.1 VAC at BJ-10.

meter +Pin 12
- Pin 11

H. Adjust 1P on the PCB completely CCW.

I. Adjust 7P on the PCB from CCW to CW.

7P	~~±2.87~~ (3)	8TP Voltage
CCW	~~2.69~~ 2.17	± 0.1 VAC
CW		4.35 ± 0.1 VAC

J. Adjust 7P on the PCB for -5 ± 0.05 Volts DC at 9TP.

K. Adjust 8P on the PCB completely CCW.

8P	(1)	5TP Voltage
CCW	3.75	~~3.85~~ 3.85 ± 0.2 VDC
C		6.3 ± 0.4 VDC (2)

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I. Adjust 8P on the PCB for 0 ± 0.02 VDC at 2TP. 5TP=5.0 ± 0.1 VDC

M. Open "SW-10" and turn Ø1 Balance variac to zero.

N. Place "RS-1" to position 4.

O. Close SW-5 and increase Ø2 balance variac to 10 ± 0.1 VAC at BJ-10. Make sure Ø1 Balance Variac is at zero. Jumper Pin 11 to Pin 3.

P. Adjust 2P (R pot) completely CW and adjust 3P from CW to CCW. Voltage at 5TP shall go from 0.450 ± 0.05 to 0.912 ± 0.05 VDC.

Q. Adjust 3P for 0.5 ± 0.005 VDC at 5TP.

R. Adjust 2P (R pot) completely CCW. Voltage at 5TP will go to 0 ± 0.02 VDC.

S. Connect jumper for LDC operation.

T. Connect phase angle indicator ref. input to 6TP and Signal input to 3TP and adjust 4P for 45° ± 1° lag. (3TP lags 6TP).

U. Connect phase angle indicator ref. input to 4TP and Signal input to 6TP. Adjust 5P for 90° ± 1° lag (6TP lags 4TP).

V. Adjust 1P (X) completely CW, then adjust 6P for 1.25 ± 0.01 volts D.C. at 5TP.

W. Connect an oscilloscope channel 1 (trigger) to 6TP. Connect channel 2 to 10TP. Shall be 180 ± 2° out of phase. Connect channel 2 to Droop Terminal. Shall be in phase with channel 1.
Remove jumper pin 11 to pin 3.

X. Turn Ø2 Balance Variac to zero. Open "SW-5" and close "SW-10". Place "RS-1" to position 1. Adjust Ø1 Balance Variac to 17 ± 0.1 VAC at "BJ-10". Place "RS-1" to Position 2. Adjust 3Ø variac to 10 ± 1 VAC at BJ-10.

Y. Adjust Balance variac until 9TP reads -5 ± 0.05 VDC (1). The voltage at 5TP should be 5.15 ± 0.1 VDC and BJ-12 shall be -0.6 ± 0.4 VDC

Z. Return all variacs to zero. Open or return to normal all switches on UPS and UT. Remove all instrumentation.

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