g		GE Energy		Functiona	al Testing Spe	cification
	Parts & Repa Louisville, KY			LO	U-GED-115D2227	'G4
	Test F	Procedure for a voltage	comparator	ard 115D2227	Gx series.	
DOCUI	MENT REVISION STATUS	Determined by the last entr	y in the "REV" a	nd "DATE" columr	1	
REV.		DESCRIPTION			SIGNATURE	REV. DATE
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DATE 06/14	/13	DATE	DATE		DATE 6/17/2013	

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	Louisville, KY	

1. SCOPE

1.1 This is a functional testing procedure for a voltage comparator card 115D2227Gx series.

2. STANDARDS OF QUALITY

2.1 Refer to the current revision of the IPC-A-610 standard for workmanship standards.

3. APPLICABLE DOCUMENTS

- **3.1** The following document(s) shall form part of this specification to the extent specified herein. Unless otherwise indicated, the latest issue shall apply.
 - 3.1.1 P3K-AL-0349-A01

4. **ENGINEERING REQUIREMENTS**

- 4.1 Equipment Cleaning
 - **4.1.1** Equipment should be clean and free of debris prior to applying power unless performing an initial check. Refer to site specific SRA's for cleaning guidelines.
- **4.2** Equipment Inspection
 - **4.2.1** Equipment should be visually inspected for any defects prior to applying power. This inspection should include the following as a minimum:
 - 4.2.1.1 Wires broken, cracked, or loosely connected
 - 4.2.1.2 Terminal strips / connectors broken or cracked
 - 4.2.1.3 Components visually damaged
 - **4.2.1.4** Capacitors bloated or leaking
 - 4.2.1.5 Solder joints damaged or cold
 - 4.2.1.6 Circuit board burned or de-laminated
 - 4.2.1.7 Printed wire runs / Traces burned or damaged

5. EQUIPMENT REQUIRED

5.1 The following equipment is required to perform the process requirements. Equipment may be substituted provided that all accuracy's and test ratios are equivalent or better.

Qty	Reference #	Description
1		Fluke 87 DMM (or Equivalent)
1		Dual Power Supply
1		Millivolt Source
1		Oscilloscope

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Testing Procedure

6.1.1 Follow Procedure Below

CONT ON	SHEEL 2 SH NO. 1 FIRST MADE FOR EHC MARK II
	WITH REVISIONS FOR G4
I.	SCOPE
	-
	This instruction outlines the test specifications for circuit board 115D222
	Groups 1, 2, and 3. (Schematics 115D2228, 133D6534, 137D5135 and 145D3820)
II.	CIRCUIT DESCRIPTION
	This circuit is used for all voltage comparison functions in the Mark II
	system with the exception of the Power Load Unbalance, Early Valve Actuation
S)	and Standby functions which have their own specialized voltage comparators.
è	
Ž	Each circuit board contains two identical Voltage Comparator functions.
Ĕ	The circuit, in general, looks at two input voltages and picks up a relay when one voltage exceeds the other in accordance with the following rules:
ō	when one vortage exceeds the other in accordance with the fortowing lates:
٣	TYPE A Relay (and LED) picks up when the voltage on Input #1 is more
S	TYPE A Relay (and LED) picks up when the voltage on Input #1 is more positive than the reference voltage which is connected on Input
J	#2.
4	TYPE B * Relay (and LED) picks up when the voltage on Input \$2 is more negative than the reference voltage which is connected on Input
d	#1.
€,	TYPE C Relay (and LED) picks up when the voltage on Input #1 is more
4	positive than the voltage on Input #2. (The internal reference
~	voltage is not used in this case).
,	
	The relay contects available per voltage comparator are two single pole
	double throw dry circuit contacts with 3.0 amp capacity at 28 VDC and resistance load.
	represente vosa.
	Circuit Description:
	This circuit, in general, consists of a high input impedance discrete
	component differential amplifier; an integrated circuit differential
	comparator; a transistor relay driver; a relay with bifilar coil and
	dry contacts; and a temperature compensated adjustable reference voltage
	with plus and minus capability.
	A balance potentiomete; is provided in the differential amplifier section
-	so that the firing point can be adjusted exactly in spite of small componen
	differences in each half of the amplifier. A hysteresis potentiometer is
	provided around the integrated circuit comparator to allow some adjustment
	of the difference between the pick-up point and drop-out point of the cir-
	cuit. Having some hysteresis also prevents the relay from chattering if the input voltage is holding near the reference voltage. In order to improve
,	noise immunity and to prevent false triggering on narrow pulses, two R-C
	filter networks have been included. One is on the input of the differentia
	amplifier and the other is on the input of the integrated circuit differen-
	tial comparator.
	LONG AND TO CONTROL THE CONTROL OF T
CPYRI	GHT 1983 GENERAL ELECTRIC CO.
	, and the same of
	Polnicek Scot. 14, 1977 APPROVALS Steam Turbine BN ON P. P3K-AL-0349-A01
SSUED	CTD 4 F 1079
	Schenectady, N.Y. LOCATION CORT ON SHEET 2 SH

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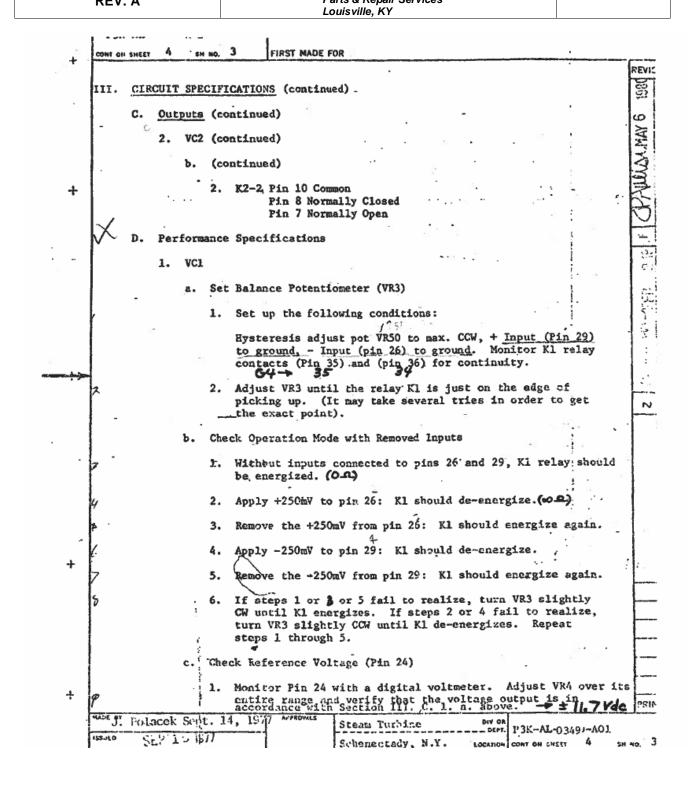
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	5°.	Р3К	-AL-	0349-	-VOT		1F21 TM	PIKOCITON2	FOK YOL	THEE CO	di vivi	K-DIUM	,,,,,,,	
	+	CON1 04	SMEET	3	\$H H	o. 2	FIRST MADE	FOR						Inc.
-		111.	. CIRCUIT SPECIFICATIONS											REVISI
			A.	Pow	Power Supply Requirements									
				1.		er Supply Pin 37	7 1: +22	.000 ± 0.0 at 150 ma						THE ,
				2.		er, Supply Pin 41	7 2: -22	.000 + 0.0 at 60 ma	O2 VDC					Villa .
	+		в.	Inp	ut S	ignal Lev	/els		N/					2
		1		1.	VC1									\square :
					a. b.			± 15.0 VDC ± 15.0 VDC			-			
				2.	VC2									2
		1.			a. b.			15.0 VDC 15.0 VDC						
			c.	Out	puts								•	2
				1.	VC1									134
Table Plant	+>	19			a.	Reference the range	ce Voltage ge <u>+</u> 11.7	(Pin 24) VDC with t	adjustal colerance	ble by 6 e <u>+</u> 5%.	changin ('2,	g VR4 o 28 – 1	ver 1,12)	200
	145	ĺ			ъ.	Relay K	L Contacts							n
						1. K1-		Common Normally (Normally (34 36			_	de les aux land
	:					2. Kl-		Common Normally (Normally (33				
				2.	VC2									
					. a.	Reference +	ce Voltage	(Pin 2) a with tole	adjustab rance <u>+</u> .	le by c 5%.	hanging	VR2- o	ver the	
	+				ъ.	Relay K	2 Contacts			-				
						1. K2-		Common Normally (Normally (_			~		
									0,000					-
							2 2		m 12					
														-
	+													PRINT
		MADE BY	Po1	acek	Sept 5 13	. 14, 19	77 APPROVALS	•	Turbine ctady, N		DIV OR _ 9CPT.	P3K-AL	-0349-A03	1. H NO 2
		L					i	1 2cueue	ccady, r		334134			

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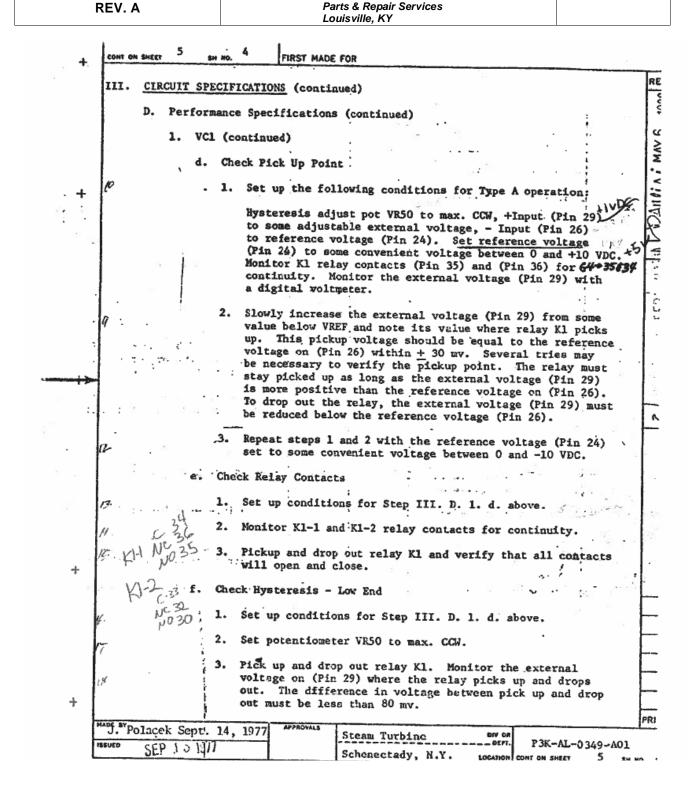
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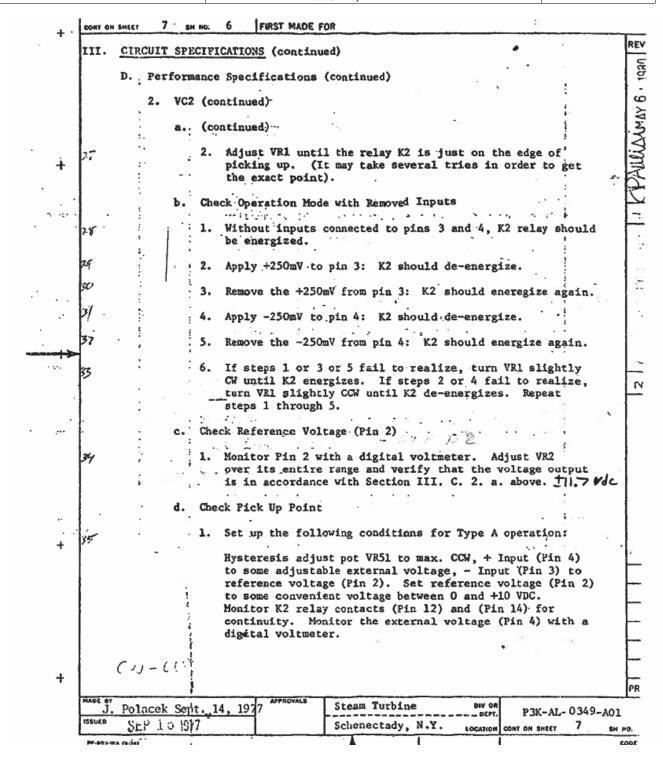
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+	CONT ON	\$HEET	6	\$H .	NO. 5 FIRST MADE	FOR		
	111.	CIR	CUIT	SPEC	CIFICATIONS (contin	ued) .		REVI
		D.	Per	forma	nce Specifications	(continued)	,:	1380
			1.	VC1	(continued)		;	9 A
	. (d		:	g.	Check Hysteresis -	High End	· · · . · . · . · . · . · . · . · . · .	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\
+	ti	Ť	!		 Set up conditi 	ons for Step III. D.	l. d. above.	
. ×	20	ı	:		2. Set potentione	ter VR50 to max. CW.		Z .
88	Żi			*	voltage on (Pi	op out relay Kl. Monin 29) where the relay in voltage between pin 125 mv.	picks up and drops	
				h.	Check Pickup Time		*	品
6)	27 -				Hysteresis adj to a voltage s can be switche	lowing conditions for ust pot VR50 to max. (ource and switch (S1) d from OV to +5V, - Ir age (Pin 24). Set ref	CCW, + Input (Pin) such that this input (Pin 26) to	put
-			•		to 1.0 V. Mon through the KI	itor DC voltage source -1 normally ôpen relay Trigger the oscillose	of nominally 24 to contacts with an	VDC -
***	23			·		bserve on the oscillos acts Kl-1 to close. 7 0 ms.		
		- 1		i.	Check Drop Out Tim		l	. 8
	24			rei.	 This test can above with the 	be performed at the s same set up.	same time as III.	D. 1. h.
+	25				it takes for r	and observe on the ose clay contacts K1-1 to ess than 34 ms.	open. The drop or	ne ut
			2.	VC2			ķ9:	70.
P57	X.				Set Balance Potnet: 1. Set up the fol:	iometer (VR1)	. 1.1 5.1	183
+	01.8 810	Ì			to ground, - In contacts (Pin	ust pot VR51 to max. (nput (Pin 3) to ground (Pin 14) for c	. Monitor K2 rela	4) ay
7	ISSUE0	Pola	cek s SEP	ept.	14, 197	Stcam Turbine	P3K-AL-03	
						Schenectady, N.Y.	LOCATION CONT ON SHEET	6 \$H NO. 1

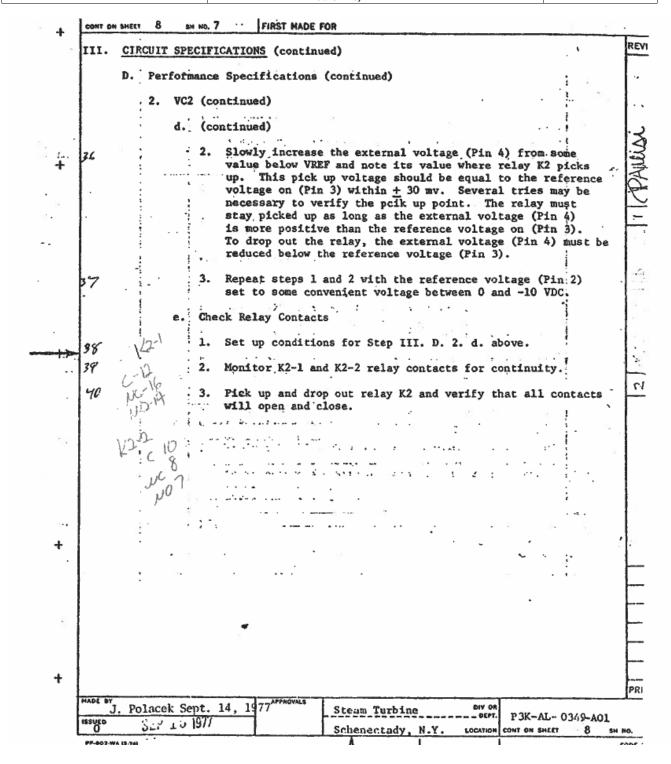
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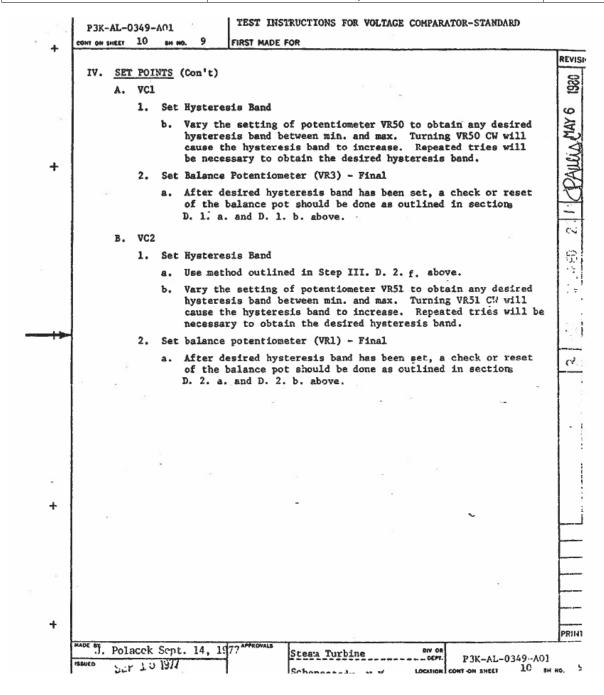
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· 🛕	P3K-A		9	\$H NO.	8	FIRST MADE FOR	
·	III.	CTR	CHIT	SPEC	TFIC	TIONS (Con't)	REVIS
		D.				pecifications (Con't)	1:25
				VC2		,	9
			2.		Checl	: Hysteresis - Low End	₹
	u.					Set up conditions for Step III. D. 2. d. above.	13
	242_					Set potentiometer VR51 to max. CCW.	13
+	43				3.	Pick up and drop out relay K2. Monitor the external voltage on (Pin 4) where the relay picks up and drops out. The difference in voltage between pick up and drop out must be less than 80 mv.	+ CPALLICALMAY 6
-	!		- į	g.	Chec	Hysteresis - High End	92
	44		-		1.	Set up conditions for Step III. D. 2. d. above.	~
	45		- 1		2.	Set potentiometer VR51 to max. CW.	65
	46			¥7		Pick up and drop out relay K2. Monitor the external voltage on (Pin 4) where the relay picks up and drops out. The difference in voltage between pick up and drop out must be greater than 150 mv.	1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1
				h.	Chec	k Pick Up Time	1.
	47				1.	Set up the following conditions for Type A operation:	È
	•	-			į	Hysteresis adjust pot VR51 to max. CCW, + Input (Pin 4) to a voltage source and switch (S1) such that this input can be switched from OV to +5V Input (Pin 3) to reference voltage (Pin 2). Set reference voltage (Pin 2) to +1.0 V. Monitor a D.C. voltage source of nominally CV VDC through the K2-1 normally open relay contacts with an oscilloscope. Trigger the oscilloscope with the output of external switch S1.	1
	48				2.	Close SI and observe on the oscilloscope the time it takes for relay contacts K2-1 to close. The pick up time must b less than 18 ms.	e
				i.,	Che	ck Drop Out Time	1
+	48				1.	This test can be performed at the same time as III. D. 2. g. above with the same set up.	-
Ŀī	50				2.	Open switch S1 and observe on the oscilloscope the time it takes for relay contacts K2-1 to open. The drop out time must be less than 34 ms.	
	ıv.	SE	T PO	INTS	_	tes V	
		Α.	VC	1		•	
			1.	Set		teresis Band	
+				a.	Use	method outlined in Step III. D. 1. f. above.	PRI
	MADE DY.	Pola	cek	Sept.	14,	1977 APPROVALS Steam Turbine DIV OR P3K-AL-0349-A01	
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6.2 ***TEST COMPLETE ***

7. Notes

7.1 None at this time.