



GE Power Generation Engineering

Materials and Processes Engineering
Schenectady, NY 12345

PROCESS SPECIFICATION

P3K-AL-0387-A01

TEST INSTRUCTIONS FOR POWER LOAD UNBALANCE TRANSFORMER CIRCUIT BOARD

DOCUMENT REVISION STATUS: DETERMINED BY THE LAST ENTRY IN THE "REV" AND "DATE" COLUMN

REV.	AN NO.	DESCRIPTION	SIGNATURE	REV. DATE
A	YA00096	SPECIFICATION LISTED IN STEAM TURBINE/GENERATOR INDEX AS "INACTIVE" HAS BEEN FORMALLY REVISED AS "INACTIVE FOR NEW DESIGN". (PR BUDKA)	C.R. Tripp	DEC 02 1991
<div>INACTIVE FOR NEW DESIGN AS OF 12/02/91</div>				

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PREPARED BY: P.R. BUDKA

ORIG. ISSUE DATE: --

REV. NO. A	TITLE TEST INSTRUCTIONS FOR POWER LOAD UNBALANCE TRANSFORMER CIRCUIT BOARD 1PU1-B101		CONT ON SHEET 3	SH NO. 2
P3K-AL-0387-A01		FIRST MADE FOR EHC MARK II (ASS'Y DRAWING 118D1318 G1)		
CONT ON SHEET 3				SH NO. 2
<p>I. <u>CIRCUIT DESCRIPTION</u></p> <p>This circuit board contains three current transformers 5A/3.88 ma (RMS) 45 to 70 HZ, which provide the current signals to the Power Load Unbalance.</p> <p>Zener diodes are connected across the transformer outputs to limit the output voltage to 120V should the load circuits accidentally open.</p> <p>II. <u>CIRCUIT SPECIFICATIONS</u></p> <p>1. Transformer Ratios</p> <p>A 5A RMS, 60 HZ sine wave input should produce a 3.88 ± 0.08 ma RMS sine wave output into a load of 1.4 to 5.8K ohms.</p> <p>2. Output Voltage Limits</p> <p>The maximum value of the output voltage of each transformer with 5A RMS input and no load connected across it (open-circuited) should be $120V \pm 20\%$ (both polarities).</p> <p>THIS DOCUMENT INCLUDING THE INFORMATION IT CONTAINS IS CONFIDENTIAL AND PROPRIETARY TO GENERAL ELECTRIC COMPANY AND IS MADE AVAILABLE SOLELY TO YOU RESPONSE TO AN INQUIRY OR MAKE A BID AS A POTENTIAL VENDOR OR IF PERFORM A CONTRACT WITH GENERAL ELECTRIC. IT MAY NOT BE REPRODUCED OR COPIED AND SHALL BE RETURNED IMMEDIATELY UPON REQUEST. RECipient WILL TAKE ALL REASONABLE STEPS TO PROTECT THIS DOCUMENT AND THE INFORMATION IT CONTAINS.</p> <p>COPYRIGHT 1983 GENERAL ELECTRIC CO.</p>				REVISIONS
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ISSUED JAN 15 1975		Schenectady, N.Y.	LOCATION	CONT ON SHEET 3 SH NO. 2

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TITLE
TEST INSTRUCTIONS FOR POWER LOAD UNBALANCE TRANSFORMER CIRCUIT BOARD 1PUL-B101
FIRST MADE FOR EHC MKII

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TEST INSTRUCTIONS 1PUL-B102

Assembly 118D1318

Schematic 173B6549

PROCEDURE:

1. Refer to Fig. A for test circuit.
2. Refer to Fig. B for equipment list and setup instructions.

NOTE: All settings and specifications are + 1 MV unless otherwise specified.

3. Apply load of 1.4K at RL1, RL2 and RL3.
4. Apply 5A RMS into transformer primaries.
5. Measure 5.320 to 5.544 VRMS across each load.
6. Remove input current.
7. Change loads to 5.8K at RL1, RL2 and RL3.
8. Apply 5A RMS into transformer primaries.
9. Measure 22.040 to 22.968 VRMS across each load.
10. Remove input current.
11. Remove loads.
12. Apply 5A RMS into transformer primaries.
13. Measure 96.0 to 144.0 VRMS across each load.
14. Remove input currents.
15. End of Test.

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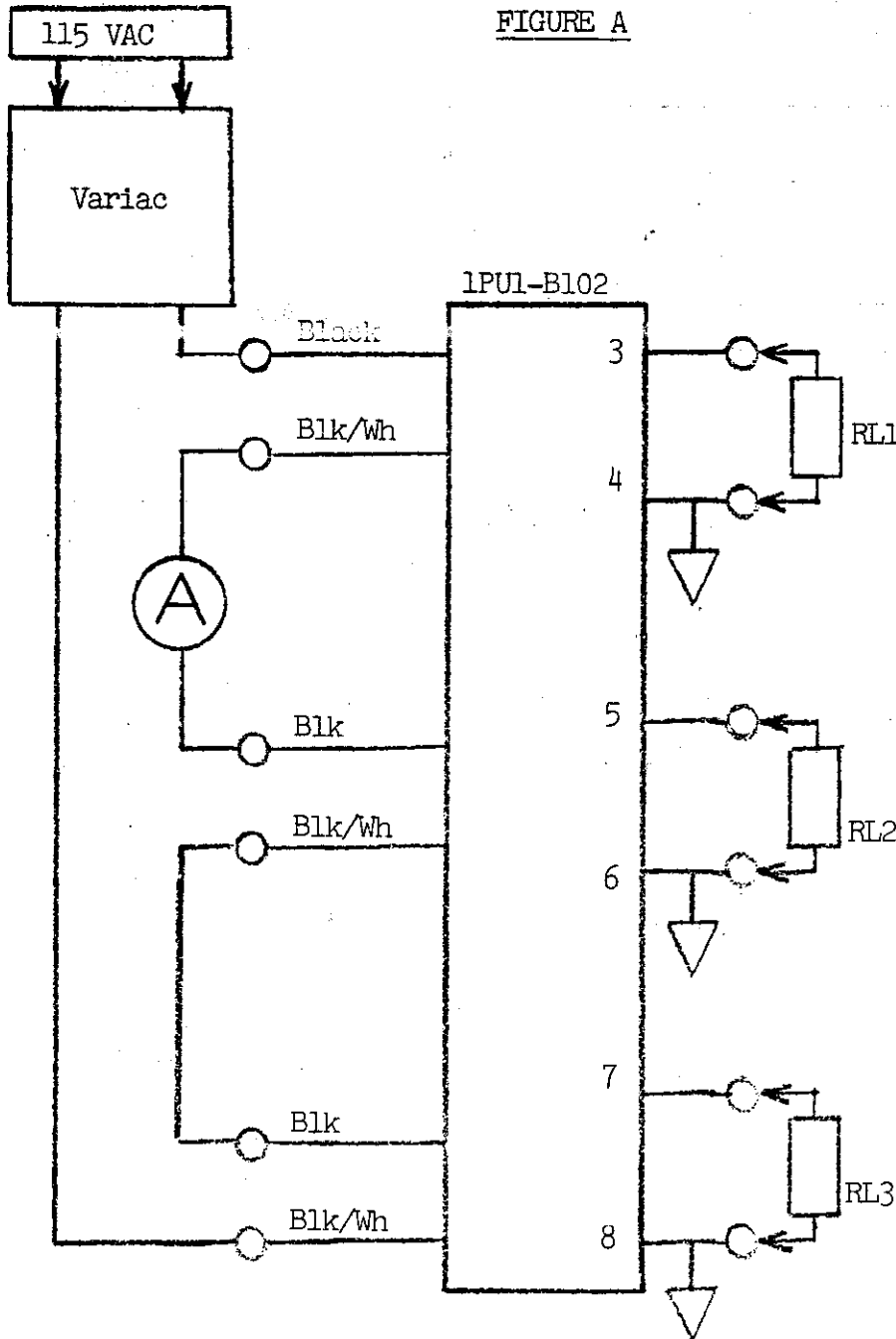
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 TEST INSTRUCTIONS FOR POWER LOAD UNBALANCE
 TRANSFORMER CIRCUIT BOARD 1PUL-B101
 FIRST MADE FOR EHC MKII

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FIGURE A



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FIGURE B

EQUIPMENT NEEDED

- A. DVM AC/DC SD7205A
- B. Variac
- C. AC Ammeter 0 - 10A

SETUP INSTRUCTIONS

1. The load to be applied to the circuit under test is $\pm 1\%$ tolerance.
2. Use the variac to adjust the AC input current.

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TEST INSTRUCTIONS FOR POWER LOAD UNBALANCE
TRANSFORMER CIRCUIT BOARD 1PU1-B101

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SH NO. 6

FIRST MADE FOR EHC MARK II

REVISIONS

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DATE

2/20/74

D. Economou

EHC DESIGN ENGINEERING

APPROVED BY

P.C. Callan

DATE

1-13-75

P.C. Callan - MANAGER

EHC DESIGN ENGINEERING

TEST PROCEDURE

PREPARED BY

C.A. Bugg

DATE

12/23/74

C. Bugg

EHC TEST ENGINEER

TEST PROCEDURE

APPROVED BY

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1/6/75

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EHC DESIGN ENGINEERING

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MADE BY

D.Mone Jan. 15, 1975

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