

CONT ON SHEET 2 SH NO.

CODE IDENT NO.

FF-803 WF (11-77)
PRINTED IN U.S.A.

CARL

REV NO. 2 7 8 A 3 0 7 2
CONT ON SHEET 6 SH NO. 5

TITLE TEST SPECIFICATIONS
FIRST MADE FOR 3S7700PB103B1

- I. TEST EQUIPMENT Addendum to TI# 278A3072. This portion is to be used when testing with the new CDO test console.
- II. VISUAL *Very small value readings may be wrong if using common point because of voltage drop. Take from actual component specified*
- III. SET-UP
1. Set all pots CCW
 2. Connect +15VDC to Pin B
Connect -15VDC to Pin U
Connect common to Pin N
 3. Connect an oscillator to Pin L with common to Pin N.
Set for $60 \pm .1$ Hz sinewave. *(16.66 msec)*

IV. TEST (All AC voltages are RMS unless noted)

1. Set oscillator for zero output
2. Apply ± 15 VDC. Pin E should be $1.0 \pm .1$ VDC
3. Set oscillator for $60 \pm .1$ Hz sinewave and $0.8 \pm .005$ VAC.
Adjust R2 for $10 \pm .005$ VDC at Pin E.
4. Set oscillator output for zero. Set R29 for $2 \pm .005$ VDC at Pin E.
5. Repeat 3 and 4 until limits are met *1.647*
6. Set oscillator for zero output. Pin S should be 1.4 to 1.9 VDC.
Orange jack should be 1.4 to 1.6 VDC. 0V for 103F PK 103A
7. Connect a scope to Pin S, common to Pin N. Increase the oscillator output until Pin E reads $12 \pm .1$ VDC. The waveform should be a smooth sinewave with no clipping.
(oscillator should be $1.02 \pm .1$ VAC. *2.88 P-P*)
8. Verify following chart:

| OSCILLATOR OUTPUT(60Hz) | PIN E VDC | R+, P- MVDC | F+ N- MADC | T+, V- MADC |
|-------------------------|----------------|---------------|---------------|---------------|
| 0 VAC $\pm .01$ | 2.0 $\pm .05$ | 8 ± 1 MV | .2 $\pm .01$ | 1.0 $\pm .01$ |
| 0.2 " | 4.0 $\pm .1$ | | | |
| 0.4 " | 6.0 $\pm .1$ | | | |
| 0.6 " | 8.0 $\pm .1$ | | | |
| 0.8 " | 10.0 $\pm .05$ | 40 ± 1 MV | 1.0 $\pm .01$ | 5.0 $\pm .01$ |
| 0.88 " | 10.8 $\pm .05$ | | | |

9. Frequency Response - Verify following chart:

| OSCILLATOR OUTPUT(+.001VAC) | OSCILLATOR FREQ(+ .1Hz) | PIN E VDC |
|-----------------------------|-------------------------|------------|
| 1.20VAC (RMS) | 120 <i>8.33 msec</i> | 7.9 to 8.1 |
| 0.60 | 60 <i>16.6 msec</i> | 7.9 to 8.1 |
| .300 | 30 <i>33.3 msec</i> | 7.9 to 8.1 |
| .153 | 15 <i>66.6 msec</i> | 7.8 to 8.2 |
| .123 | 12 <i>83.3 msec</i> | 7.8 to 8.2 |
| .103 | 10 <i>100 msec</i> | 7.7 to 8.3 |
| .074 | 7 <i>142 msec</i> | 7.6 to 8.4 |

*for 103B
103F has low
freq comp. PK*

MADE BY Gene Post
ISSUED 5/29/81

APPROVALS *Dcl*

DRIVE SYSTEMS
SALEM, VIRGINIA

DIV OR DEPT. LOCATION
2 7 8 A 3 0 7 2
CONT ON SHEET 6 SH NO. 5

2 7 8 A 3 0 7 2

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| 2 7 8 A 3 0 7 2 | | TEST SPECIFICATIONS | | CONT ON SHEET | | Fl. | SH NO. | 6 |
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| <p>10. Remove oscillator from Pin L and connect a decade box from pin L to Pin N.</p> <p>From 0-50 ohm D4(LED) Shall be lit</p> <p>From 110-500 D4 Shall not be lit</p> <p>750 or greater D4 Shall be lit</p> <p>11. Remove all power.</p> | | | | | | | | REVISIONS |
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APPS