

REV. NO. 1
68A999191
CONT ON SHEET 2 SH NO. 1

TITLE
TEST SPECIFICATION
GATE PULSE GENERATOR
FIRST MADE FOR IC3600TPGD1

CONT ON SHEET 2 SH NO. 1

TEST SPECIFICATION

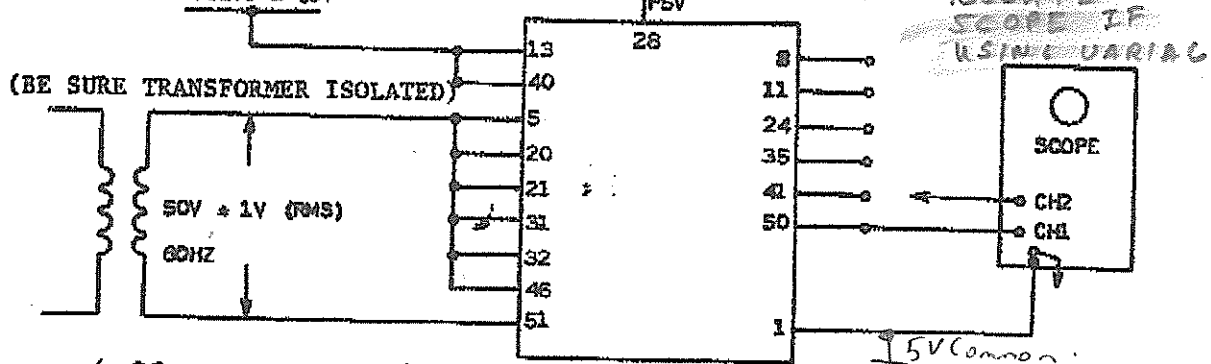
ELEMENTARY - IC3600TPGD, SH. 3.0

NOTE: "REMOVE AC VOLTAGE BEFORE INSERTING OR REMOVING CARD FROM TEST FIXTURE"

1. BEFORE APPLYING POWER TO CARD, USE DIGITAL VOLT/OHM METER TO VERIFY THE FOLLOWING RESISTANCES: (OMIT THIS STEP IF FIXIT TEST HAS BEEN PERFORMED)

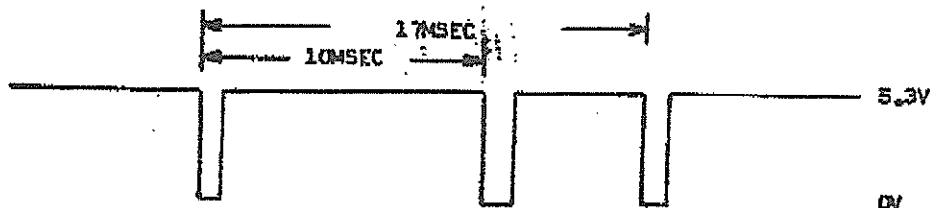
- | | |
|---------------------------|----------------|
| A) BETWEEN PINS 13 AND 20 | 36.8KΩ ± 1KΩ |
| B) BETWEEN PINS 40 AND 20 | 36.7KΩ ± 1KΩ |
| C) BETWEEN PINS 5 AND 46 | 52.4KΩ ± 1.5KΩ |
| D) BETWEEN PINS 5 AND 21 | 52.4KΩ ± 1.5KΩ |
| E) BETWEEN PINS 31 AND 21 | 52.4KΩ ± 1.5KΩ |
| F) BETWEEN PINS 31 AND 32 | 52.4KΩ ± 1.5KΩ |
| G) BETWEEN PINS 32 AND 20 | 52.4KΩ ± 1.5KΩ |
| H) BETWEEN PINS 20 AND 46 | 52.4KΩ ± 1.5KΩ |

PS VOLTAGE ± .3V



(USE AMX CARD #1)

2. (A) SET SCOPE TRIGGER FOR (≈) INTERNAL, 50 μSEC/DIV., CHOPPED. LEADING EDGE OF ALL 0 PULSES AT OUTPUT PINS 8, 11, 24, 35 AND 41 MUST BE WITHIN 20 MICRO-SECONDS OF OUTPUT AT PIN 50.
- (B) SET SCOPE TO 100 μSEC/DIV., AND OBSERVE THAT ALL OUTPUT PULSES ON PINS 8, 11, 24, 35, 41 AND 50 ARE BETWEEN 32 TO 100 MICRO-SECONDS AT THE 1 VOLT LEVEL.
- (C) SET SCOPE TO 2μSEC/DIV. OBSERVE 3 PULSES ON SCOPE,



RAISE REF. AND BIAS VOLTAGE ON PINS 13 AND 40 TO 10 VOLTS. OBSERVE THAT MIDDLE PULSE DISAPPEARS.

See NOTES

REVISION

ACV REH REH

1. 31/21/69
2. 4/25/70
3. 8/3/70

4. 4/29/71
5. 5/22/73
6. 2-21-75

JMT

40A3

1338

2520

PRINTS TC

MADE BY

R. E. MANNAH

APPROVALS

M.B.J.

DRIVE SYSTEMS

DIV OR DEPT.

68A999191

SALEM, VIRGINIA

LOCATION

CONT ON SHEET 2 SH NO. 1

CODE 10000000

68A999191

REV
NO.

TITLE

CONT ON SHEET FL. SH NO. 2

68A999191

TEST SPECIFICATION
GATE PULSE GENERATOR

CONT ON SHEET FL. SH NO. 2

FIRST MADE FOR IC8800TPED1

TEST SPECIFICATION (AUX CARD #2)

3. REMOVE ALL AC INPUTS AND DC INPUT TO PINS 13 AND 40. CHECK LOGIC OF ALL 6 CHANNELS AS FOLLOWS:

(DEPRESS PB 1C1)

A. MOMENTARILY CONNECT PIN 26 TO PSV AND THEN CONNECT TO COMMON. TIE POINT OGP COM TO COMMON.

(DEPRESS PB 2C)

B. OBSERVE OUTPUT AT POINT OGP WHEN POINT S IS MOMENTARILY CONNECTED TO PSV. OUTPUT MUST BE LESS THAN .5 VOLTS.

(DEPRESS PB 3C)

C. MOMENTARILY DISCONNECT PIN 26 FROM COMMON. OUTPUT MUST INCREASE TO MORE THAN 2.5 VOLTS.

(DEPRESS PB 4WI)

D. CONNECT W. I. INPUT TO COMMON. OUTPUT OGP MUST BE LESS THAN .5 VOLTS.

E. REPEAT STEPS A THROUGH D FOR ALL INPUTS AND OUTPUTS PER THE TABLE BELOW.

POSITION OF
BOTH ROTARY
SWITCHES

	CIRCUIT	(PSV) S	(METER) OGP	(COM) V.I.	(OGP) COM
3	P1	PIN 20	PIN 2	PIN 9	17
1	P2	PIN 5	PIN 10	PIN 16	2
2	P3	PIN 31	PIN 17	PIN 25	10
6	N1	PIN 21	PIN 36	PIN 37	43
4	N2	PIN 32	PIN 38	PIN 42	36
5	N3	PIN 46	PIN 43	PIN 49	38

REVISION

CLG

1 - 2-21-75
2) 8MAY86 JMT

40A3

1338

2520

PRINTS TO:

MADE BY

R. E. HANNAH

APPROVALS

H.B.J.

DRIVE SYSTEMS

ENV OR
DEPT.

SALEM, VIRGINIA

LOCATION

68A999191

CONT ON SHEET FL. SH NO. 2

CODE IDENT NO.

FF-805 WF G-74
PRINTED IN U.S.A.

F-8-4

IC8800TPED1 68A999191 68A999191 68A999191