



# GENERAL ELECTRIC

P3K-AL-0654

CONT ON SHEET 1

REV NO. 17

P3K-AL-0654-A01

TITLE

TEST SPECIFICATIONS FOR THE 125V  
RELAY INTERFACE CIRCUIT BOARD 1M2-B001

CONT ON SHEET 4

SH NO. 3

FIRST MADE FOR EHC MARK II

## III. SPECIFICATIONS (continued)

3. The solid state "front end" functions as a 1 out of 2 logic circuit. Therefore, each channel must be independently checked. Using an IC test clip, verify that pin 5 of each optical isolator goes low (.8V or less) when the circuit has been energized and that when de-energized, that point goes high (4.5 or more). Be careful not to short any of the pins of the optical isolator together.
4. Each relay circuit must also be individually checked such that each can operate without falsely energizing any of the others, i.e. via excessive relay noise, high voltage arcing or solder bridges.

PRINTS TO

MADE BY

J. AULISI 18 AUG. 1981

ISSUED

AUG 18 1981

APPROVALS

Steam Turbine

DIV OR  
DEPT.

Schenectady, N.Y.

LOCATION

P3K-AL-0654-A01

CONT ON SHEET 4

SH NO. 3

CODE IDENT NO.

# GENERAL ELECTRIC

P3K-AL-0654-A01

REV NO. QA

TITLE  
TEST SPECIFICATIONS FOR THE 125V  
RELAY INTERFACE CIRCUIT BOARD 1M2-B001

CONT ON SHEET — SH NO. 4

P3K-AL-0654-A01

CONT ON SHEET — SH NO. 4

FIRST MADE FOR EHC MARK II

REVISIONS

PREPARED BY: RJ Wood DATE 8-11-81

R.J. Wood  
EHC DESIGN ENGINEERING  
Building 285 Room 231

APPROVED BY: RP Olson DATE 8/17/81

R.L. Olson - Manager  
EHC DESIGN ENGINEERING  
Building 285 Room 231

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SH NO. 4