CONT ON SHEET 2 sh NO. 1

TITLE

BOARD TEST SPECIFICATION - LOSS OF 22VDC POWER -1P3-D001 - SCHEMATIC 173B6502

P3K-AL-0324-A01 sh NO. 1 CONT ON SHEET

FIRST MADE FOR

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Purpose. l.

- The purpose of this specification is to convey the proper information needed to test the referenced circuit board for its proper function.
- To enable the skilled Test Engineer to produce the detailed Test Procedure for the referenced circuit board.
- 2. Documents pertaining to this specification.
 - Loss of 22VDC Power 117D9154.
 - AC/DC Power Ground System 117D7762.

C.

- Equipment needed to test.
 - DC Power Supply 24VDC nominal, 1 ampere output.
 - DC Power Supply 0 to 28 VDC nominal, 5 ampere output. В.
 - DC Voltmeter 50 $\pm 2\%$ VDC, 20,000 Δ /volt
 - Switch SPST 1 ampere break capacity.
 - Switch \$P3T 1 ampere break capacity.
 - 2 indicating devices 24 VDC nominal, 80 ma current rating each. F.

G.

Circuit Description.

This circuit consists of two voltage sensors which will energize relays whenever the input voltage connected to them exceeds 22 VDC.

5. Circuit Integration.

The output voltages of the +22VDC and -22 VDC Power Supplies must be monitored and displayed whenever either fails or falters. This card contains the sensors which will detect a staggering system voltage and relay this information to the Malfunction Bus.

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P3K-AL-0324-A01

CONT ON SHEET 3

TITLE

P3K-AL-0324-A01

BOARD TEST SPECIFICATION - LOSS OF 22VDC POWER -1P3-D001 - SCHEMATIC 173B6502

CONT ON SHEET 3

sh No. 2

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- Test Specification. 6.
 - A. Connect the board as shown in Figure 1.
 - Energize PS1 and set to 24 \pm 1 VDC. Energize PS2 and set to 0 VDC.
 - C. Close Sl. Indicators 1 and 2 shall indicate. Set S2 to position 2.
 - D. With the DC Voltmeter connected between TP51 (+) and TP50 (-), increase the voltage output of PS2 at a rate of 1 volt/second.
 - E. When the voltage between TP51 and TP50 reads 20 + 1, 0 volts, indicator 1 shall not indicate, indicator 2 shall continue to indicate.
 - F. When the voltage between TP51 and TP50 reaches 28 VDC, begin to decrease the PS2 voltage toward zero at the rate of 1 volt/second.
 - G. When the voltage between TP51 and TP50 reaches 20 + 0, -1 volts, indicator 1 shall indicate.
 - Reduce the voltage of PS2 to zero.
 - I. Set S2 to position 3.
 - J. Repeat steps A thru H except read indicator 2 for indicator 1, TP53 (+) for TP51 (+).
 - K. De-energize PS1 and PS2.
- 7. Report Sheet.

Indicator	Indicated	<u>Volts</u>
1 up 1 down		
2 up 2 down		

8. Notes.

If either of the indicators do not indicate at the voltage specified, the board is to be rejected.

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P3K-AL-0324-A01 GENERAL (%) ELECTRIC CONT ON SHEET TITLE Board Test Specification - Loss of 22 VDC Power P3K-AL-0324-A01 1P3-D001 FIRST MADE FOR SH NO. CONT ON SHEET REVISIONS TEST INSTRUCTIONS 1P3-D001 Assembly 117D9154 Schematic 173B6502 PROCEDURE: 1. Refer to Fig. A for test circuit. 2. Refer to Fig. B for equipment list and setup instructions. NOTE: All settings and specifications are + 1MV unless otherwise specified. 3. All switches down. Ll and L2 are ON. Sl and S2 up. Measure voltage from TP51 to TP53. Increase the output of voltage source until VSl picks up (indicated by L1) the pickup and dropout voltage must lie in the range +20.00 to 21.00 VDC. During this test, it is important that the voltage source be NOTE: ramped slowly.((<.1V/s)) S1 and S2 down. S3 and S4 up. Measure voltage from TP53 to TP50. Increase the output of the voltage source until VS2 picks up (indicated by L2). The pickup and dropout voltage must lie in the range of +20.00 to +21.00 VDC. 8. End of test.

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