REVIOL.

SCOPE I.

This instruction outlines the tests specifications for circuit board 1L2-B005 (Ref. Drawing 137D5751). System Schematic 137D6747 (S&L MSV) with CV test bias.

II. CIRCUIT DESCRIPTION

This version of SPF logic circuit board is used on turbines having SPF during control valve testing only. The switching signal which introduces the SPF and CV test bias signals to the CV amplifier is given directly by the CV test circuits.

This board contains only one relay which is energized under certain external conditions and permits the testing of control valves.

This arrangement permits the conversion of the turbine operation (addition of SPF operation during normal operation), by simple replacement of circuit boards.

III. CIRCUIT SPECIFICATIONS

Apply ground to pin 40 and +24 VDC to pin 38. The board should not draw current at any time.

Connect 24V 20mA lamps to pins 13, 19 and 36.

The Logic and path continuity should conform with the following table:

INPUTS AT PINS			CORRECT OUTPUTS AT PINS						
37	35	12		36	1.3	19			
0	0	0		0	0	0			
0	1	0		0	0	0			
1	1	0		1	0	0			
1	0	0		0	0	0			
0	0	0	÷	0	0	0			
0	0	1		0	1	1			
0	0	0		0	0	0			

1: 24V or lamp off

0: OV or lamp on

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PRINTS

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Steam Turbine Schenectady, N.Y.

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