MARK III PC BOARD TEST

REV.C SHEET 1 of 1

| | 0 8 9 | ASSEMBLY DRAWING | PC BOARD DRAWING | |
|-------|---|---|--|--|
| .u# | - 10 SF SE | 7130037001 | 4136J34-2 | SERIAL # |
| 0# | | SCHEMATIC DRAWING | TEST KIT | DATE TESTED |
| 1.0 | | 4:30=23 | Universal Test | TESTER |
| 0.0 | | T. J & - L J | ANTAGESST 1520 | 7.10 |
| | 1.0 INSPECTION | 2 (0) | 5 0 0 0 | |
| | 1.0 INSPECTION | - | :00) | |
| W 12 | .1 IDENTIFICAT | rion3 soi | LDER/WIRE | .5 KEY SLOT |
| 5 | .2 COMP./ CON | N4 TE | TP CYCLE | .6 |
| | | | | m y |
| | | | E 150 150 150 150 150 150 150 150 150 150 | .7 |
| - 6 | REMARKS: Coses sim | ace 122 is mit | | 3 3 35 |
| | | , , , | | |
| A N | Ch. | eck for jump | ers at cr | 14CRZ |
| | | a second die V | | |
| 2.0 E | REPARATION | | | |
| | * | 7 7 A46 A | | |
| ei li | | | | |
| 2.1 | CONNECT PER FIGURE 1. | (USE WATE AOZ RO | h Dog prob | e) |
| | CHECKOUT | 102 | m use | 2 11 5 |
| | CHECKOUI | AUZ NO | w/ owe | nd of one |
| *1 | (REFERENCE SCHEMATIC 4 | 136.123 FOR THIS STEDY | | 150% |
| 1 | SET OSCILLATOR FOR | 1 VRMS SINE WAVE, 4160 | | |
| | AS NECESSARY TO CAUS | E THE AC VOLTAGE AT TP2 | TO PEAK (APOINT) OF | NDMC) |
| 3.2 | SET OSCILLATOR AT 2000 | | | |
| 12020 | CONNECT PIN 32 TO -5 | AND THE RESERVE AND ADDRESS OF THE PARTY OF | lottage will peak and | |
| | WHILE PUSHING RST MO | | could be bighen | peak |
| 4 | TP1 SHOULD READ +24 | | | |
| | TP3 SHOULD READ -2 T | 0 -6 VDC. | of the Your endined | Name of the last o |
| | TP5 SHOULD READ APPR | | | |
| 200 | PIN 40 SHOULD READ L | ESS THAN 0.4 VDC. | | |
| | SET THE OSCILLATOR TO | O V.RMS. | | |
| 3.3 | | | the state of the s | |
| 3.3 | TPS SHOULD READ + 1 | 4.00 V.DE. | | 100 |
| | PIN 40 CHOULD DEAD 1 | BEE THAN D. HYDE | . American de la comp | I mall |
| 3.4 | PIN 40 SHOULD READ L | ess than D. 4 voc Ble Dl. Source and Apply | batween - 6.1 V AND 7 | LVON MISS |
| | PIN 40 SHOULD READ L CONNECT PIN 32 TO VARIO TP5 WILL GO TO -13.3 | ess than 0.440C Be Dl. Source and Apply O and Pin 40 well go | TO + 24.00 DC | |
| | PIN 40 SHOULD READ L. CONNECT PIN 32 TO VARIO TPS WILL GO TO - 13.3 REMOVE VARIABLE DA | ess than 0.440C bee DC. Source and Apply o and pin 40 wal go c. source | / batween - 6.1 V AND 7 | LVON MISS |
| | PIN 40 SHOULD READ L. CONNECT PIN 32 TO VARIOTPS WILL GO TO -13.3 REMOVE VARIABLE DA SET OSCILLATOR AT 3600 | ESS THAN D.440C BEE DC. SOURCE AND APPH O AND PIN 40 WWI GO C. SOURCE HZ, 1.40 VRMS. | 10 + 24,00 DC. | 2/2/88 |
| 3.4 | PIN 40 SHOULD READ L. CONNECT PIN 32 TO VARIOTPS WILL GO TO - 13.8 REMOVE VARIABLE DA SET OSCILLATOR AT 3600 USING A COUNTER AND 1 | ESS THAN D. 4 VOC LEE DL. SOURCE AND APPL O AND PIN 40 WWI 90 C. SOURCE HZ, 1.40 VRMS. RST PB AS NECESSARY. RA | ISE THE OSCILLATOR F | 2/2/88 |
| 3.4 | PIN 40 SHOULD READ L. CONNECT PIN 32 TO VARIOTIES WILL GO TO -13.8 REMOVE VARIABLE DA SET OSCILLATOR AT 3600 USING A COUNTER AND 1 AND VERIFY THAT TP1 1 | ESS THAN D. 4 VOC LEE DL. SOURCE AND APPL O AND PIN 40 WWI 90 C. SOURCE HZ, 1.40 VRMS. RST PB AS NECESSARY, RA DROPS TO 0 VDC AT 4060 | ISE THE OSCILLATOR F | REQUENCY 3/2/88 |
| 3.4 | PIN 40 SHOULD READ L CONNECT PIN 32 TO VARIO TPS WILL GO TO -13.3 REMOVE VARIABLE DA SET OSCILLATOR AT 3600 USING A COUNTER AND I AND VERIFY THAT TP1 I VARY OSCILLATOR OUTPUT | ESS THAN D. 4 VOC BLE DL. SOURCE AND APPL O AND PIN 40 WWI 90 C. SOURCE HZ, 1.40 VRMS. RST PB AS NECESSARY, RA DROPS TO 0 VDC AT 4060 LEVEL FROM 1.2 VRMS TO | ISE THE OSCILLATOR F | REQUENCY 3/2/88 |
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use pleade lap Box. Ho thou entire test then select C3, C4, C5, C10 as sequired to thath cap of Box. 4Pl
12/23/92