GENERAL (%) ELECTRIC

2 7 8 A 3 O 1 6

REVISIONS

CONT ON SHEET 3 SH NO. 2

278A3016

TEST SPECIFICATIONS

CONT ON SHEET 3 SH NO. FIRST MADE FOR 44C331830-G01

Test Equipment Required

- Printed Circuit Board Test Setup 44C931365.
- Power Supply Adaptor cable.
- C. Adaptor - Amp. Mod. II 30 Pin.

TITLE

- Patchboard #PB-1.
- E. Drawings

440306942 440331830

Elementary

Assembly

440931365

Elementary of Universal Power Supply

II. Connection

- Connect the Amp. Mod. II adaptor cable to PL1 on Universal Tester (UT).
- Connect the power supply adaptor cable to PL3 on the UT UPS PL1 and to power supplies per lead markings.
- Insert Patchboard #PB-1 in carrier of Universal Tester and close.
- D. Connect DC voltmeter to BJ-1 red (+) and black (-).
- E. Connect AC voltmeters to BJ-10.

III. Wire Check

Pin	To	Test Point €	Resistance (Ohms)
28		2TP	95K to 105K
29		2TP	0
12		<i>577</i> 3TP (1)	19K to 21K
14 16		4TP	19K to 21K
10		777 5TP (1)	19K to 21K
)		. 11P	0

Visually check 3R = 4.3K ohms

6R = 2.4K ohms

IV. Setup

Turn all switches to OFF or Normal on both the UT and UPS.

Turn all power supplies to zero on the UT and all variacs to zero В. on the UPS.

40A3 1RA2

3EL1

4EK1

DL13

PRINTS TO

MADE BY R.K.Gerlitz 790605 APPROVALS DIV OR Drive Systems 2 7 8 A 3 O 1 6 WLL Salem, Va. 6/7/79 LOCATION CONT ON SHEET SH NO. CODE IDENT NO.

FF-803 WF (11-77)

2 7 8 3 3 0 1 6

CONT ON SHEET FL. SH NO. 3 TITLE 2 7 8 A 3 O 1 6 TEST SPECIFICATIONS CONT ON SHEET FL. SH ND. 3 FIRST MADE FOR 44C331830-G01 REVISIONS C. Apply power to test stand. D. Install board under test to adaptor. V. Electrical Test 24U NOT uses on chall A. Close "SW-1" and "SW-34", depress "LPB-1" and then set "PS-1" to 24 ±0.5 VDC at BJ-1. NOTE: Should it become necessary to remove all DC power from the PCB, open "SW-1". B. Depress "LPB-2" and adjust "PS-2" to 15 ±0.15 VDC. +15 40 1 -15 105 C. Depress "LPB-3" and adjust "PS-3" to 15 \pm 0.15 VDC. D. With all variacs on the UPS completely CCW, depress main power on pushbutton, close switch 1 on the UPS, using PB-1, PB-2 and PB-3 on UPS and set \$1, \$2, and \$3 balance variacs for 17 ±0.2 volts AC at L-N voltage jacks. common E. Place "RS2" to position 6. (Connects 10K resistor pins 29 to 4). STEP . /2/93 Close "SW-5". (Applies 17 volts AC to pins 11, 13, 15). Readjust \emptyset 1, \emptyset 2, and \emptyset 3 balance variacs if necessary, reading AC voltage at "BJ-10" using "RS1" in position 1, 2, and 3 respectively. ADDED JJW 8/ F. Adjust 1P from CCW to CW position. The voltage at "BJ-12" shall go from-2.3 \pm 0.2 VDC to -7.3 \pm 0.2 VDC. Adjust 1P to -5 \pm 0.005 VDC at "BJ-12". *SEE BELOW FOR PEAK TO PEAK LIMITATION.
G. Place "RS1" to position 5, close "SW-6", depress PB-20 on the UPS. $\mathbf{w}_{k_{\sqrt{k}}}$ e Increase 30 variac until voltage at "BJ-10" is 17 ±0.017 VAC. THERE A CONTRACT Check \$2 and \$3 at \$50.77 position 6 and position 7. 4.7 a H. Increase the 30 variac slowly and note that take over (BJ-12 voltage 7 goes more negative) occurs at 17.2 ± 0.2 VAC L-N. 0 I. Turn all variacs to zero on UPS and remove all instrumentation. Open or return all switches to normal. + N * Ripple pin 2TP to 1TP shall be less than 30 millivolt peak for G02. 3EL1 Ripple pin 2TP to 1TP shall be less than 100 millivolt peak to 40A3 peak for GO1. 1RA2 4EK1 ABOVE PROCEDURE INSURES THAT ALL POWER IS OFF BEFORE BOARD DL13 INSERTION OR REMOVAL. + PRINTS TO DIV OR R.K.Gerlitz 790605 Drive Systems 2 7 8 A 3 O 1 6 WLL Salem, Va. LOCATION CONT ON SHEET **в**н но. 3 CODE IDENT NO. FF-803 WF (11-77) RINTED IN U.S.A.