g	GE Energy		/	Functional Testing Specification		
Parts & Repair Services Louisville, KY				LOU-GED-531X310SND		
		Test Procedur	e for a DC300 si	nubber card.		
	MENT REVISION STATUS	: Determined by the last of	entry in the "REV" a			
REV.		DESCRIPTION			GNATURE	REV. DATE
Α	Initial release, conv	erted from GEDS word	I doc to this forma	it. J.	Wychulis	1/29/2014
В						
С						
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J. Wy	ARED BY chulis	REVIEWED BY	REVIEWE	D BY	Charlie War	
<b>DATE</b> 1/29/2	2014	DATE	DATE		DATE 1/29/2014	

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LOU-GED-531X310SND	GE Energy	Page 2 of 4
REV. A	Parts & Repair Services	
	Louisville, KY	

#### 1. SCOPE

**1.1** This is a functional testing procedure for a DC300 snubber card.

## 2. STANDARDS OF QUALITY

**2.1** Refer to the current revision of the IPC-A-610 standard for workmanship standards.

## 3. APPLICABLE DOCUMENTS

- **3.1** The following document(s) shall form part of this specification to the extent specified herein. Unless otherwise indicated, the latest issue shall apply.
  - **3.1.1** Check board's electronic folder for more information
  - **3.1.2** Elementary Diagram 173C6760

## 4. ENGINEERING REQUIREMENTS

- 4.1 Equipment Cleaning
  - **4.1.1** Equipment should be clean and free of debris prior to applying power unless performing an initial check. Refer to site specific SRA's for cleaning guidelines.
- 4.2 Equipment Inspection
  - **4.2.1** Equipment should be visually inspected for any defects prior to applying power. This inspection should include the following as a minimum:
    - **4.2.1.1** Wires broken, cracked, or loosely connected
    - 4.2.1.2 Terminal strips / connectors broken or cracked
    - **4.2.1.3** Components visually damaged
    - 4.2.1.4 Capacitors bloated or leaking
    - 4.2.1.5 Solder joints damaged or cold
    - 4.2.1.6 Circuit board burned or de-laminated
    - 4.2.1.7 Printed wire runs / Traces burned or damaged

## 5. **EQUIPMENT REQUIRED**

**5.1** The following equipment is required to perform the process requirements. Equipment may be substituted provided that all accuracy's and test ratios are equivalent or better.

Qty	Reference #	Description
1		Fluke 87 DMM (or Equivalent)
1		22.1K resistor (68A7035P221F)
1		100VAC Voltage Source
1		10VDC Power Supply

LOU-GED-531X310SND
REV. A

GE Energy
Parts & Repair Services
Louisville, KY

Page 3 of 4

## 6. Testing Process

# 6.1 Testing Resistance

Special Note: This test has been used for the G1 version card; the group 2 version may need adjustments to this test.

**6.1.1** Verify < 1 ohm resistance between each of the following sets of points.

From	То
1FSPL1	1FSPL3
1FSPL2	1FSPL4
4RSPL1	4RSPL3
4RSPL2	4RSPL4
2FSPL1	2FSPL3
2FSPL2	2FSPL4
5RSPL1	5RSPL3
5RSPL2	5RSPL4
3FSPL1	3FSPL3
3FSPL2	3FSPL4
6RSPL1	6RSPL3
6RSPL2	6RSPL4
4FSPL1	4FSPL3
4FSPL2	4FSPL4
1RSPL1	1RSPL3
1RSPL2	1RSPL4
5FSPL1	5FSPL3
5FSPL2	5FSPL4
2RSPL1	2RSPL3
2RSPL2	2RSPL4
6FSPL1	6FSPL3
6FSPL2	6FSPL4
3RSPL1	3RSPL3
3RSPL2	3RSPL4
A	AA

LOU-GED-531X310SND
REV. A

GE Energy
Parts & Repair Services
Louisville, KY

Page 4 of 4

**6.1.2** Verify between 1.314 meg and 1.341 meg ohms between the following sets of points.

From	То	
P2	4ACS	
P2	5ACS	
P2	6ACS	
P1	1ACS	
P1	2ACS	
P1	3ACS	

### 6.2 Snubber AC Test

**6.2.1** For each of the sets of points listed below, apply 100  $\pm$  1 VAC through a 22.1KΩ resistor to point A with respect to point B. Then verify 68  $\pm$  4 VAC across the 22.1KΩ resistor.

From	То
P2	4ACS
P2	5ACS
P2	6ACS
P1A	1ACS
P1A	2ACS
P1A	3ACS

## 6.3 Diode Bridge Test

- **6.3.1** Connect a 22.1k $\Omega$  resistor between points M and B.
- **6.3.2** Apply +10  $\pm$  .01 VDC at CAC1 with respect to CAC2 and verify +8.8  $\pm$  .3 VDC at M with respect to B.
- **6.3.3** Change the polarity of the DC input -10  $\pm$  .01 VDC and again verify + 8.8  $\pm$  .3 VDC at M with respect to B.

### 6.4 \*\*\*TEST COMPLETE \*\*\*

### 7. Notes

**7.1** None at this time.

### 8. Attachments

**8.1** None at this time.