Parts & Repair Services *Louisville, KY*	**Functional Testing Specification**
	LOU-GED-DS3800NPIG

Test Procedure for a

DOCUMENT REVISION STATUS: Determined by the last entry in the "REV" and "DATE" column

REV.	DESCRIPTION	SIGNATURE	REV. DATE
A	Initial release	J MORGAN	7/12/2023
B			
C			

PREPARED BY	REVIEWED BY	REVIEWED BY	QUALITY APPROVAL
J MORGAN			
DATE	**DATE**	**DATE**	**DATE**
7/12/2023			

1. SCOPE

1.1 This is a functional testing procedure for a DS3800NPIG Card.

2. STANDARDS OF QUALITY

2.1 Refer to the current revision of the IPC-A-610 standard for workmanship standards.

3. APPLICABLE DOCUMENTS

3.1 The following document(s) shall form part of this specification to the extent specified herein. Unless otherwise indicated, the latest issue shall apply.

3.1.1 Check board's electronic folder for more information

4. ENGINEERING REQUIREMENTS

4.1 Equipment Cleaning

4.1.1 Equipment should be clean and free of debris prior to applying power unless performing an initial check. Refer to site specific SRA's for cleaning guidelines.

4.2 Equipment Inspection

4.2.1 Equipment should be visually inspected for any defects prior to applying power. This inspection should include the following as a minimum:

4.2.1.1 Wires - broken, cracked, or loosely connected
4.2.1.2 Terminal strips / connectors - broken or cracked
4.2.1.3 Components - visually damaged
4.2.1.4 Capacitors - bloated or leaking
4.2.1.5 Solder joints - damaged or cold
4.2.1.6 Circuit board - burned or de-laminated
4.2.1.7 Printed wire runs / Traces - burned or damaged

5. EQUIPMENT REQUIRED

5.1 The following equipment is required to perform the process requirements. Equipment may be substituted provided that all accuracy's and test ratios are equivalent or better.

Qty	Reference #	Description
1		Fluke 87 DMM (or Equivalent)

6. Modifications/Upgrades

6.1 Fill out if applicable.

7. Testing Process

7.1 Setup

7.1.1.1

Note: This is a static measurement test.

7.2 Testing Procedure

7.2.1 Preform the following resistance checks. These are all precision resistors so values should be within 1% tolerance.

7.2.1.1 JD-1 to JD-2 = short

7.2.1.2 JD-1 to JE = 498K ohm

7.2.1.3 JE to JM = 498K ohm

7.2.1.4 JM to JL = 200K ohm

7.2.1.5 JC-1 to JC-2 = short

7.2.1.6 JC-1 to JF = 498K ohm

7.2.1.7 JF to JL = 498K ohm

7.2.1.8 JB-1 to JB-2 = short

7.2.1.9 JB-1 to JG = 498K ohm

7.2.1.10 JG to JJ = 498K ohm

7.2.1.11 JJ to JI = 20K ohm

7.2.1.12 JA-1 to JA-2 = short

7.2.1.13 JA-1 to JH = 498K ohm

7.2.2 JH to JI = 498K ohm

7.3 Post Testing Burn-in Required ___ Yes ___ No

Note: All MARK I, II, & III Turbine related cards require a post testing burn-in of 100 hours.

*7.4 ***TEST COMPLETE ****

8. Notes

8.1 None at this time?

9. Attachments

9.1 None at this time?