g		GE Energy	/	Functional [*]	Testing Spe	ecification		
Parts & Repair Services Louisville, KY			LOU-GED-DS3800XTFP					
Test Procedure for a DS3800XTFP								
DOCUI	MENT REVISION STATUS	Determined by the last of	entry in the "REV" a	nd "DATE" column				
REV.		DESCRIPTION			SIGNATURE	REV. DATE		
Α	Initial release			St	eve Pharris	11/2/2011		
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DATE 11/2/2	2011	DATE	DATE		DATE	ue		
11/2/2	2011				11/7/2011			

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1. SCOPE

1.1 This is a functional testing procedure for a DS3800XTFP.

2. STANDARDS OF QUALITY

2.1 Refer to the current revision of the IPC-A-610 standard for workmanship standards.

3. APPLICABLE DOCUMENTS

- **3.1** The following document(s) shall form part of this specification to the extent specified herein. Unless otherwise indicated, the latest issue shall apply.
 - **3.1.1** Check board's electronic folder for more information

4. **ENGINEERING REQUIREMENTS**

- 4.1 Equipment Cleaning
 - **4.1.1** Equipment should be clean and free of debris prior to applying power unless performing an initial check. Refer to site specific SRA's for cleaning guidelines.
- **4.2** Equipment Inspection
 - **4.2.1** Equipment should be visually inspected for any defects prior to applying power. This inspection should include the following as a minimum:
 - 4.2.1.1 Wires broken, cracked, or loosely connected
 - 4.2.1.2 Terminal strips / connectors broken or cracked
 - 4.2.1.3 Components visually damaged
 - **4.2.1.4** Capacitors bloated or leaking
 - 4.2.1.5 Solder joints damaged or cold
 - 4.2.1.6 Circuit board burned or de-laminated
 - 4.2.1.7 Printed wire runs / Traces burned or damaged

5. EQUIPMENT REQUIRED

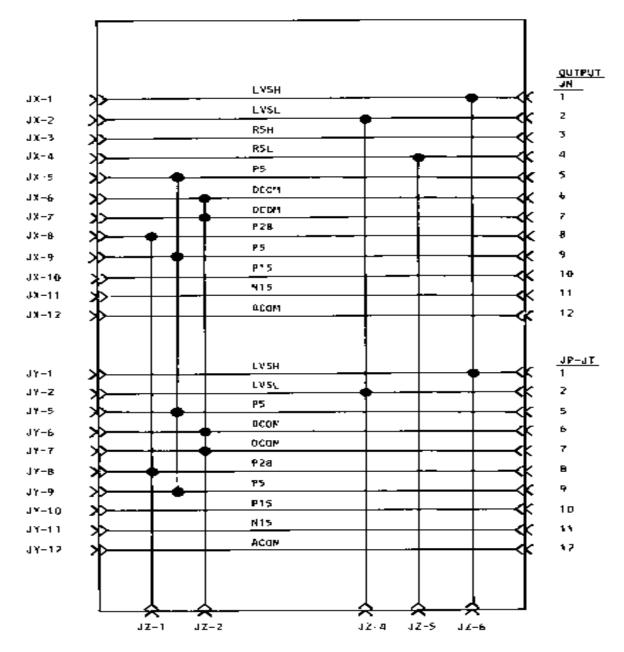
5.1 The following equipment is required to perform the process requirements. Equipment may be substituted provided that all accuracy's and test ratios are equivalent or better.

Qty	Reference #	Description
1		Fluke 87 DMM (or Equivalent)

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6. <u>Testing Process</u>

- **6.1.1** Verify less than 2 Ohms resistance between the points listed.
- **6.1.2** JX-1 to JN1 to JY-1 to JP-1 to JQ-1, JR-1, JS-1 to JT-1 to finally JZ-6.
- **6.1.3** Measure from the JX and JY inputs to the JN and JP thru JT and JZ outputs. Continue continuity check using the drawing below on the rest of the card.



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6.2 ***TEST COMPLETE ***

- 7. Notes
 - **7.1** None at this time.
- 8. Attachments
 - **8.1** None at this time.