g GE Energy	**Functional Testing Specification**
Parts & Repair Services ***Louisville, KY***	**LOU-GED-IC3600LLDB1**

Test Procedure for a IC3600 Line Driver Board

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DATE	**DATE**	**DATE**	**DATE**
7/21/2014			7/21/2014

1. SCOPE

1.1 This is a functional testing procedure for a Line Driver Card.

2. STANDARDS OF QUALITY

2.1 Refer to the current revision of the IPC-A-610 standard for workmanship standards.

3. APPLICABLE DOCUMENTS

3.1 The following document(s) shall form part of this specification to the extent specified herein. Unless otherwise indicated, the latest issue shall apply.
3.1.1 Check board's electronic folder for more information

4. ENGINEERING REQUIREMENTS

4.1 Equipment Cleaning
4.1.1 Equipment should be clean and free of debris prior to applying power unless performing an initial check. Refer to site specific SRA's for cleaning guidelines.
4.2 Equipment Inspection
4.2.1 Equipment should be visually inspected for any defects prior to applying power. This inspection should include the following as a minimum:
4.2.1.1 Wires - broken, cracked, or loosely connected
4.2.1.2 Terminal strips / connectors - broken or cracked
4.2.1.3 Components - visually damaged
4.2.1.4 Capacitors - bloated or leaking
4.2.1.5 Solder joints - damaged or cold
4.2.1.6 Circuit board - burned or de-laminated
4.2.1.7 Printed wire runs / Traces - burned or damaged

5. EQUIPMENT REQUIRED

5.1 The following equipment is required to perform the process requirements. Equipment may be substituted provided that all accuracy's and test ratios are equivalent or better.

Qty	Reference #	Description
1		Fluke 87 DMM (or Equivalent)
1		12VDC Power Supply
1		2.2K Resistor

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6. Testing Process

6.1 Test

6.1.1 This card consists of 16 identical circuits.

6.1.2 Apply 12Vdc to pin 27 and common to pin 1.

6.1.3 Using the chart in figure 1 verify, 5.1Vdc to 6.3Vdc at each of the input pins with no input applied.

6.1.4 Verify all output pins are < 1.5VDC with no input applied.

6.1.5 Connect each of the output pins of the card through a 2.2K ohm resistor to ground.

6.1.6 Verify when the input pin of each circuit is connected to common the output of the corresponding circuit goes to a minimum of 9.8VDC.

Figure 1

CIRCUIT	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
INPUT	3	7	9	11	13	17	19	21	31	33	37	39	41	43	47	49
OUTPUT	4	8	10	12	14	18	20	22	32	34	38	40	42	44	48	50

6.2 ***TEST COMPLETE ***

7. Notes

7.1 None at this time.

8. Attachments

8.1 None at this time.

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