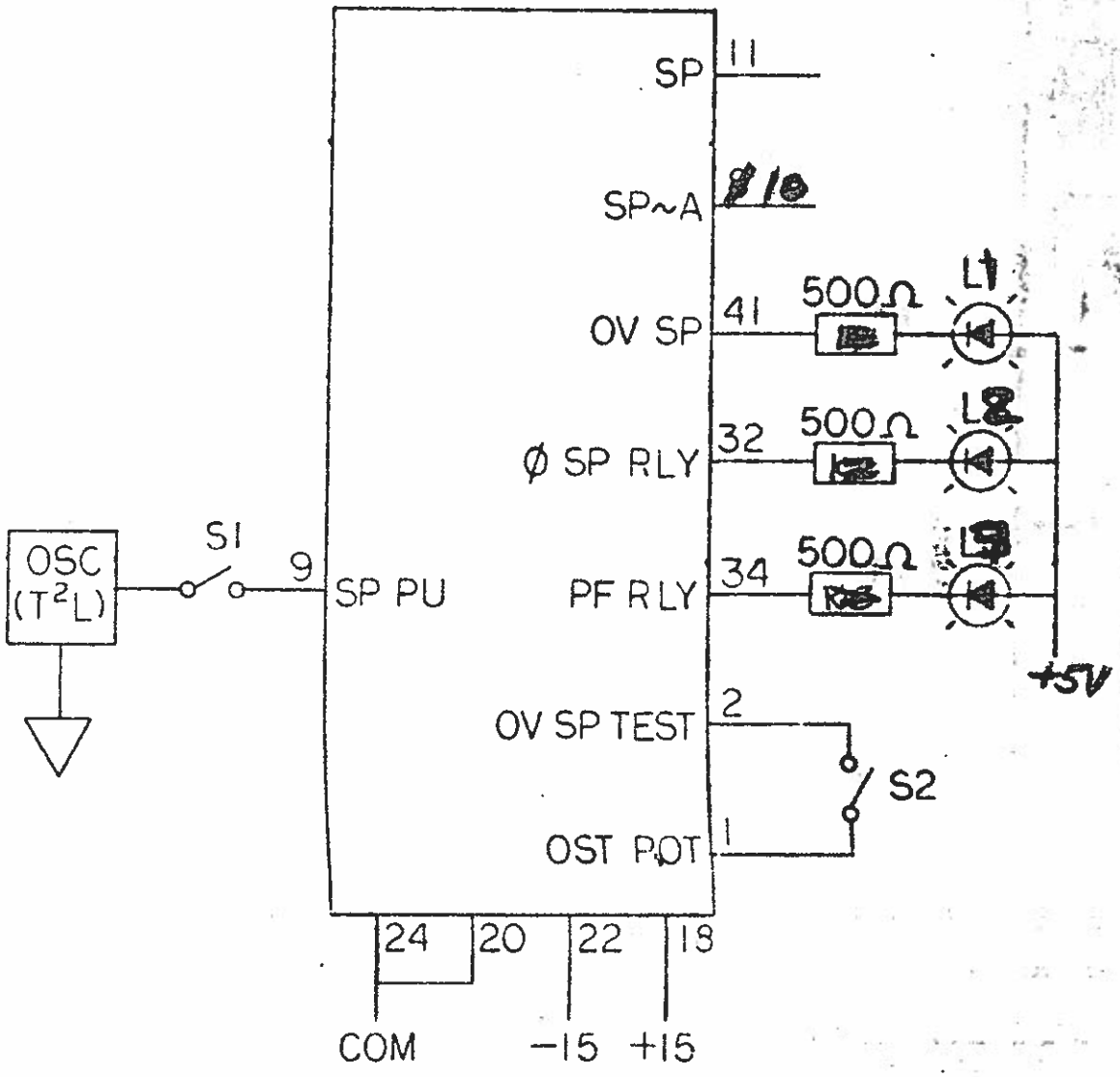
SIZE A 1284J69 SHEET 3

SP/OV SP/O SP BD 4136J55

FIGURE 1



PERFORMANCE REQUIREMENTS

SIGNATURES			
DRAWN			
ISSUED			

SIZE	FSCM		REV
A	01289	1284J69	—
		IN	SH 3

GE-102

SPEED/OVERSPEED/0 SPEED BOARD
1284J69
:p1284j69 GE8

REV. B
SHEET 1 of 2

ASSEMBLY DRAWING	PC BOARD DRAWING
4136J55-G01	4136J35-2
SCHEMATIC DRAWING	**TEST KIT**
4136J24	PROGRAMMABLE TEST KIT

1.0 INSPECTION

.1 Identification _____ .3 Solder/Wire _____ .5 Key Slot _____

.2 Comp./Conn. _____ .4 Temp. Cycle _____ .6 _____

REMARKS: CHANGES PER A.N. 85EC1008, R36 TO 10K, J.A.W. 2/26/85 REV. B.

2.0 PREPARATION

2.1 CONNECT +5V, +15V, -15V D.C. AND COMMON TO TEST KIT. _____
2.2 CONNECT +26 VDC TO +30V TEST JACK. _____
2.3 CONNECT A +5V, ZERO BASED SQUAREWAVE TO SG1. _____
2.4 USE TEST CABLE #4136J55G1. _____
2.5 SET R15 ◇2◇ FULL CW, ◇1◇ R35 CCW. _____

0 CHECKOUT

3.1 WITH S1 OFF, ADJUST R5 ◇5◇ FOR (-0.005 TO +0.005 VDC) AT TP7. _____
L1 SHOULD BE ON. _____
L2 SHOULD BE OFF. _____
L3 SHOULD BE OFF. _____
PIN 11 SHOULD READ (-0.01 TO +0.01 VDC). _____ VDC

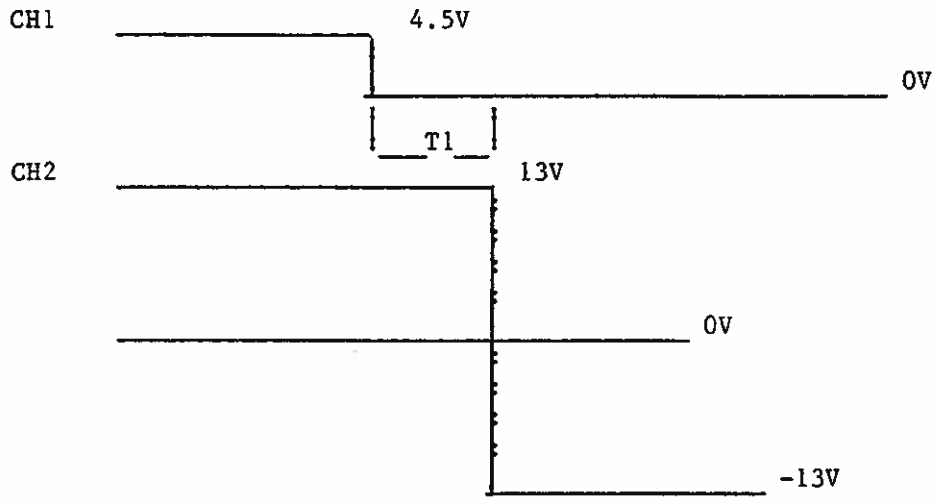
3.2 ADJUST OSCILLATOR (SG1) FOR 4000 HZ. _____
TURN ON S1. _____
ADJUST R8 ◇4◇ FOR (+9.99 TO +10.01 VDC) AT TP7. _____
ADJUST R12 ◇3◇ FOR (-9.99 TO -10.01 VDC) AT TP3. _____
L1, L2, AND L3 SHOULD BE ON. _____
TP1 SHOULD BE -15 VDC. _____

3.3 SET SG1 TO 3960 HZ, ADJUST R15 CCW UNTIL L1 JUST GOES OUT. SET SG1 TO 3950. TURN S1 OFF, THEN ON. L1 SHOULD BE ON. INCREASE SG1, L1 SHOULD GO OUT AT APPROXIMATELY 3960 HZ.

3.4 WITH THE OSCILLATOR AT 3970 HZ, TP9 WILL READ -13 VDC AFTER TURNING S1 OFF. _____

3.5 SET THE OSCILLATOR TO 2 HZ. _____
TURN S1 ON. _____
L1, L2, AND L3 ARE ON, AND STAY ON. _____

3.6 CONNECT UP A DUAL TRACE SCOPE:
CHNL 1 TO PIN 9.
CHNL 2 TO TP9.
CHNL 1, 2V/DIV, .2 SEC/DIV, SINGLE SWEEP, INT TRIG, CHOP MODE, TRIG SOURCE CHNL 1, SLOPE (-), LEVEL (-), STORE MODE, CHNL 2 5V/DIV.
SET SG1 TO .1 HZ. _____



3.7 T1 MUST BE .5 TO .8 SEC.

3.8 SET OSCILLATOR TO 3564 HZ.
TURN ON S2. CHECK THAT PIN 1 IS -7.06 TO -7.89 VDC. _____
ADJUST R35 ◇1◇ CW TILL L1 GOES OFF.

3.9 SET OSCILLATOR TO 4000 HZ.
PIN 10 SHOULD READ (-10.4 TO -10.7 VDC). _____ VDC
CONNECT -15V D.C. TO PIN 11.
TP4 SHOULD READ (+0.4 TO +0.7 VDC). _____ VDC
REMOVE -15 VDC FROM PIN 11.
A-C RIPPLE ON PIN 11 SHOULD READ LESS THAN .05V PEAK TO PEAK. _____