

REV NO. 01  
P3K-AL-0356 -A01  
CONT ON SHEET 2 SH NO. 1

TITLE  
TEST INSTRUCTIONS FOR LOAD RATES AND LOAD SET LIMITS  
ANALOG/LOGIC CIRCUIT BOARD 1L3-C001  
FIRST MADE FOR (Ass'y Drawing 117D7728 G-1) *OK*

REVISIONS

1 0. Mone 10/28/74  
No chg. this off

## I. CIRCUIT DESCRIPTION

This circuit board is used to consolidate all of the analog to logic relays that provide switching signals to the Loading Rates and Load Set Limits Analog Circuit.

The board contains eight (8) 24V mercury wetted relays.

## II. CIRCUIT SPECIFICATIONS

1. Connect +24 VDC to pin 38 and ground to pin 40. A +24V should appear between TP1 and TP2. Connect 24V 20 MA lamps to pins 6, 7, 8, 9, 10, 11, 12, and 14.
2. The logic and path continuity should be checked according to the following table:

**ACTIVE**  
BY \_\_\_\_\_

273-221  
273-227  
273-20  
273-12  
273-138  
273-71  
PRINTS TO

MADE BY <b>B. Mone</b>	OCT. 28, 1974	APPROVALS	Steam Turbine	DIV OR DEPT.	P3K-AL-0356 -A01
ISSUED	<b>OCT 28 1974</b>		Schenectady, N.Y.	LOCATION	CONT ON SHEET 2 SH NO. 1

REV NO. 1

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P3K-AL-0356 -A01

TEST INSTRUCTIONS FOR LOAD RATE AND LOAD SET LIMITS  
ANALOG/LOGIC CIRCUIT BOARD IL3-C001

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FIRST MADE FOR (Ass'y Drawing 117D7728 G-1)

INPUTS AT PINS  
0: OV or lamp off  
1: 24V or lamp on

CORRECT OUTPUTS AT PINS

REVISIONS

17	20	26	16	18	19	21	22	23	24	25	6	7	8	9	10	11	12	14
1	2	3	4	5	6	7	8	9	10	11	1	2	3	4	5	6	7	8

0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
1	1	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0	0	0
1	1	1	0	0	0	0	0	0	0	0	1	0	1	1	0	0	0	0
1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	0	0	0	0
1	1	1	0	0	0	0	0	0	0	0	1	0	1	1	0	0	0	0
1	1	1	0	0	1	0	0	0	0	0	1	0	0	1	0	0	0	0
1	1	1	0	1	1	0	0	0	0	0	1	0	1	1	0	0	0	0
1	1	1	0	0	1	0	0	0	0	0	1	0	0	1	0	0	0	0
1	1	1	0	0	0	0	0	0	0	0	1	0	1	1	0	0	0	0
1	1	1	0	0	0	1	0	0	0	0	1	0	1	0	0	0	0	0
1	1	1	0	0	0	0	0	0	0	0	1	0	1	1	0	0	0	0
1	1	1	0	0	0	0	1	0	0	0	1	0	1	1	1	0	0	0
1	1	1	0	0	0	0	0	0	0	0	1	0	1	1	0	0	0	0
1	1	1	0	0	0	0	0	0	0	0	1	0	1	1	0	1	0	0
1	1	1	0	0	0	0	0	0	0	0	1	0	1	1	0	0	0	0
1	1	1	0	0	0	0	0	0	0	1	1	0	1	1	0	0	0	1
1	1	1	0	0	0	0	0	0	0	0	1	0	1	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

1 O.Mone APR 28 1974  
No chg. this vld

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ISSUED OCT 28 1974

APPROVALS

Steam Turbine

DIV OR DEPT.

Schenectady, N.Y.

LOCATION

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REV  
NO. /

TITLE  
TEST INSTRUCTION FOR LOAD RATES  
AND LOAD SET LIMITS

CONT ON SHEET 4

SH NO. 3

P3K-AL-0356-A01

CONT ON SHEET 4

SH NO. 3

FIRST MADE FOR MK II

REVISIONS

TEST INSTRUCTIONS 1L3-C001

Assembly 117D7728  
Schematic 117D7724

PROCEDURE:

- A. Refer to P3K-AL-0494-A02 for test circuits, equipment list, and set up instructions.
- B. Table A provides the step by step procedure to follow in testing this circuit.
- C. Table B provides a step by step description of input voltages (to use as a troubleshooting aid).

1 0.47 pnc. APR 28 1976  
Rev. 9. Schenectady

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REV. NO. /

P3K-AL-0356-A01

CONT ON SHEET 5 SH NO. 4

TITLE

TEST INSTRUCTION FOR LOAD RATES  
AND LOAD SET LIMITS

FIRST MADE FOR

ACTION	VOLTAGE AT PINS										REVISIONS
	6	7	8	9	10	11	12	14			
STEP 1 INIT COND											1047/00 APR 28 1976 S.L.P. Polysad
STEP 2 APP P 38											
STEP 3 APP P 17	A	Ø									
STEP 4 APP P 20			A								
STEP 5 APP P 26				A							
STEP 6 APP P 16	Ø	A									
STEP 7 REM P 16	A	Ø									
STEP 8 APP P 19			Ø								
STEP 9 APP P 18			A								
STEP 10 REM P 18			Ø								
STEP 11 REM P 19			A								
STEP 12 APP P 21				Ø							
STEP 13 REM P 21				A							
STEP 14 APP P 22					A						
STEP 15 REM P 22					Ø						
STEP 16 APP P 23						A					
STEP 17 REM P 23						Ø					
STEP 18 APP P 24							A				
STEP 19 REM P 24							Ø				
STEP 20 APP P 25								A			
STEP 21 REM P 25								Ø			
STEP 22 REM P 26	A		A								
STEP 23 REM P 20	A										
STEP 24 REM P 17											
STEP 25 REM P 38											

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CONT ON SHEET 6 SH NO. 5

TITLE  
TEST INSTRUCTION FOR LOAD RATES AND  
LOAD SET LIMITS  
FIRST MADE FOR MK II

ACTION	INPUT AT PINS										REVISIONS
	16	17	18	19	20	21	22	23	24	25	
STEP 1											1 D.Mone APR 28 1976 Ralph - Retyped
STEP 2 APP P 38											
STEP 3 APP P 17		X									
STEP 4 APP P 20		X			X						
STEP 5 APP P 26		X			X						
STEP 6 APP P 16	X	X			X						
STEP 7 REM P 16		X			X						
STEP 8 APP P 19		X		X	X						
STEP 9 APP P 18		X	X	X	X						
STEP 10 REM P 18		X		X	X						
STEP 11 REM P 19		X			X						
STEP 12 APP P 21		X			X	X					
STEP 13 REM P 21		X			X						
STEP 14 APP P 22		X			X		X				
STEP 15 REM P 22		X			X						
STEP 16 APP P 23		X			X			X			
STEP 17 REM P 23		X			X						
STEP 18 APP P 24		X			X				X		
STEP 19 REM P 24		X			X						
STEP 20 REM P 25		X			X					X	
STEP 21 REM P 25		X			X						
STEP 22 REM P 26		X									
STEP 23 REM P 20		X									
STEP 24 REM P 17											
STEP 25 REM P 38											

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REV NO.  

P3K-AL-0356-A01

CONT ON SHEET 7 SH NO. 6

TITLE  
TEST INSTRUCTION FOR LOAD RATES  
AND LOAD SET LIMITS

FIRST MADE FOR MK II

ACTION		INPUT AT PINS								REVISIONS
		26	38							<div style="writing-mode: vertical-rl; transform: rotate(180deg);"> APR 28 1976  D. Mone  Retired </div>
STEP 1										
STEP 2 APP P 38			X							
STEP 3 APP P 17			X							
STEP 4 APP P 20			X							
STEP 5 APP P 26	X	X								
STEP 6 APP P 16	X	X								
STEP 7 REM P 16	X	X								
STEP 8 APP P 19	X	X								
STEP 9 APP P 18	X	X								
STEP 10 REM P 18	X	X								
STEP 11 REM P 19	X	X								
STEP 12 APP P 21	X	X								
STEP 13 REM P 21	X	X								
STEP 14 APP P 22	X	X								
STEP 15 REM P 22	X	X								
STEP 16 APP P 23	X	X								
STEP 17 REM P 23	X	X								
STEP 18 APP P 24	X	X								
STEP 19 REM P 24	X	X								
STEP 20 APP P 25	X	X								
STEP 21 REM P 25	X	X								
STEP 22 REM P 26		X								
STEP 23 REM P 20		X								
STEP 24 REM P 17		X								
STEP 25 REM P 38										

MADE BY D. Mone Oct. 28, 1974

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P3K-AL-0356

CONT ON SHEET — SH NO. 7

REVISIONS

1 D. Mone APR 28 1976  
No chg. this dng

PREPARED BY

*D. Economou*

DATE

*4-18-73*

D. Economou  
EHC DESIGN ENGINEERING

APPROVED BY

*P.C. Callan*

DATE

*10-25-74*

P.C. Callan - MANAGER  
EHC DESIGN ENGINEERING

TEST PROCEDURE  
PREPARED BY

*C.A. Bugg*

DATE

*8/5/74*

C. Bugg  
EHC TEST ENGINEER

TEST PROCEDURE APPROVED BY

*M.J. Sindoni*

DATE

*10/24/74*

M.J. Sindoni  
EHC DESIGN ENGINEERING

PRINTS TO

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