

GE Power Generation Engineering

PROCESS SPECIFICATION

Materials and Processes Engineering Schenectady, NY 12345 P3K-AL-0046-A01

PROCESS INSTRUCTIONS FOR TESTING THE PRESSURE-LOAD GATE AMPLIFIER CIRCUIT BOARD 114D6003 G-3

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PREPARED BY:	P.R.	BUDKA	
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PROCESS INSTRUCTIONS FOR TESTING THE PRESSURE-LOAD GATE AMPLIFIER CIRCUIT BOARD

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Drawing 114D6003 G-3

This process instruction provides a method for performing a functional check with the necessary adjustments for the subject circuit board prior to installation in the EHC cabinet.

TEST PROCEDURE

SCOPE

- (1) Examine the circuit board to see that the electrical components and printed circuits are not physically damaged.
- Check for continuity between pins 8 and 9. (2)
- (3) Check for continuity between pins 6 and 7.
- (4) Plug circuit board 114D6003 G-3 into the test fixture.
- Use an ohmmeter to check that there are no short circuits between (5) any combinations of pins 17, 19, 21, 38 and 40.
- (6) Connect a resistance of 900 ohms between pins 37 and 19.
- (7) Connect a resistance of 900 ohms between pins 27 and 19.
- (8) Connect the input of a high gain dc operational amplifier to pin 29 and the output to pin 41.
- (9) Connect a well regulated plus (+) 30.0 volt dc power supply to pins 17 and 38. The negative voltage terminal should be connected to pin 19.
- Connect a well regulated minus (-) 22.0 volt dc power supply to (10)pins 21 and 40. The positive voltage terminal should be connected to pin 19.
- (11) All voltage measurements will be made with respect to testpoint TP11
- (12) Adjust R3 to provide +10.40 volts at pin 37.
- (13) Adjust R2 to provide -10.40 volts at pin 27.
- (14)Apply a test voltage of +0.500 volts to pin 20 as measured at TP1. The voltage at pin 25 and TP2 should be between -4.90 and +5.15 volts.
- Apply a test voltage of -0.500 volts to pin 20 as measured at TP1. The voltage at pin 25 and TP2 should be between -0.10 and +0.10 volts.

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Drawing 114D6003 G-3

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		P3K-AL-0046-A01	PRESSURE-LOAD GATE AMPLIFIER CIRC	UIT BOARD	

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(16) Remove all test equipment.

(17) Remove the circuit board from the test fixture and identify it with a suitable mark to indicate that it has been tested and adjusted in accordance with this instruction.

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Control Design Engineering

APPROVED BY:

P. C. Callan, Manager

DATE: 1476, 1970

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