1.0 SCOPE

This instruction covers the test procedure for production testing of the 193X185AAG01 reverse legic card. Test conditions are given in Section 3.0. Performance is covered in Engineering Specification 224X329AA.

2.0 INSTRUCTIONS

In these tests a "high" (H) refers to 3.5 to 5.25 volts. A "Low" (L) refers to zero to 0.5V.

2.01 REVERSING

Run through the following truth table in sequence. Tabs 9 and 12 need not be checked after the first three steps.

Input Conditions						Output Check			
Tab 14	Tab 16	Tab 13	Tab 6	Tab 5	Tab 17	Tab 8	9	11	12
H	L	H	H	L	H	-	-	-	-
H	L	H	L	H	L	H	L	L	H
L	H	H	L	H	L	L	H	H	L
		L	↓	L	L	↓		↓	
		↓	Pulse H	↓	Pulse H				
		↓	L	↓	L	H		L	
		H	L	H	L				
		↓	Pulse H	↓	L				
		↓	L	↓	Pulse H				
		↓	↓	L	L			↓	
		↓	↓	↓	Pulse H	↓		H	

2.02 LINEAR TIMING

Turn both pots P477 and P478 full clockwise. Short Tab 5 to common. Place a -10 volt step into Tab 19. Tab 10 should take more than 35 seconds to time up to +10 volts (±.25 volts). Remove the -10 volts to Tab 19 and Tab 10 should take more than 35 seconds to time down to zero.

2.03 REFERENCE CLAMP

Again apply -10 volts to Tab 19. Then apply a +3 volts to Tab 30 and the Tab 10 should settle to a final value between zero and -1 volt.

2.04 REVERSING CLAMP

With -10 volts applied to Tab 19 and Tab 5 open, short Tab 13 to common. Tab 10 should time to a final value between zero and -1 volt. Momentarily short Tab 5 to common, Tab 10 should return to +10 (+.25V). Now open Tab 13, Tab 10 should again go to 0 to -1V

TD(1)
5D(BW)
5E(BW)
5OC(2BW)
5R(BW)
5(SL)
PRINTS TO

2.05 CURRENT SUMMING

Short Tab 5 to common and run through the following sequence, checking the output at Tab 22.

Condition		Output Check
Tab 27 = +2V	Tab 25 = H	-3.1 to -3.5V
Tab 27 = +2V	Tab 25 = L	0 to ±0.2V.
Tab 28 = +2V.	Tab 16 = H	-3.1 to -3.5V
Tab 28 = +2V	Tab 16 = L	0 to ±0.2V
Tab 29 = +2V.	Tab 14 = H	-3.1 to -3.5V.
Tab 29 = +2V	Tab 14 = L	0 to ±0.2V

2.06 With Tab 29 = +5V. and Tab 14 = H, the output on Tab 20 can be varied with pot P476 and output on Tab 24 with P475.

2.07 Input to Tab 21 through a 10K resistor. Output check on Tab 26. The input voltage for which a change in outvoltage takes place should be:

Input	Output
9.3V to +11.7V	0 change to approx. +10V.
-9.3V to -11.7V	0 change to approx. -10V.

3.0 TEST CONDITIONS

3.01 DC Supply Voltages: +19.8 to 20.2V
-19.8 to 20.2V
4.9 to 5.1V
Room Temp.

4.0 REQUALIFICATION

This card should be requalified by Quality Control every 18 months or every 200 production cards, whichever comes first.

REVISIONS

ENG. 12-07-5 & 12

1

TDU

5D(BD)

5E(BW)

5QC(2BW

5R(BW)

5(SL)

PRINTS TO