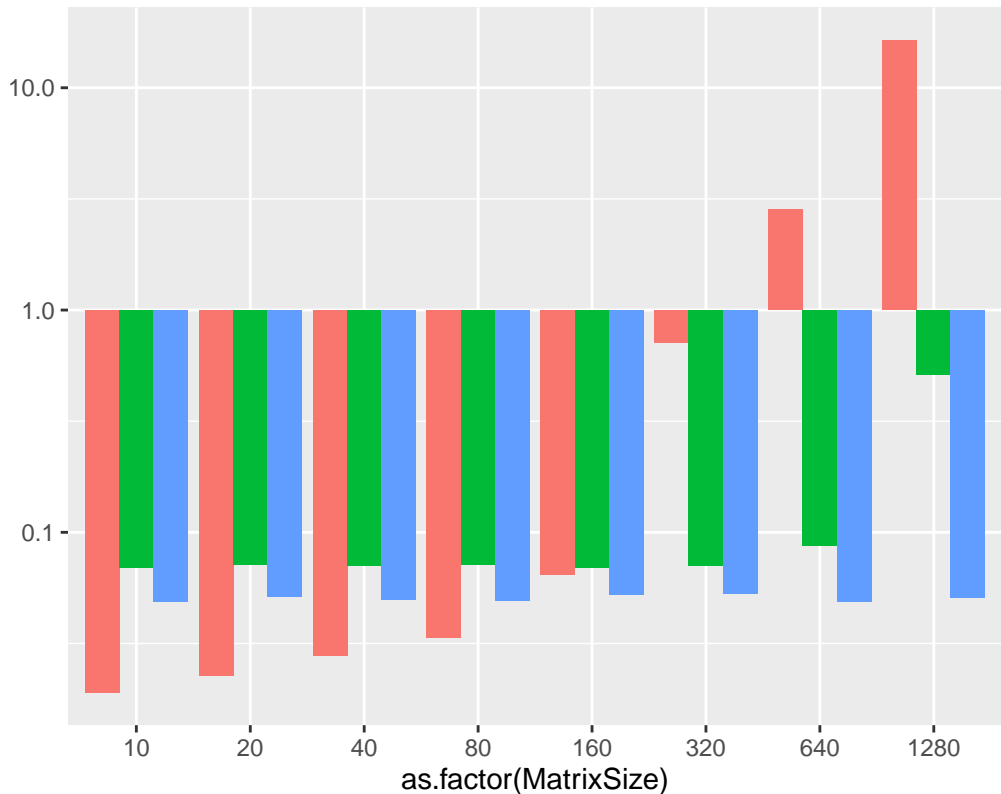


Matrix Operation: Addition

Runtime



Processor

CPU

GPU

TPU