

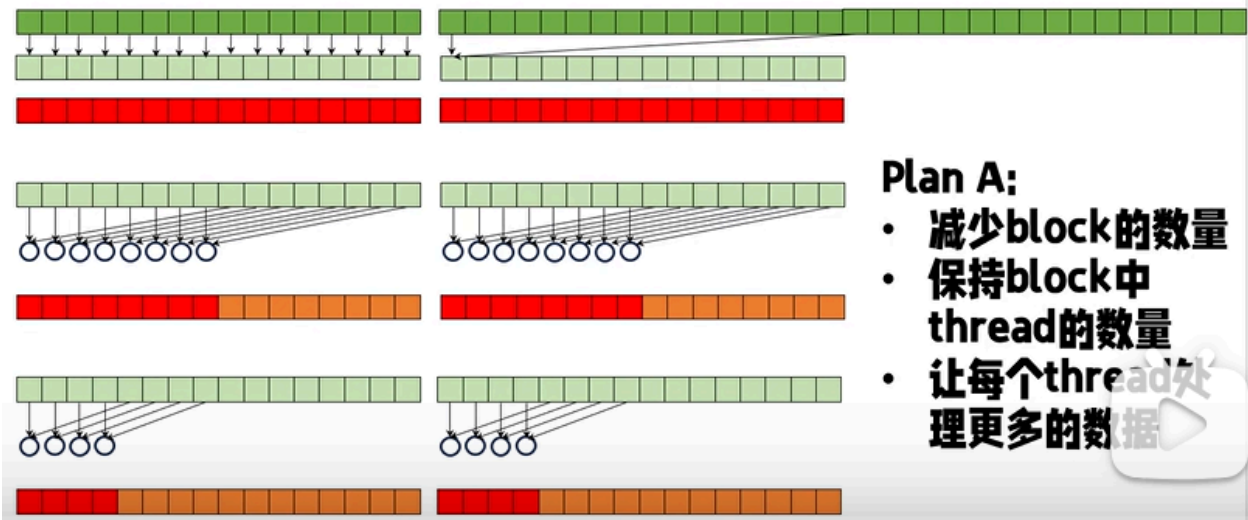


05_06_内存搬运过程中相加

算法思想

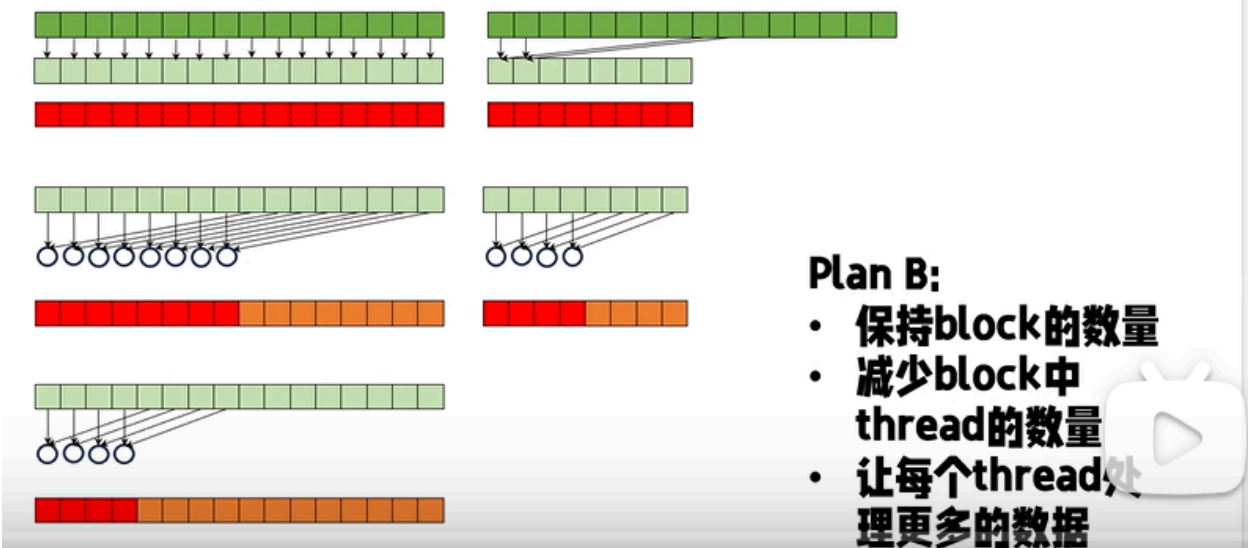
1. Plan A：减少block的数量，保持block中线程数量

扩大每个block处理的数据，让数据从全局内存→共享内存搬运的过程中进行加和，后续消除bank冲突的过程不变。



2. Plan B：保持block的数量，减少block中线程数量

Reduce_v4_add_during_load



工程链接

1. https://github.com/JingliangGao/CudaRoad/tree/main/reduce_case/5_reduce_add_during_load_PlanA
2. https://github.com/JingliangGao/CudaRoad/tree/main/reduce_case/5_reduce_add_during_load_PlanB