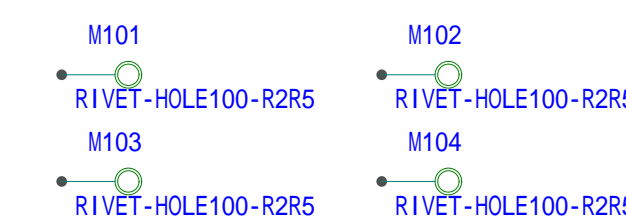
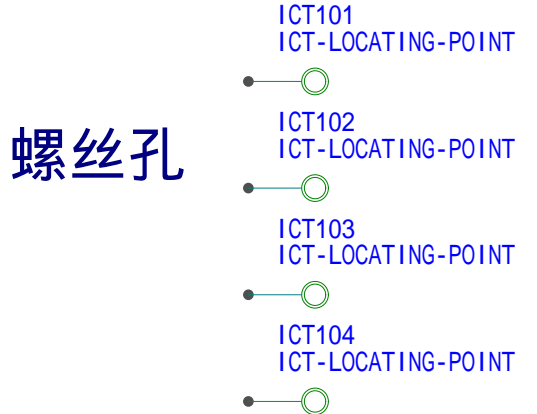
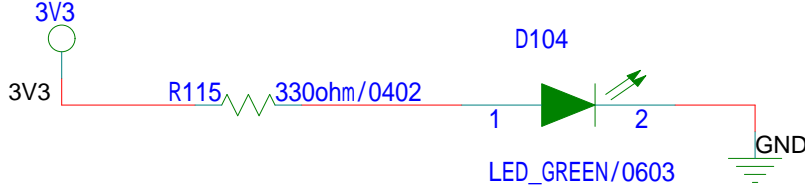
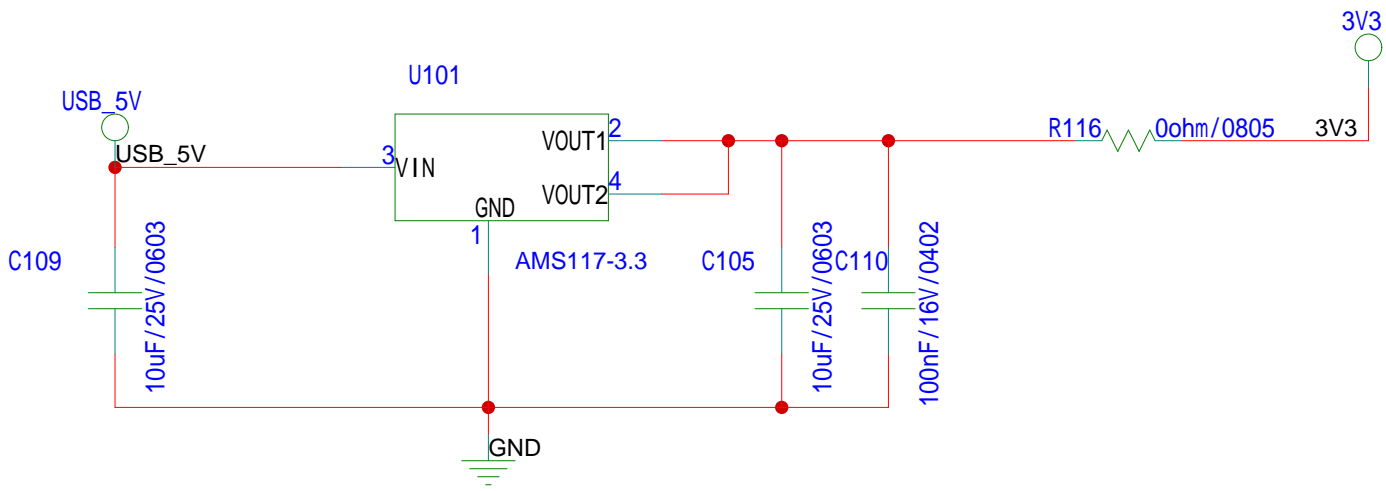
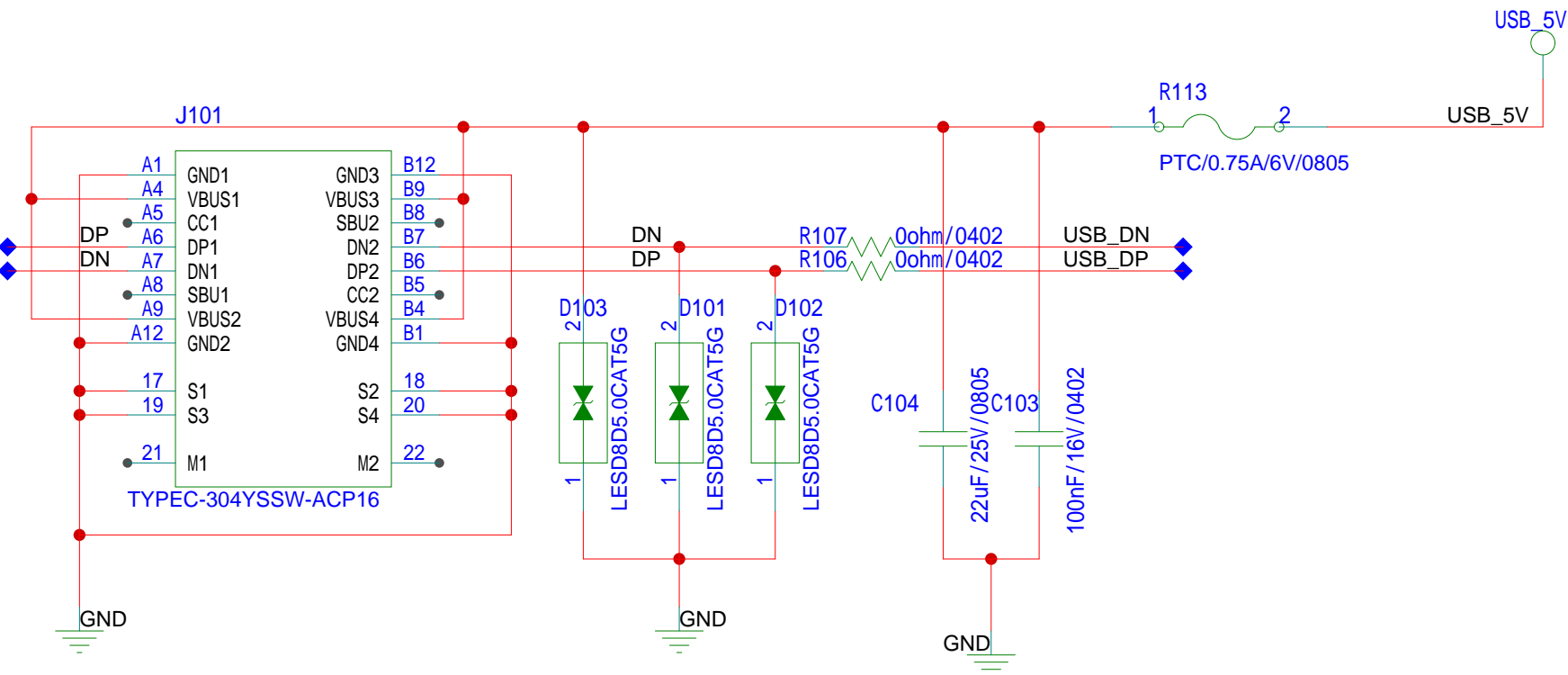


A

B

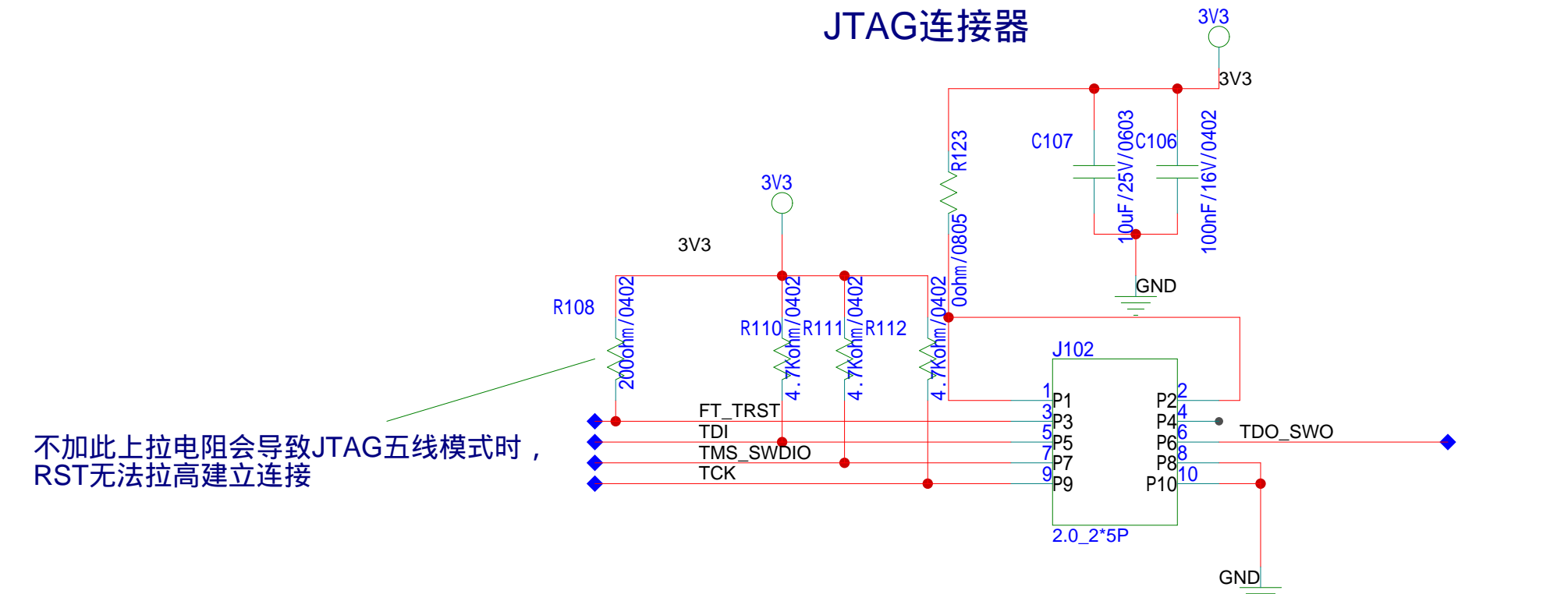
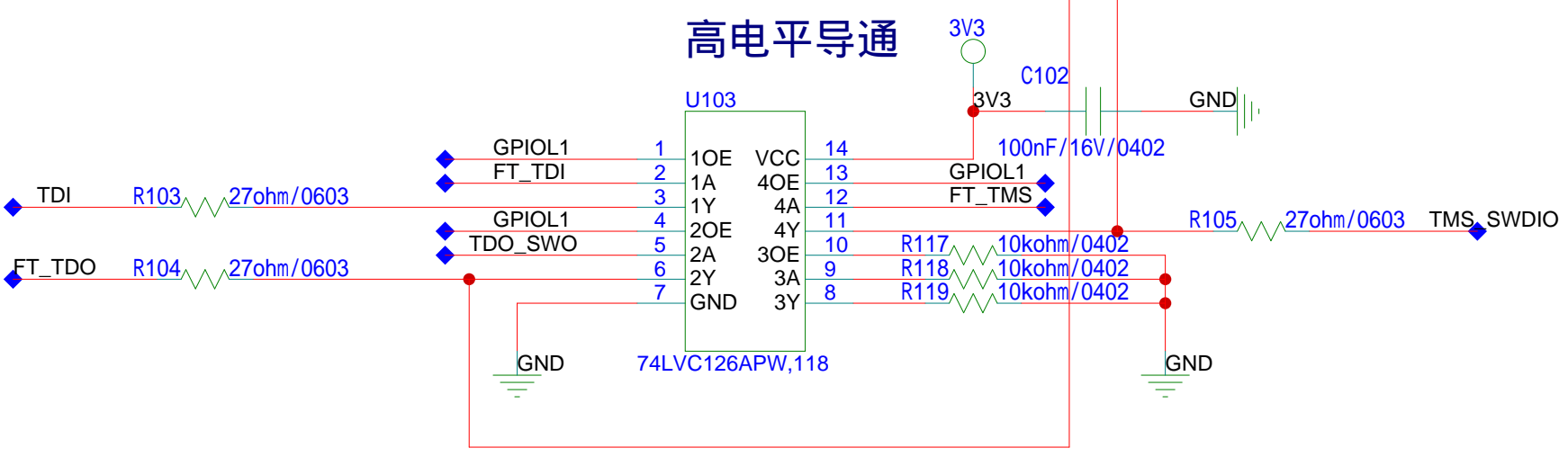
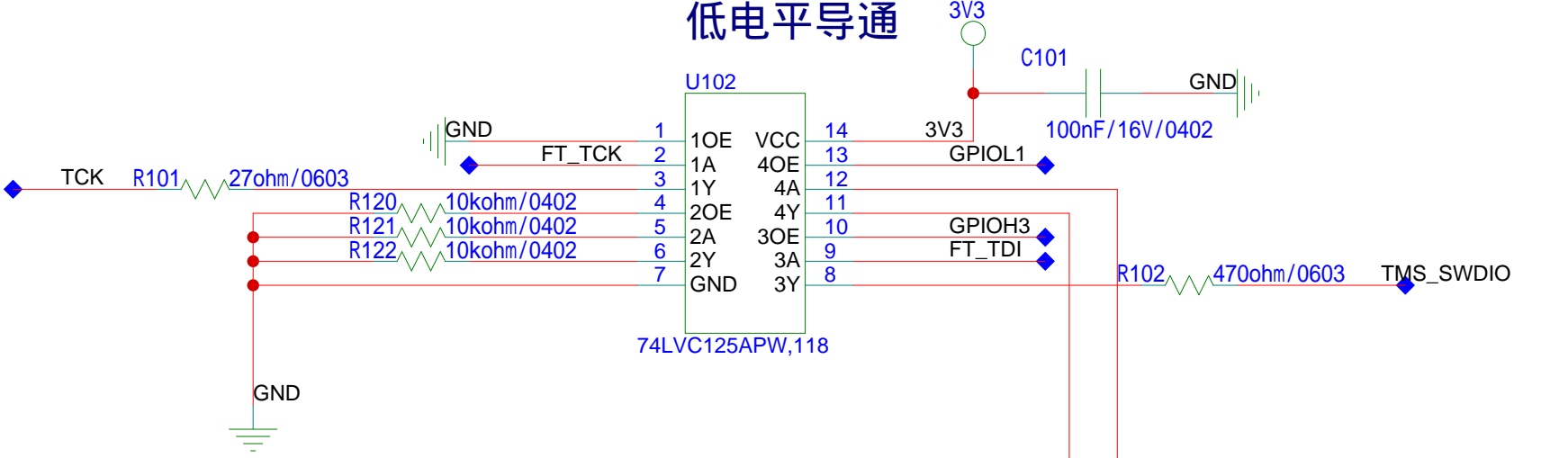
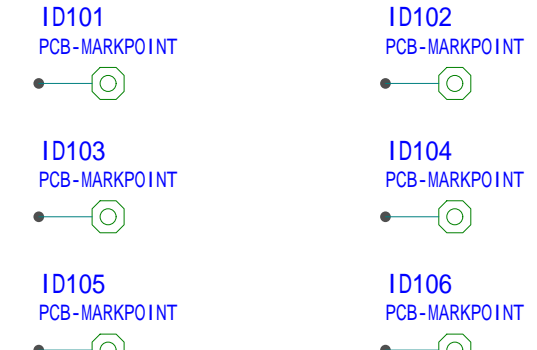
C

D



拼板孔

光学定位



不加上拉电阻会导致JTAG五线模式时，RST无法拉高建立连接

The type and specification of the components refer to the BOM					
				ECA NO	DATE
DESIGNED		HiSpark_Link_VERB			
REVIEWED					
		VER	PART_NUMBER	SHEET	1 OF 2
		B		HiSilicon(Shanghai) Technologies Co., Ltd	

