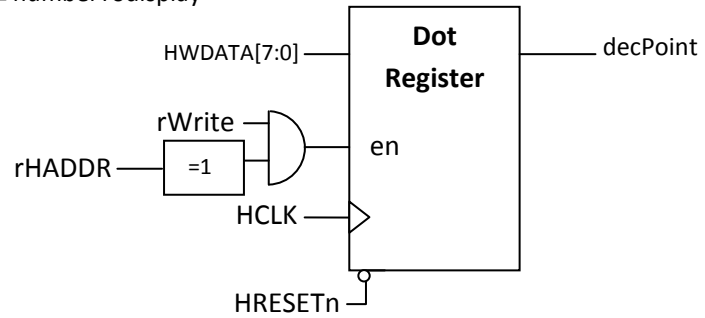
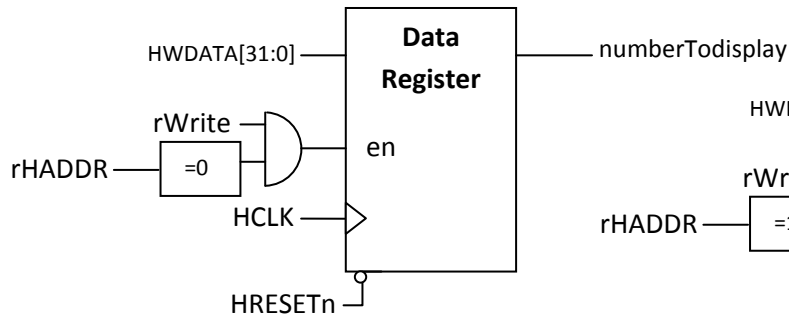


Registers to hold signals



Registers to hold signals

