1. Add comments to the following MIPS code and describe in one sentence what it computes in register \$vo. Assume that \$a0 is used for the input and initially contains n, a positive integer. Assume that \$v0 is used for the output.

```
$t0, $zero, 0
begin: addi
                $t1, $zero, 0
       addi
                $t2, $a0, $t1
       slt
loop:
                $t2, $zero, finish
        bne
                $t0, $t0, $t1
        add
                $t1, $t1, 2
       addi
                loop
       j
finish: add
                $v0, $t0, $zero
```

2. Consider the following **while** loop in a high-level language:

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The MIPS assembly language program given below implements the above loop.

```
Loop: add
                 $t1, $s3, $s3
                                            # t1 \leftarrow i + i
                 $t1, $t1, $t1
                                            # t1 ← 4*i
        add
                 $t1, $t1, $s6
                                            #t1 		 address of a [i]
        add
        lw
                 $t0, 0 ($t1)
                                            # t0 \( \sime \) a [i]
                 $t0, $s5, Exit
        bne
                 $s3, $s3, $s4
                                            # i \leftarrow i + j
        add
        J
                 Loop
Exit:
```

Assume that in the while loop condition test, a [i + m*j] is equal to **k** for values of m in the range $0 \le m \le 9$, and is not equal to k when m = 10. Therefore, 10 iterations of the loop are executed.

- (a) How many assembly language instructions are executed in the MIPS program given above for 10 iterations of the while loop?
- (b) Write a semantically equivalent MIPS assembly language program to reduce by ten the number of instructions executed in part (a) for 10 iterations of the while loop.
- (c) Write a semantically equivalent MIPS assembly language program to reduce by more than half the total number of instructions executed in part (a) for 10 iterations of the while loop.