	Total Cell Area	Dynamic Power (mW)	Leakage Power (uw)	Total Power (uW)	Matrix Length	Ideal Number of Cycles
Part 1	3194.925961	1.0582	65.7944	1123.9944	3	21
Part 2 (New Matrix)	3238.017961	1.1222	67.1155	1189.3155	3	21
Part 2 (No New Matrix)	3238.017961	1.1222	67.1155	1189.3155	3	12
Part 3 (New Matrix)	9146.941888	4.3373	166.5257	4503.8257	8	136
Part 3 (No New Matrix)	9146.941888	4.3373	166.5257	4503.8257	8	72
Part 4 (New Matrix)	32741.1415	23.5021	641.9619	24144.0619	8	??
Part 4 (New Matrix)	32741.1415	23.5021	641.9619	24144.0619	8	??

Min. Time per Cycle (ns)	Frequency (GHz)	vsim delay (ns)	vsim clock period (ns)	vsim number of cycles	Minumum Delay (ns)	Area Delay Product (area * ns)	Energy Consumed for 1 matrix-vector multiplication (uJ)
1.57	0.6369426752	300	10	30	47.1	150481.0128	52.94013624
1.5	0.6666666667	300	10	30	45	145710.8082	53.5191975
1.5	0.6666666667	210	10	21	31.5	101997.5658	37.46343825
1.56	0.641025641	1600	10	160	249.6	2283076.695	1124.154895
1.56	0.641025641	960	10	96	149.76	1369846.017	674.4929368
0.95	1.052631579	980	10	98	93.1	3048200.274	2247.812163
0.95	1.052631579	320	10	32	30.4	995330.7016	733.9794818
						0	0

Cycles for 1 Aritmehtic Operation yn	1 Arithmetic Operation yn Percentage of Computations	Energy Consumed by Arithmetic Operations (uJ)
3	0.1	15.88204087
3	0.1	16.05575925
3	0.1428571429	16.05575925
8	0.05	449.6619579
8	0.08333333333	449.6619579
6	0.0612244898	137.6211528
6	0.1875	137.6211528
#DIV/0!	#DIV/0!	#DIV/0!