

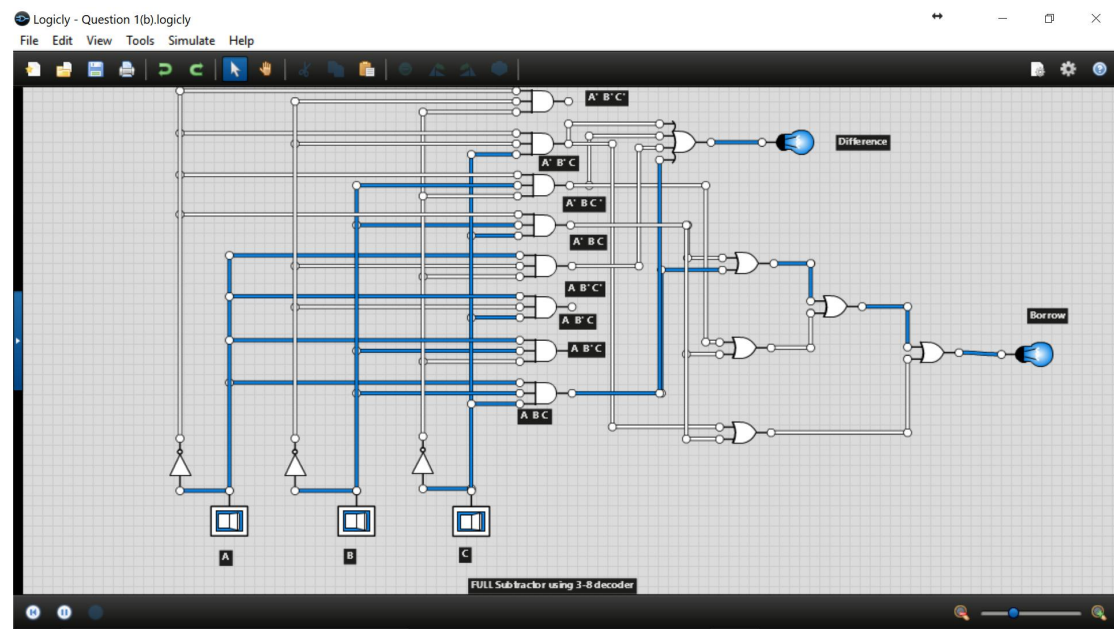
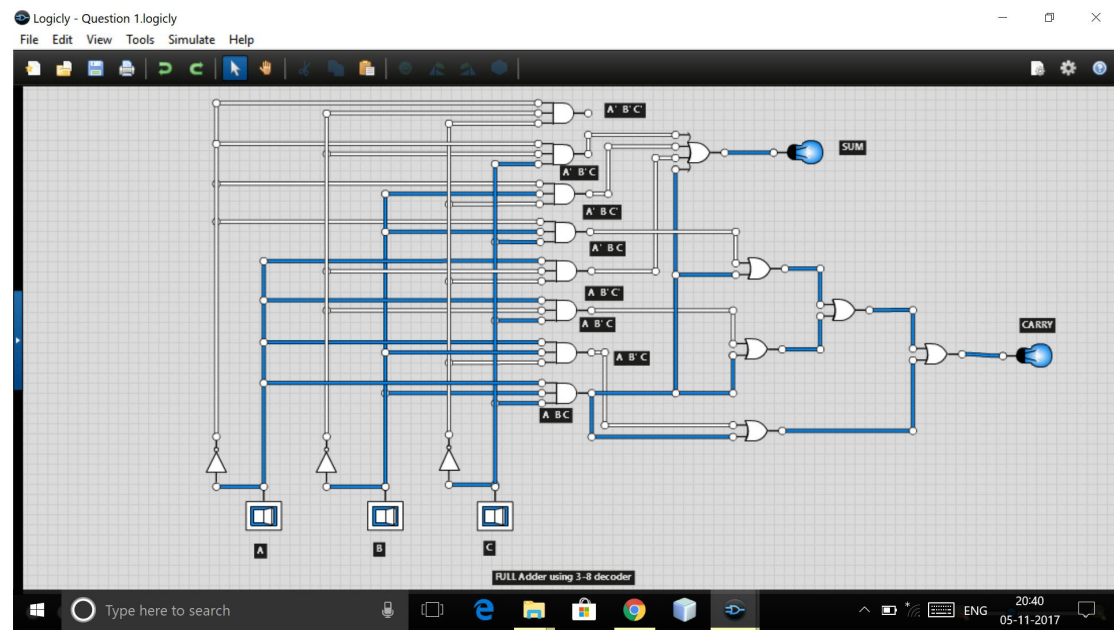


**Maulana Azad National Institute of Technology
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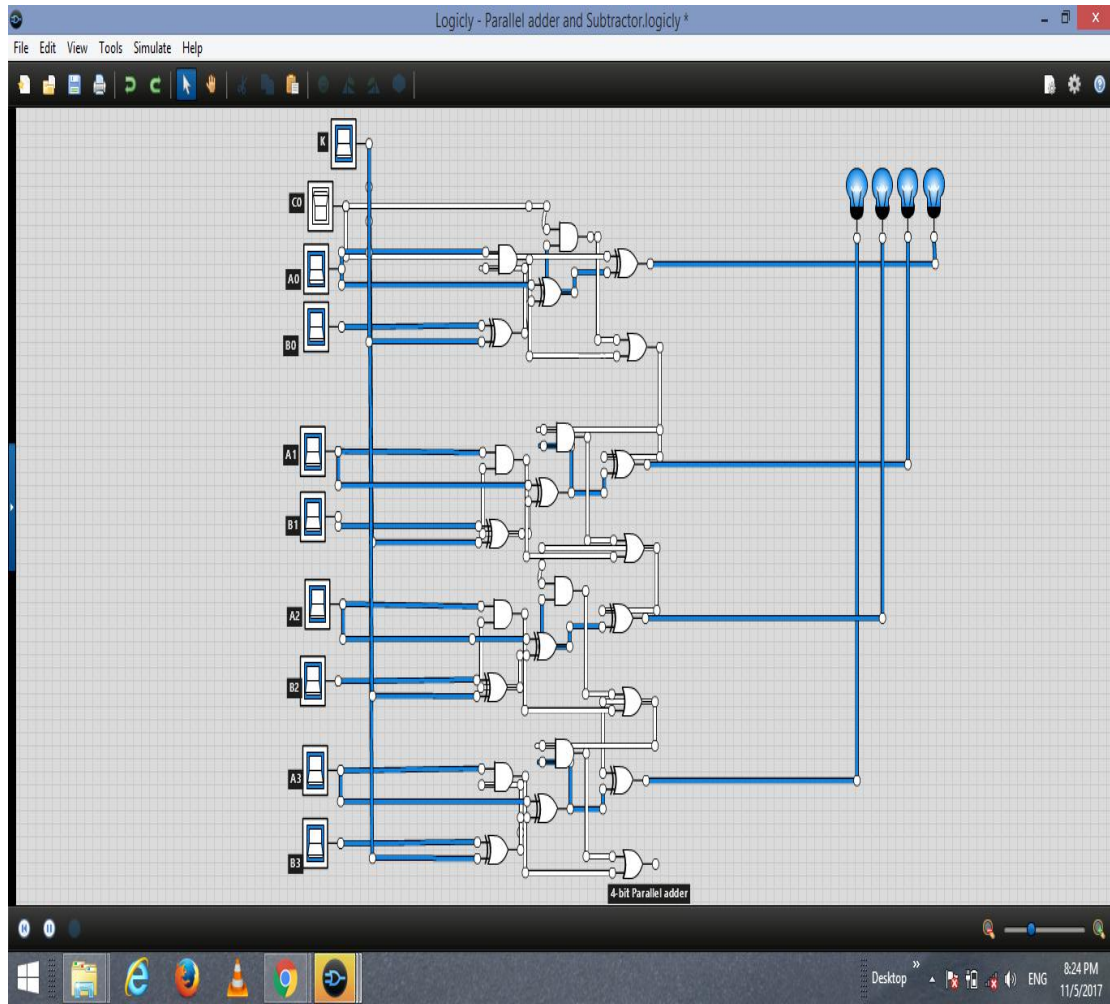
Lab-Assignment
Digital Electronics Laboratory
(CSE-218)

Submitted by: Jishan Shaikh (Scholar no. 161112013)
Section: CSE-1 (2016-20)
Submitted to: Prof. Jaytrilok Chaudhary
Department of CSE, MANIT Bhopal
Submission date: November 6, 2017

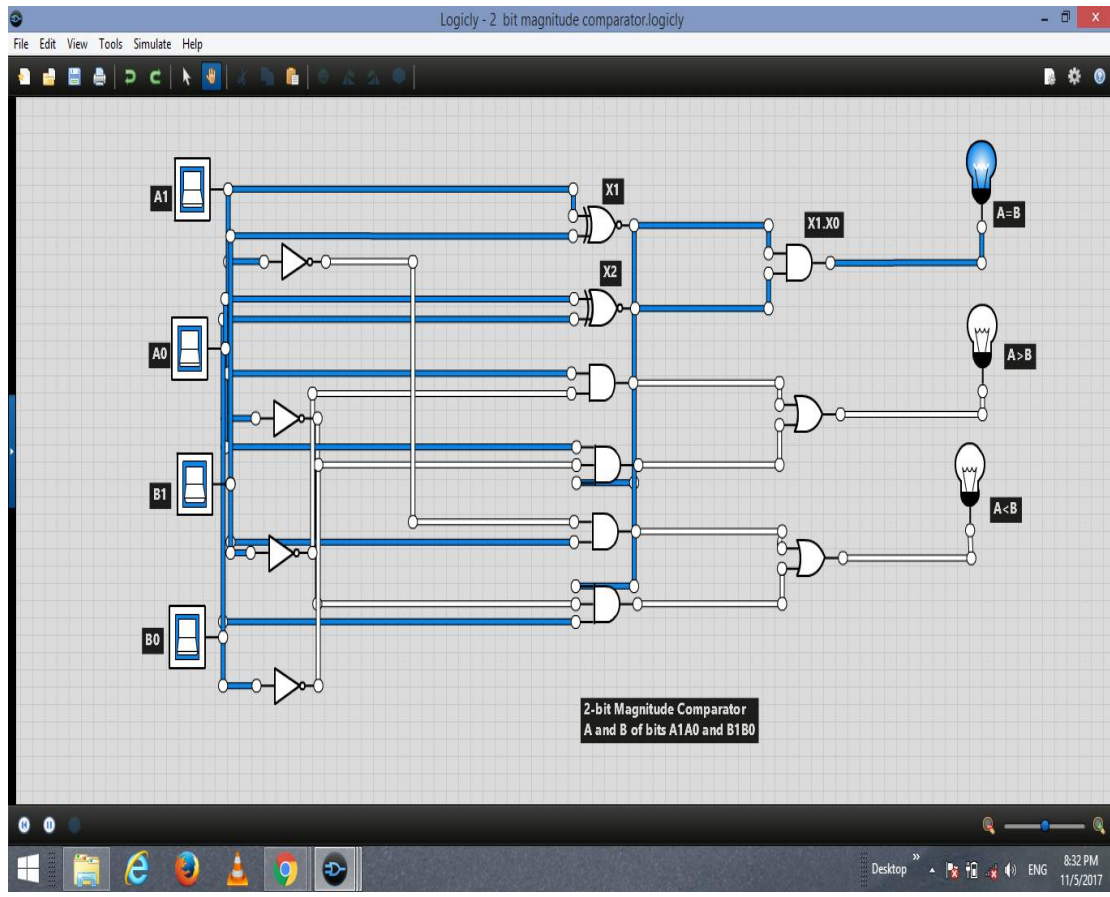
Assignment: 1 full adder and full subtractor using 3 to 8 decoder



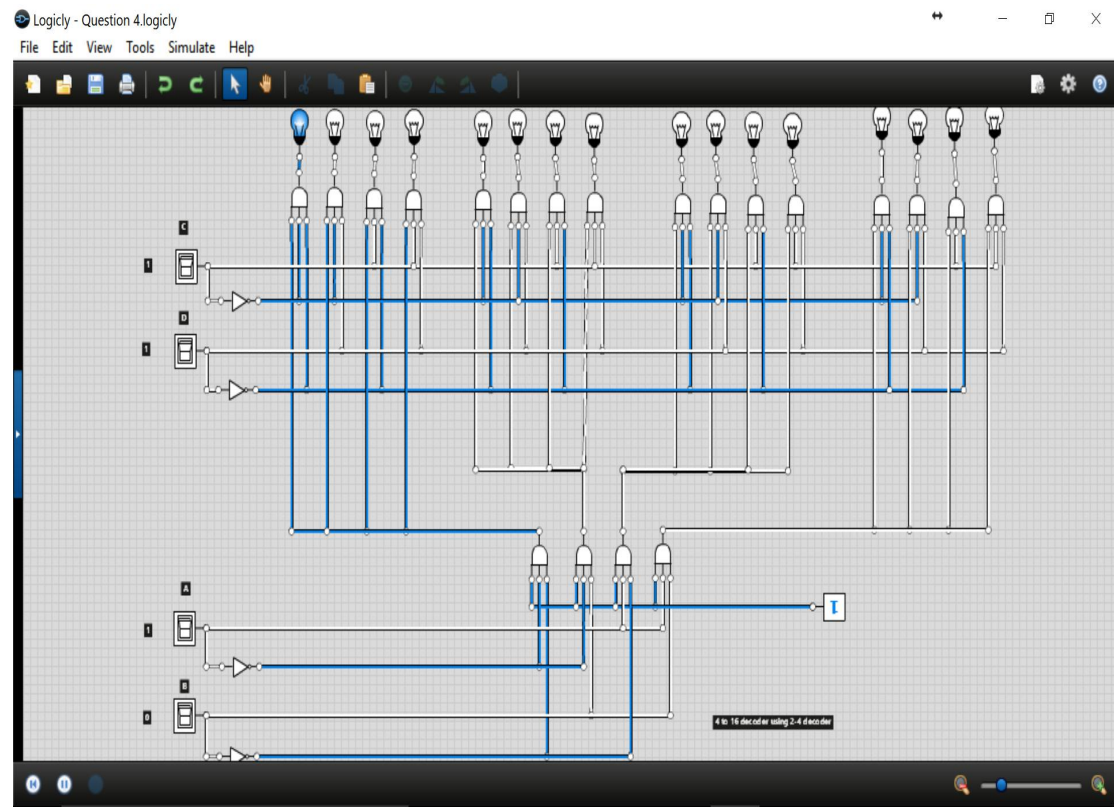
Assignment: 2 4-bit parallel adder and subtractor circuit



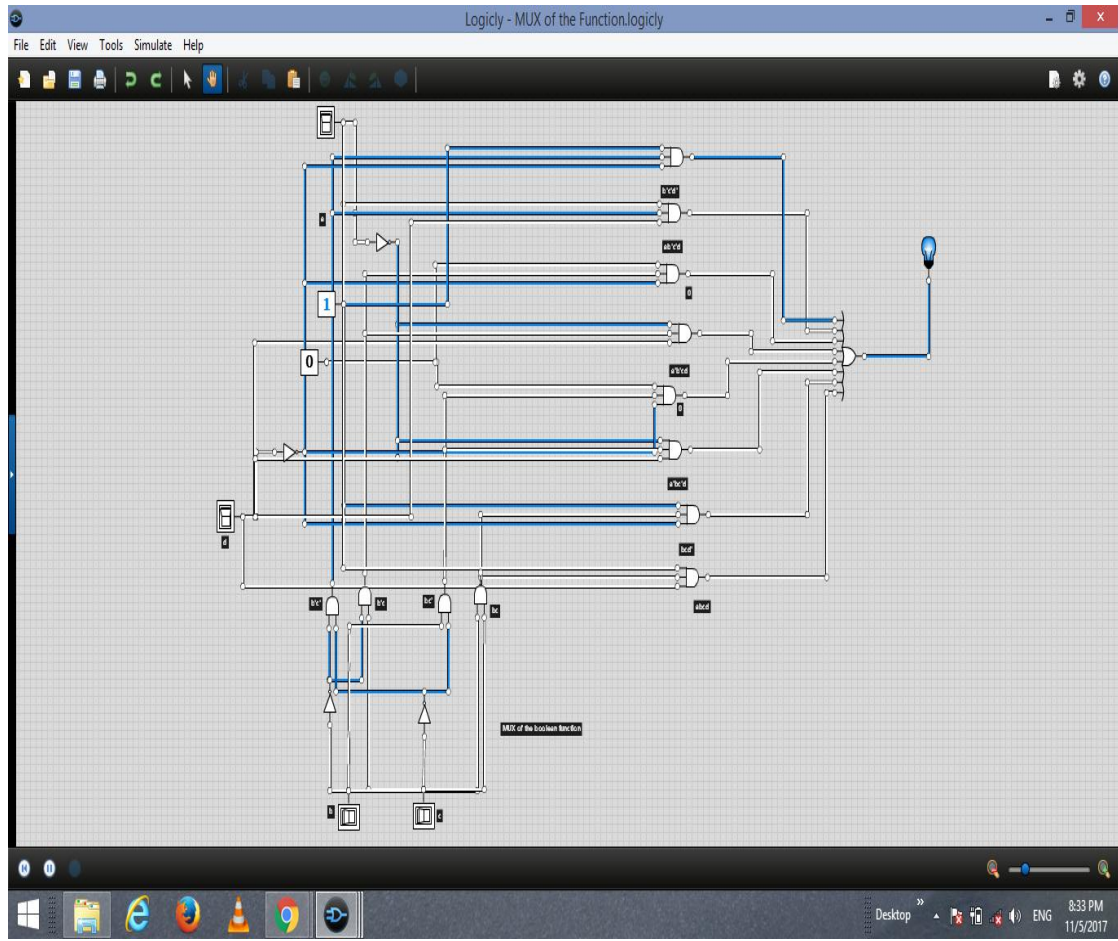
Assignment: 3 Magnitude Comparator(2-bit):



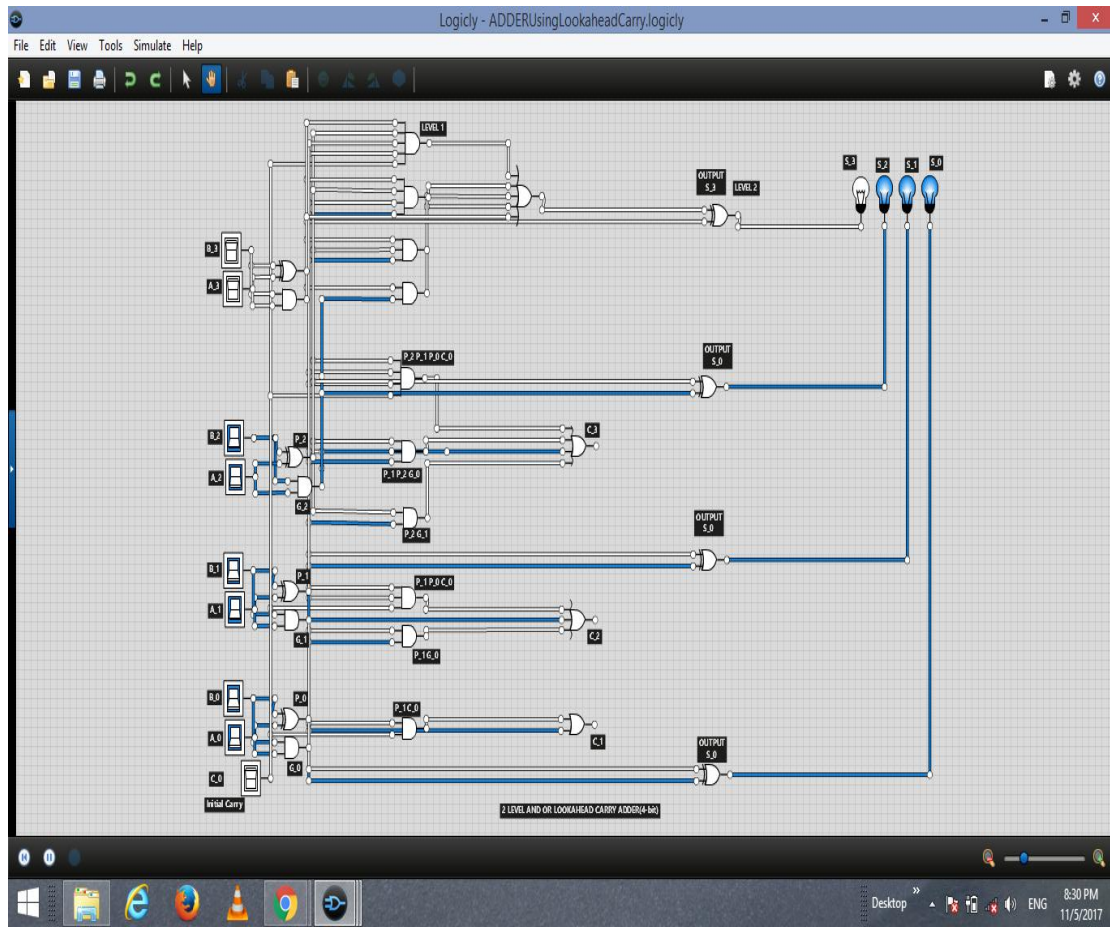
Assignment: 4 4 to 16 decoder using 2 to 4 decoders(enable i/p)



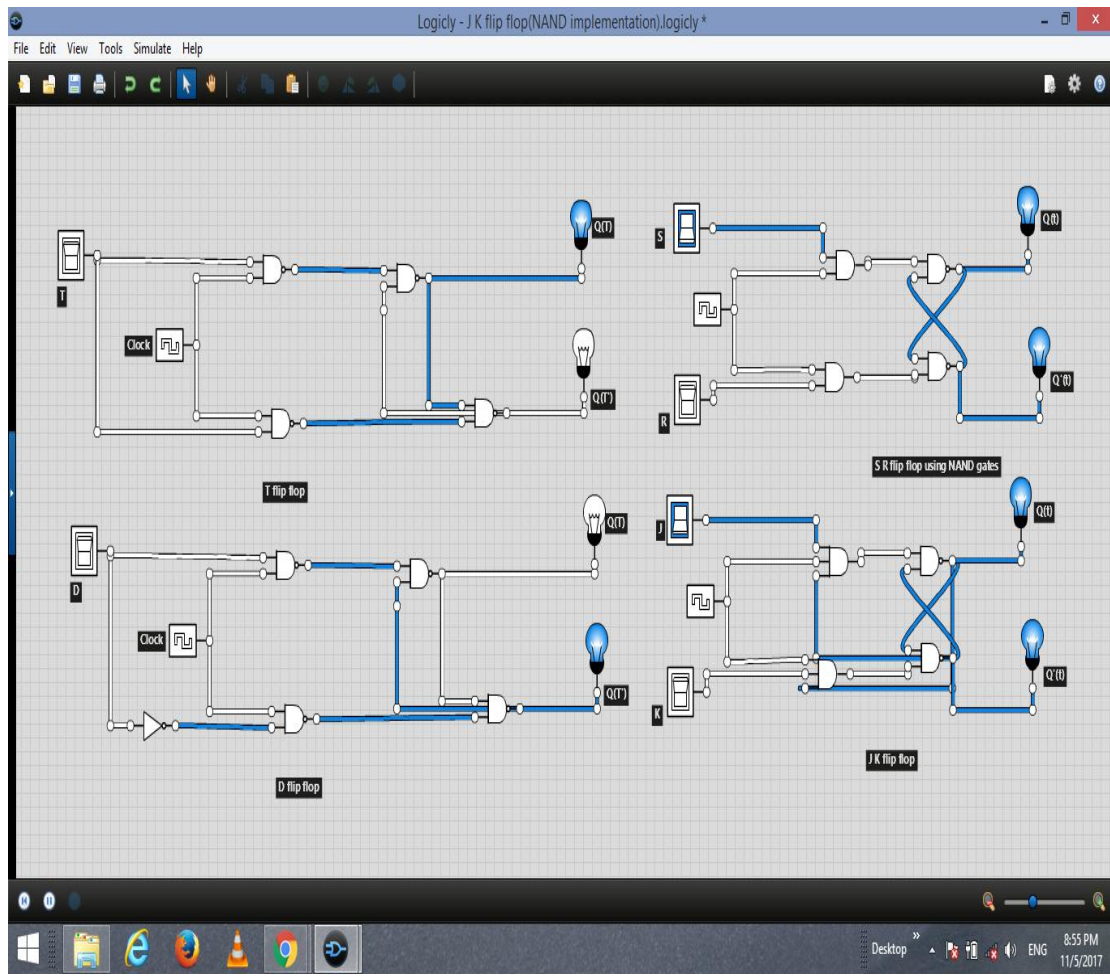
Assignment: 5 Design a MUX



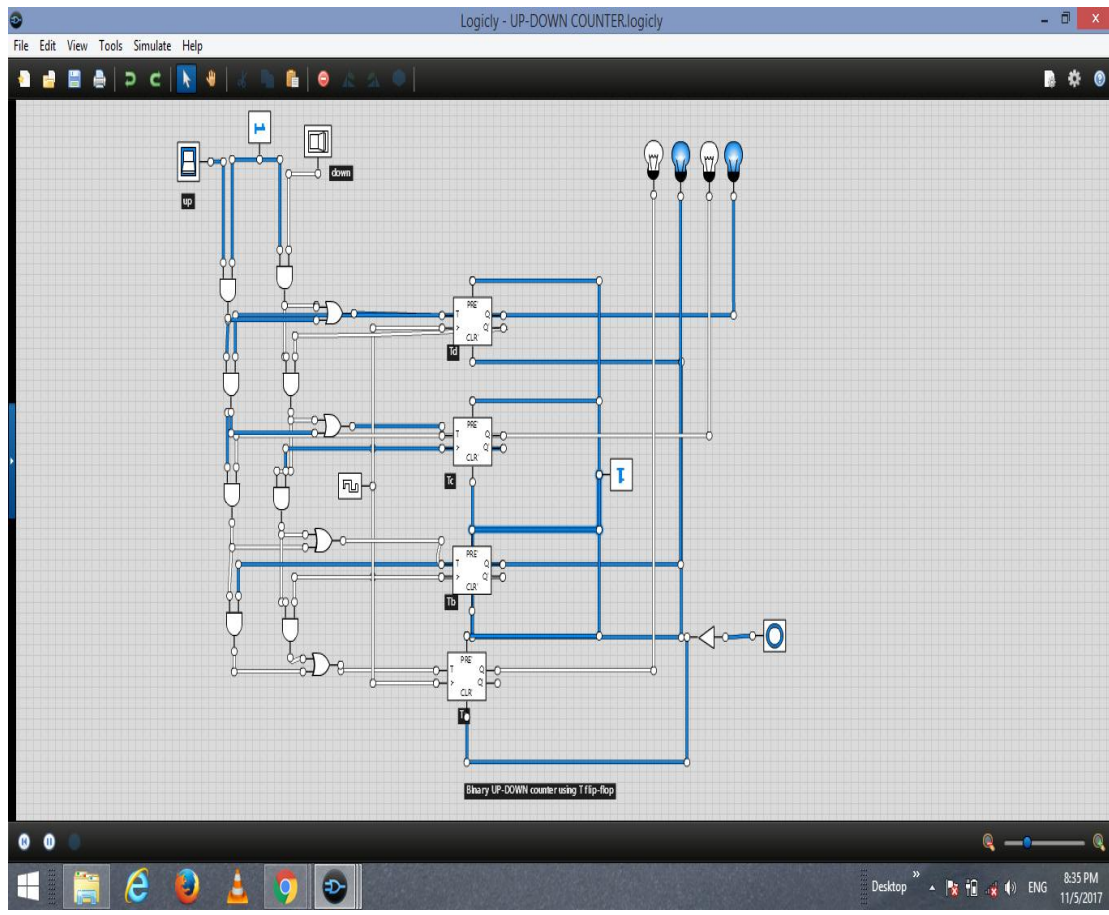
Assignment: 6 4 bit look-ahead carry adder



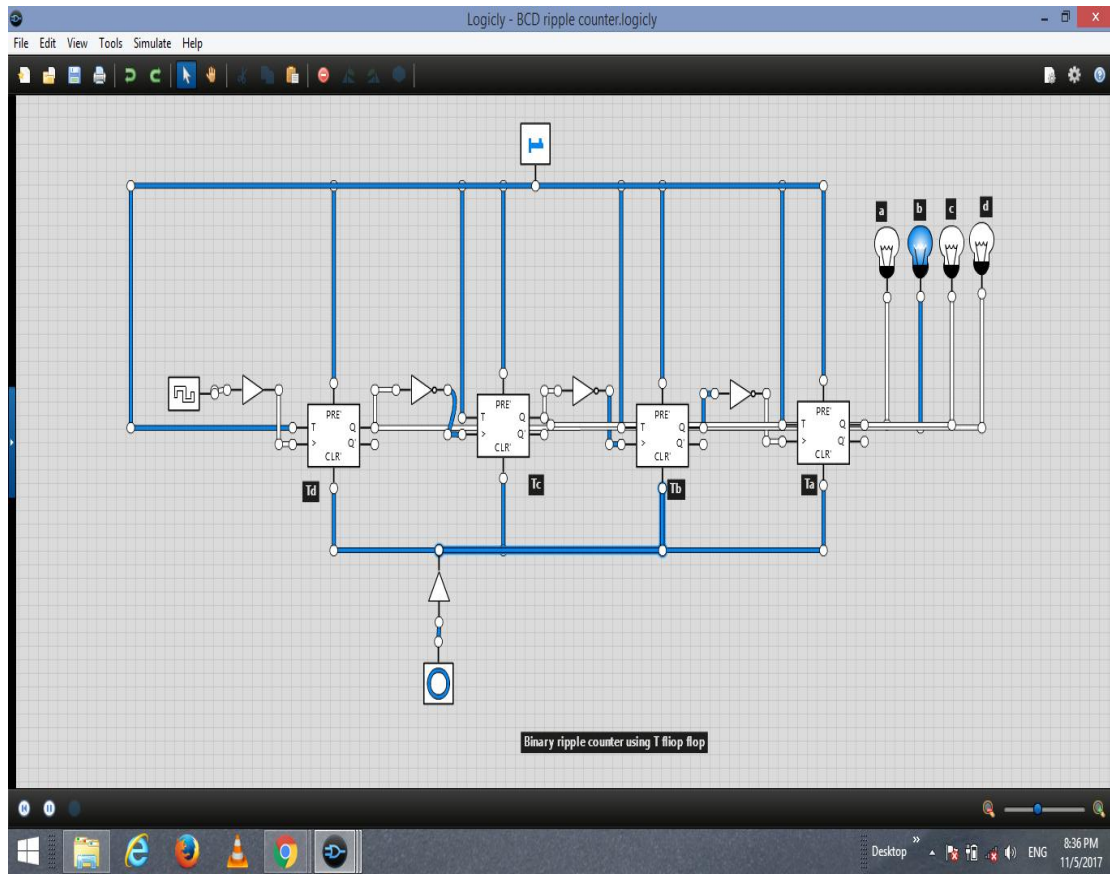
Assignment: 7 Flip-flops using Cross-coupled NAND gates



Assignment: 8 4-bit up and down counter using T flip flops



Assignment: 9 4-bit Binary ripple counter using T flip flops



End of the assignment