

PCB REPORT – LAB 11 and 12

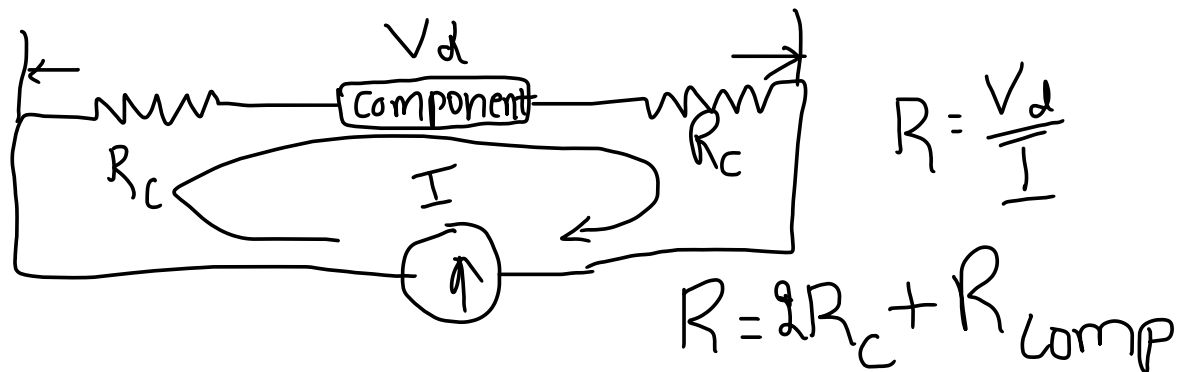
Measure trace resistance and blow-up traces

Objective:

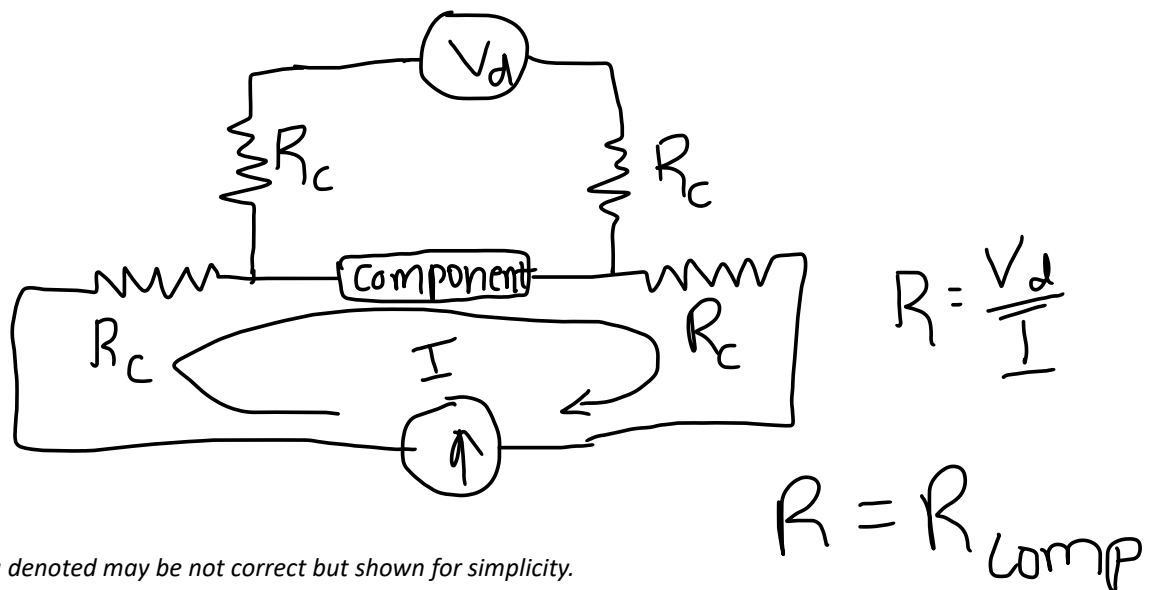
In this lab, we tried to measure the trace resistance of different sized traces ranging from 6mil, 10mil, 20mil and 100mil. We will make the measurements with a 2-wire and 4 wire method and try to find these measurements accuracy. In the subsiding lab will try to find the maximum current that trace can carry in safer conditions and what could be the maximum to blow up the traces.

Resistance Measuring methods:

2-wire: In this method, usually DMM's are used to measure resistance, where probe the component to measure its resistance. The current is passed through component via probes and measure the voltage drop across the component. Using Ohm's law to it shows the resistance. This is prone to inaccuracy, because the resistance value is from both component and the probe wire itself.



4-wire (Kelvin 4-wire): In this method the probes from DMM's are attached across component. Also, we connect the constant current source to the two ends of the component. Here the resistance from the probes connected is not considered, because the high impedance at the DMM's end doesn't allow the current to flow through the probes connected across the component. This allows us to read the correct voltage drop across the component and enables us to measure only the components resistance. This method is very useful in measuring the resistance value less than 0.1Ω .



The formula denoted may be not correct but shown for simplicity.

Board:

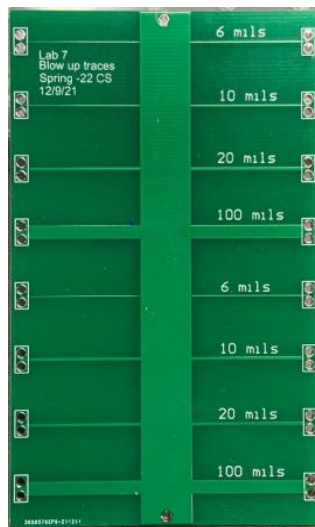


Figure 1.1 Board with different width traces

Calculation:

The resistance of the trace is given by $R = \rho * \text{length} / \text{Area}$, where ρ is resistivity of the material of the trace. The formula can also be described as $R = \rho * \text{length} / (\text{thickness} * \text{width})$, where Area is constituted from thickness and width of the trace. Further length/ width can be defined by η , It says that the trace is made of these many number of pieces. $\rho / \text{thickness}$ can be defined as R_{square} , it defines that the resistivity with respect to thickness of the trace.

Finally, $R = R_{\text{square}} * \eta$.

Let's calculate the resistance of the trace for different width traces, were $R_{\text{square}} = 0.5\text{m}\Omega/\text{square}$:

6 mil:

$$\eta = 1 \text{ inch} / 0.006 \text{ inch} = 166.66 \quad R = 83.3 \text{ m}\Omega$$

10 mil:

$$\eta = 1 \text{ inch} / 0.01 \text{ inch} = 100 \quad R = 50 \text{ m}\Omega$$

20 mil:

$$\eta = 1 \text{ inch} / 0.02 \text{ inch} = 50 \quad R = 25 \text{ m}\Omega$$

100 mil:

$$\eta = 1 \text{ inch} / 0.1 \text{ inch} = 10 \quad R = 5 \text{ m}\Omega$$

Observation:

Noted the values read from the DMM for each trace:

Line width	Estimated (in mΩ)	2-wire (in mΩ)	4-wire with 1 A current (in mΩ)
6 mil	83.3	130	63
10 mil	50	100	36.5
20 mil	25	80	18
100 mil	5	70	3.82

Table 1.1 Reading for each trace from DMM.

Using 4 wire method the resistance is calculated as $R_{\text{trace}} = V_{\text{measure}} / I_{\text{through}}$, where $I_{\text{through}} = 1\text{A}$.

6 mil:

$$V_{\text{measure}} = 62.96\text{mV}, R_{\text{trace}} = 63 \text{ m}\Omega$$

10 mil:

$$V_{\text{measure}} = 36.5\text{mV}, R_{\text{trace}} = 36.5 \text{ m}\Omega$$

20 mil:

$$V_{\text{measure}} = 18\text{mV}, R_{\text{trace}} = 18 \text{ m}\Omega$$

100 mil:

$$V_{\text{measure}} = 3.82\text{mV}, R_{\text{trace}} = 3.82 \text{ m}\Omega$$

Conclusion:

- From the above table, we can clearly say that the resistance measured using 4-wire is closer and lesser compared to the estimated one.
- The resistance measured using 2-wire is 50mΩ off due to additional resistance from probe leads and wire connecting to the DMM.
- So, we can conclude that 4 wire method preferable to measure the resistance less than 0.1 mΩ.

Board with Blown-up traces:



Figure 1.2 Board from blown-up traces

Observations:

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Embedded Resistors Er Effective Fusing Current Mechanical Information Min Conductor Spacing Ohm's Law Padstack Calculator
PDN Calculator Planar Inductors PPM-XTAL Calculator Thermal Management Via Properties Wavelength Calculator XL-XC Reactance
Bandwidth & Max Conductor Length Conductor Impedance Conductor Properties Conversion Calculator Differential Pairs / XTALK

Conductor Characteristics

Solve For:
☐ Amperage ☒ Conductor Width [? Help](#)

Plane Present?
☐ No ☒ Yes

Parallel Conductors?
☐ No ☒ Yes

Load Current: **0 Amps**

Conductor Width: **6 mils**

Conductor Length: **1000 mils**

PCB Thickness: **63 mils**

Frequency: **1 MHz** ☐ DC

IPC-2152 with modifiers mode Etch Factor: 2:1

Skin Depth: **2.59867 mils**

Power Dissipation: **0.07971 Watts**

Conductor DC Resistance: **0.08205 Ohms**

Skin Depth Percentage: **100%**

Power Dissipation in dBm: **19.0152 dBm**

Conductor Cross Section: **10.39 Sq.mils**

Loaded Voltage Drop: **0.0000 Volts**

Voltage Drop: **0.0809 Volts**

Conductor Current: **0.9857 Amps**

Options

Base Copper Weight:
☒ 0.25oz ☐ 0.5oz ☐ 1oz ☐ 1.5oz ☐ 2oz ☐ 2.5oz ☐ 3oz ☐ 4oz ☐ 5oz

Plating Thickness:
☐ Bare PCB ☐ 0.5oz ☐ 1oz ☐ 1.5oz ☐ 2oz ☐ 2.5oz ☐ 3oz

Plane Thickness:
☐ 0.5oz / 1oz ☐ 2oz

Conductor Layer:
☐ Internal Layer ☒ External Layer

Units:
☒ Imperial ☐ Metric

Substrate Options:
Material Selection: **FR-4 STD**
Er: **4.6** Tg (°C): **130**

Temp Rise (°C): **20**
Temp in (°F) = 36.0

Ambient Temp (°C): **22**
Temp in (°F) = 71.6

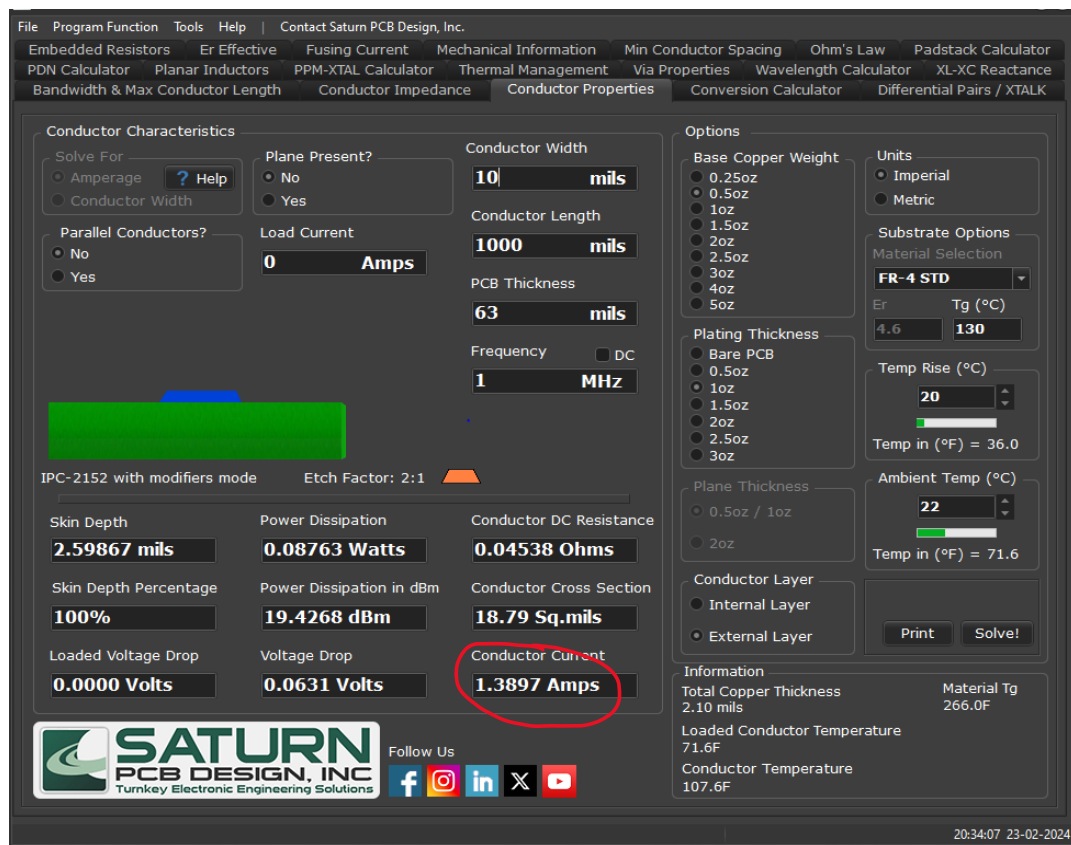
Information:
Total Copper Thickness: 2.10 mils
Loaded Conductor Temperature: 71.6F
Conductor Temperature: 107.6F
Material Tg: 266.0F

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Saturn software shows that estimated maximum current susceptible for 6mil trace should be 0.9857A.



Furthermore, the maximum current susceptible for 10mil trace should be 1.3897A.

Trace width	Safety current limit	Current reading when board trace to heat up (40 Celsius)	Current reading when board trace blow-up
6 mil	0.9857A	2.5A(100mV)	4.280A(700mV)
10mil	1.3897A	2.5A(105mV)	6A(500mV)

Table 1.2 Current applied at certain stages of the experiment.

From the above table we can see the safety current measurement for 6 and 10mil. Also observed the current reading measured at the stages board trace started heating up and blown out. During this tested the relation between temperature and resistance and voltage drop across the trace, where resistance increases with temperature, so do the voltage drop.

Conclusion:

- To avoid trace damage, we should follow strict current limit according to trace width, because incorrect estimate and design can lead to damage of trace and other components on the board.

Key learnings:

- Trace also has minute resistance, which depends on material and width of trace.
- Importance of 4-wire method in finding resistance less than 0.1ohm.
- Importance of trace width, current limit consideration while designing the board and its trace to work in ideal conditions such as negligible resistance.