### **UNIBALLOT**

Done by

P.J MUHAMMED NAZEEL-- Reg no: 210021086929

EAPPEN VARGHEESE THOMAS--Reg no: 210021086913

Under the guidance of Ms. Preethy George

In partial fulfillment of the requirements for the award of the Degree of

**Bachelor of Computer Applications** 

**O**f

Mahatma Gandhi University, Kottayam, Kerala



### **Department of Computer Science**

### NIRMALA COLLEGE, MUVATTUPUZHA

(Affiliated to Mahatma Gandhi University)

2021-2024

# NIRMALA COLLEGE (Affiliated to Mahatma Gandhi University) MUVATTUPUZHA



## **CERTIFICATE**

Certified entitled					v	•			1 0		
	ut of th	ne req	 quire	 mei	during uts for th	the year	r d of t	······································	in the	ne parti Bache	ial lor of
Int	ternal g	uide						Head (	of the depa	artment	ţ
	Subm	itted fo	or th	e Vi	iva-voce E	Examina	tion h	eld on	······································	•••••	

**External Examiner** 

#### **ACKNOWLEDGEMENT**

We give all honor and praise to the **Lord** who gave wisdom and enable to complete our project successfully.

We express our sincere and heartful thanks to respected **Dr. Thomas K.V**, our beloved principal for providing necessary facilities for the completion of our project successfully.

We think profusely **Ms. Preethy George,** Course Coordinator of BCA Department for her guidance and inspiration throughout our course of study.

We express our sincere gratitude to our project guide, **Ms. Preethy George**, Assistant Professor in BCA Department for the valuable advices and guidance throughout the completion of our project.

We also express our gratitude and thanks to all my teachers and friends for their sincere and friendly cooperation in the successful completion of our project. \_

### **CONTENTS**

Introduction				
1.1 Objective of the project	1			
2. System Analysis				
2.1 Existing System	2			
2.2 Proposed System	3			
2.3 System Requirement Specification (SRS)	6			
2.3.1 Hardware specification	6			
2.3.2 Software specification	6			
2.3.3 Front End	7			
2.3.4 Back End	10			
2.4 Feasibility Analysis	10			
2.5 Data Flow Diagram	12			
2.5.1 Introduction to Data Flow Diagram	12			
3. System Design				
3.1 Input Design	17			
3.2 Output Design	17			
3.3 Table Design	18			
4. System Testing & Implementation	24			
4.1 System Testing	26			
4.2 System Implementation	27			

5. Security Technologies & Policies	29
6. Maintenance	32
7. Scope for Future Enhancements	34
8. Conclusion	35
9. Bibliography	36
10. Appendix	37
10.1 Screen Shots	37



















