Make program allows to use macro instead of variables as they are similar to variables.

Makefile

**Flags:**

Extra piece of text that accompany a program from the command line.

*For example:*

ls

The above command will isplay all unhidden files

ls -a

The above command will display all the files including the hidden files.

Flags can even be accompanied with arguments.

Ex:

tar -z -c directory.tgz -f directory

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Note:

g++ -o fileName fileName.cpp -Wall

Here Wall it display all warning messages that are applicable to the program.

**Compiling more than one file into a single program:**

It can be done in two ways,

1. Supply all source file in one compilation.

2. we can compile them seperately and integrate them into a single executable at a later time too.

**Variables:**

Variables in makefile do not have type and are interpreted as strings. Commonly used variable names are:

> CFLAGS

> CC

These variable can quickly change the behaviour of the makefile.

To use the value stored in the variable,

${var\_name}

**Commens:**

# is used for commenting.

**Dependencies:**

It’s the rule that must adhere to.

[name of rule] : [list of other rules, separated by spaces] [list of source files, separated by spaces]

[TAB]command to execute in the event that the rule is violated.

G++ -c file.cpp

# this command gives assembly code