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CSE 325 Embedded Microprocessor Systems
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Lab Project 2
Team Member 1: 50%
Team Member 2: 50%

Question 1:
Actual 2000_1A4E
Calculated 2000_1B18

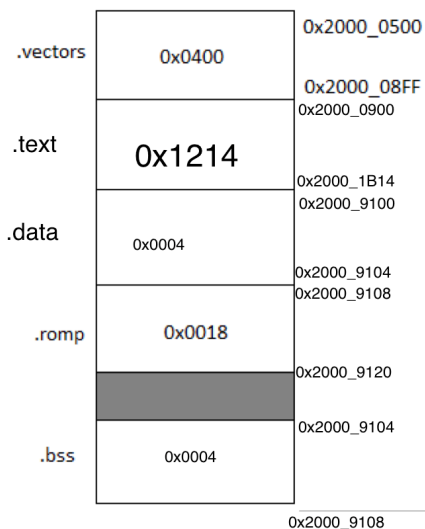
Question 2:
This tells us that the multibyte data is stored as little endian, meaning the most significant byte occupies the lowest address.

Question 3:
entry = 0x20000BFE

Question 4:
The labels name is __startup and it is contained in file startcf.c

Question 5:
0x20000C98 and its 36 bytes in decimal

Question 6:



Question 7:

VDD Pins: 135, 125, 115, 100, 92, 59, 48, 30, 20, 7
VSS Pins: 134, 124, 114, 99, 91, 60, 49, 31, 21, 8

Question 8:

The frequency of Ref Clock would be

Question 9:

To configure

We would have to write the decimal value 6 or 110 into MFD and 000 into RFD of SYNCR to obtain a clock frequency of 72 MHz.

Question 10:

No it is not possible to obtain a system frequency = 50MHz. The closest we could get to 50 MHz without exceeding that value is 50.

Question 11:

DDRTC: 0x00000648 = 0000_0110_0100_1000

PTCPAR: 0x200FBFF = 0010_0000_0000_0000_1111_1011_1111_1111