Implementation Plan

Current Status

- Lexer: Done
- Parser: Done
- Data Structures and Interfaces: Done
- Bitstream Utility Functions: Done

Timeline

- Nov 11 - Nov 14: Finish Simulator (AST evaluation)
- Nov 14 - Nov 18: Extend Parser and AST to support subcircuit definitions
- Nov 14 - Nov 21: (In parallel with above) Write Formatter and Renderer
- Nov 21 - Nov 30: Test! Test! Test! Write up final presentation (Maybe extend functionality too...?)