Homework #1 (v170412)

(Deadline: 11:59PM PDT, April 21, 2017)

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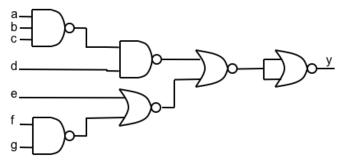
INSTRUCTIONS

This homework is to be done individually. You may use any tools or refer to published papers or books, but may not seek help from any other person or consult solutions to prior exams or homeworks from this or other courses (including those outside UCLA). You're allowed to make use of tools such as Logisim, WolframAlpha (which has terrific support for boolean logic) etc.

You must submit all sheets in this file based on the procedure below. Because of the grading methodology, it is much easier if you print the document and answer your questions in the space provided in this problem set. It can be even easier if you answer in electronic form and then download the PDF. Answers written on sheets other that the provided space will not be looked at or graded. Please write clearly and neatly - if we cannot easily decipher what you have written, you will get zero credit

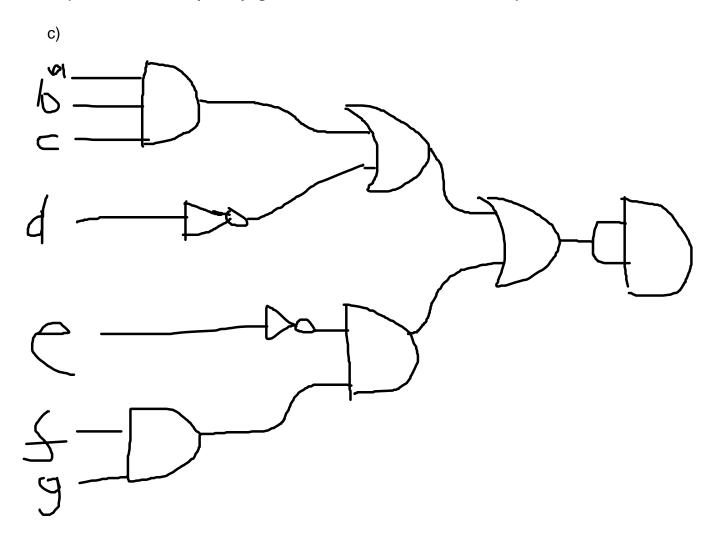
SUBMISSION PROCEDURE: You need to submit your solution online at Gradescope (https://gradescope.com/). Please see the following guide from Gradescope for submitting homework. You'd need to upload a PDF and mark where each question is answered.

http://gradescope-static-assets.s3-us-west-2.amazonaws.com/help/submitting hw guide.pdf



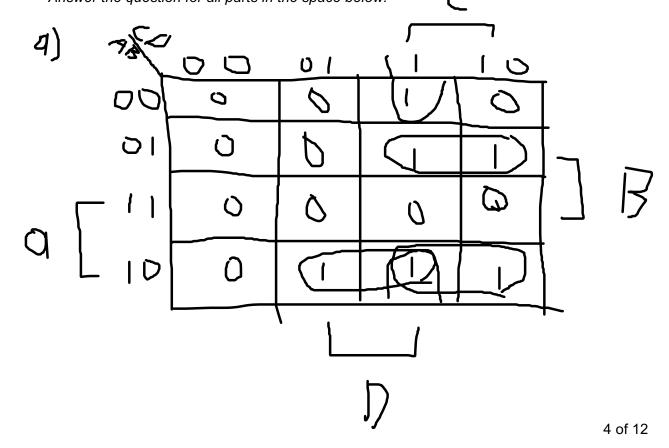
- (a) Write the equation in Normal Form (does not need to be fully disjunctive) for the logical function shown in the implementation above.
- (b) Find the dual of the function. Show the result in Normal Form (does not need to be fully disjunctive).
- (c) Push the bubbles for the implementation above (you should not change the logic gates but may use the DeMorgan representations of the logic gate) so that the function is easier to visually comprehend.

Answer the question for all parts in the space below.



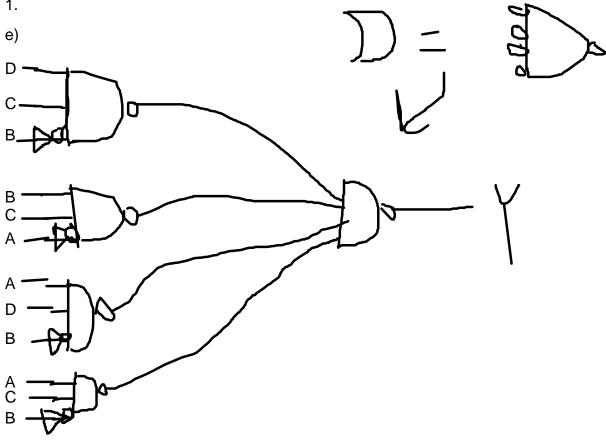
Α	В	С	D	Υ
0	0	0	0	0
0	0	0	1	0
0	0	1	0	0
0	0	1	1	1
0	1	0	0	0
0	1	0	1	0
0	1	1	0	1
0	1	1	1	1
1	0	0	0	0
1	0	0	1	1
1	0	1	0	1
1	0	1	1	1
1	1	0	0	0
1	1	0	1	0
1	1	1	0	0
1	1	1	1	0

- (a) Draw K-Map of the function corresponding to the truth table shown above.
- (b) Identify the prime implicants of the function.
- (c) Identify which of the prime implicants (if any) are essential.
- (d) Write the cover as a logical function using prime implicants.
- (e) Draw a logic gate diagram using the equation in (d) and using only NAND gates. Answer the question for all parts in the space below.



B) D) (d $\land \land$ c $\land \land$ ~b) $\land \lor$ (b $\land \land$ c $\land \land$ ~a) $\land \lor$ (a $\land \land$ ~b) $\land \lor$ (a $\land \land$ c $\land \land$ ~b)

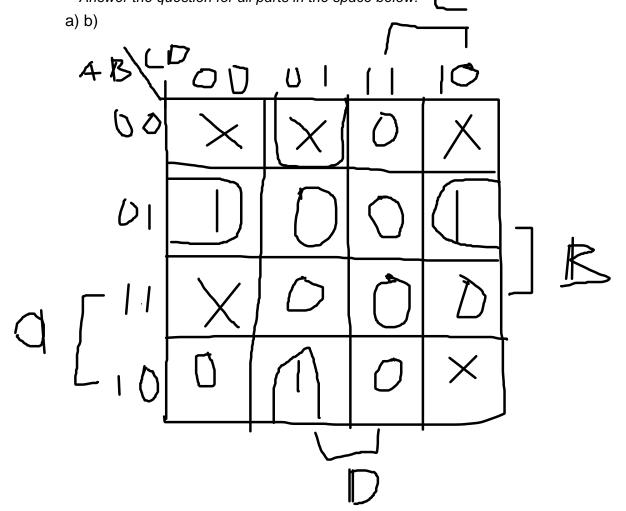
C) the four prime implicants above are all essential since each one contains a unique



Α	В	С	D	Υ
0	0	0	0	Х
0	0	0	1	Х
0	0	1	0	Х
0	0	1	1	0
0	1	0	0	1
0	1	0	1	0
0	1	1	0	1
0	1	1	1	0
1	0	0	0	0
1	0	0	1	1
1	0	1	0	Х
1	0	1	1	0
1	1	0	0	Х
1	1	0	1	0
1	1	1	0	0
1	1	1	1	0

- (a) Draw K-Map of the function corresponding to the truth table shown above. Draw all the prime implicants.
- (b) Highlight the cover of the function on the K-Map using the prime implicants.
- (c) Write the logical function using the results in (b)

 Answer the question for all parts in the space below.



c)

$$(d \land \land \neg c \land \land \neg b) \lor \lor (b \land \land \neg a \land \land \neg d)$$

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Problem #4

Simplify the following functions and find a simple implementation using AND, OR and INV gates.

- (a) $Y = (\neg a \land b \land c) \lor \neg (b \land \neg c) \lor (b \land c)$
- (b) $Z = (a \land b \land \neg c) \lor \neg (a \land \neg (\neg b \land c)) \lor \neg (\neg d \lor e) \land c$ Answer the question for all parts in the space below.

(~b /\ c) $(a \land \land b \land \land \neg c) \lor (\neg a \lor (\neg b \land \land c)) \lor (d \land \land \neg e) \land \land c$ $(a \land \land b \land \land \neg c) \lor \lor \neg a \lor \lor (\neg b \land \land c) \lor \lor (d \land \land \neg e \land \land c)$ $((a \lor \lor \neg a) \land \lor (b \lor \lor \neg a) \lor (\neg a \land \lor \neg c)) \lor (\neg b \land \lor c) \lor (d \land \lor \neg e \land \lor c)$ $(-a \lor b \land \lor -c) \lor (-b \land \lor c) \lor (d \land \lor -e \land \lor c)$ \sim a \V (b \land \ \sim c) \V (\sim b \land \\ c) \V (d \land \\ \sim e \land \\ c)

You may use whatever source desired to look up the definitions below.

- (a) Define "Gray Code".
- (b) Define "Cyclic Gray Code".
- (c) Design a 3-bit cyclic Gray code ($G=[g_2, g_1, g_0]$) such that all possible combinations of G are included.

Answer the question for all parts in the space below.

- a) Gray code a binary numeral system in which two successive values differ in exactly one bit.
- b) Cyclic Gray Code- only one value changes when going from the last bit to the first
- c) 000 001 011 010 110 111 101 100

FPGAs are designed such that arbitrary logic for a number of inputs and outputs can be implemented within the blocks. The amount of logic functions that can be implemented grows rapidly with the number of inputs. We saw this in lecture with 2-inputs.

- (a) With 4-bit inputs, how many functions are possible?
- (b) With 4-bit inputs, how many functions where half of the outputs are 1's (the other half are 0's) are possible.

Answer the question for all parts in the space below.

a)
$$2^{16} = 65,536$$

b) with 16 bits we need exactly 8 1's and 8 0's. We can find the number of unique combinations for these outputs by:

$$\frac{16!}{8! \cdot (16-8)!}$$