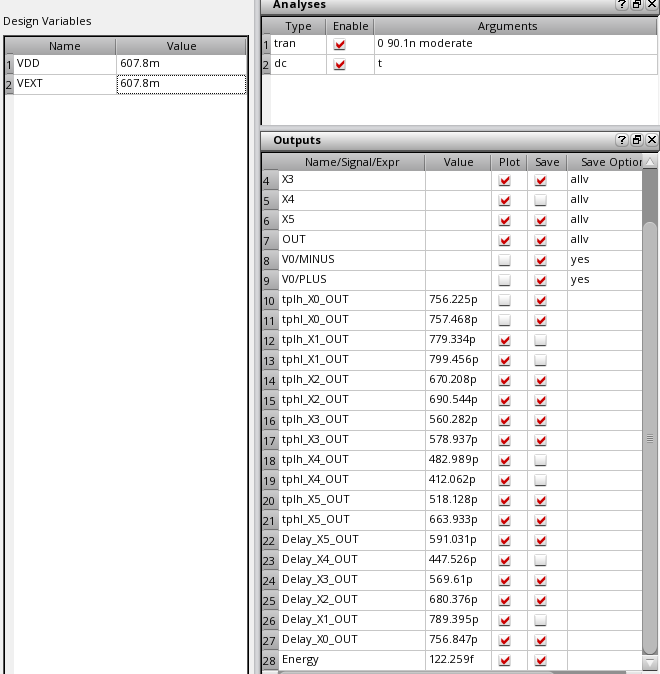
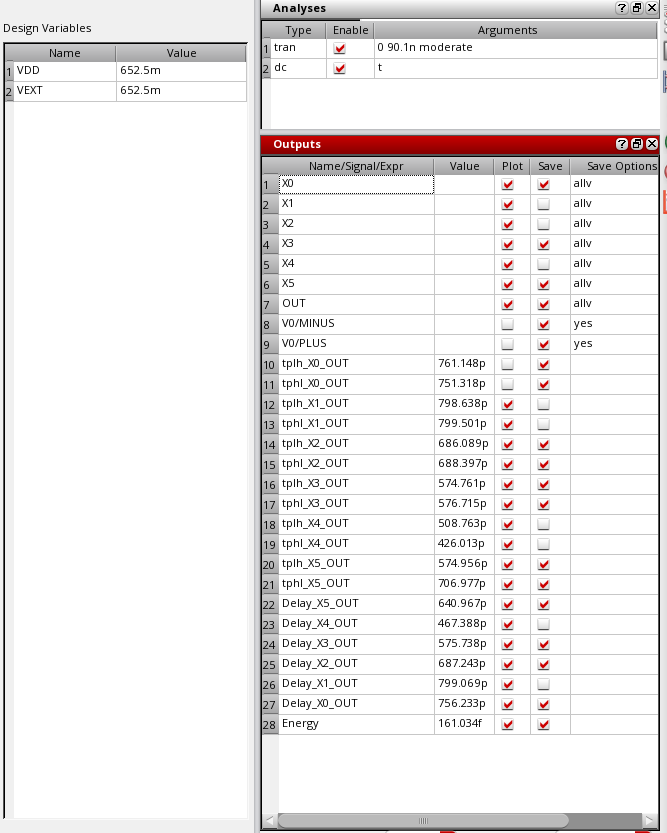
**Screenshots Pre-Layout**

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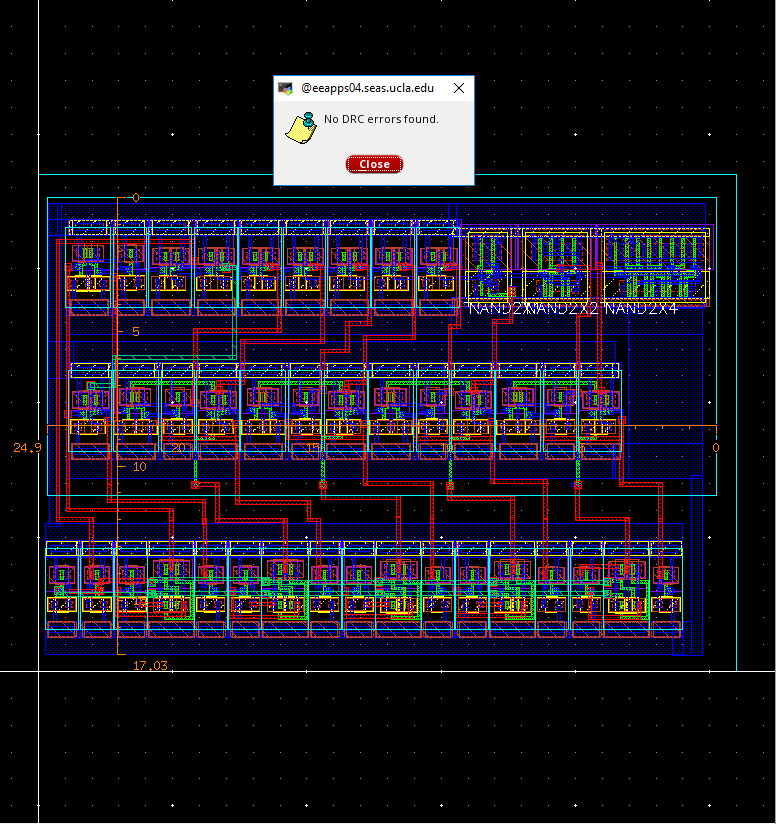
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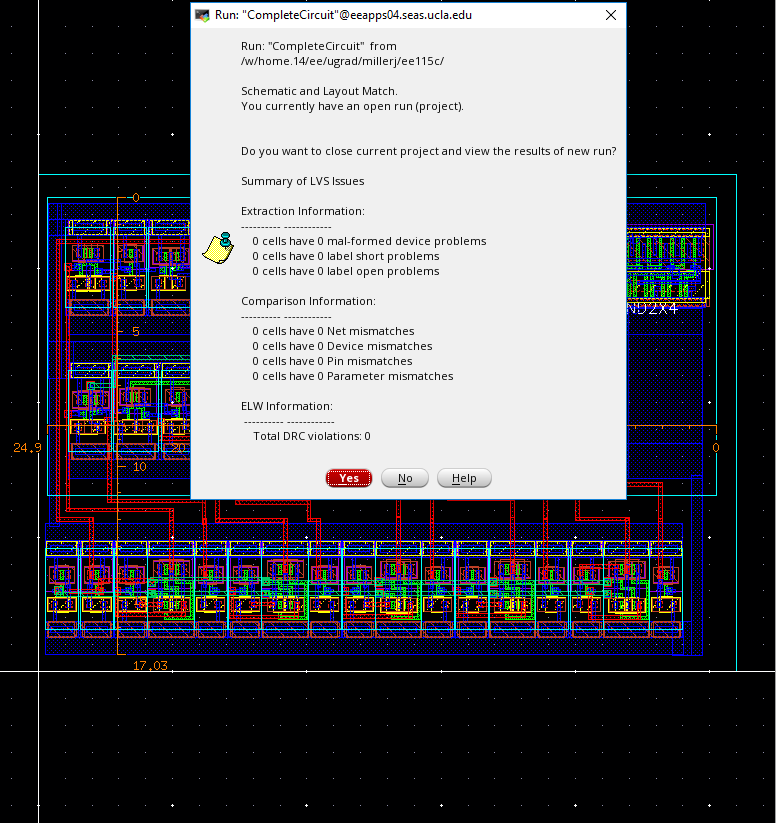
**Screenshots Post Layout**

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**Layout Verification**

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**Work Division**

Alexander:

Built schematic view for AdderModule, Built and tested carry Bypass Adder to compare with CLA, designed layout for PTL Mux half sized NAND, NOR, Inverter, as well as layout for Adder Module.

Joseph:

Designed circuit used for schematic, sized schematic, built schematic view for Inverting Stage, designed layout for Inverting Stage and Abs\_Value\_Detector.