

@concl same pinouts

CC1200EMK Pinout

@src *RF Layout Reference Design for 868-930 MHz (CC1200) Schematic - tldr180.pdf*

P1				P2			
1	GND	2		1		2	GND
3		4		3		4	
5		6		5		6	
7		8		7	P3v3	8	
9		10	GPIO0	9	P3v3	10	
11		12	GPIO2	11		12	
13		14	CSN	13		14	
15		16	SCLK	15	RESET	16	
17		18	MOSI	17		18	GPIO3
19	GND	20	MISO	19		20	

CC1175EMK Pinout

@src *CC1175EM_868_915_schematic.pdf*

P1				P2			
1	GND	2		1		2	GND
3		4		3		4	
5		6		5		6	
7		8		7	P3v3	8	
9		10	GPIO0	9	P3v3	10	
11		12	GPIO2	11		12	
13		14	CSN	13		14	
15		16	SCLK	15	RESET	16	
17		18	MOSI	17		18	GPIO3
19	GND	20	MISO	19		20	

@note These chips are from different families, with close sounding names!

- CC1200 - CC12xx Family
- CC1175 - CC112x Family