

[2] "When inventor meets IOT"

[2] → [3] mention another open P switch

[8] Schematic

- Clear segmentation and modularization of design
- Uniform, consistent layout
- Clear labelling with notes

[10] Library

- Uniform naming schema
- Consistent, uniform symbols
- Complete silkscreen & outline markup
- He had taken from a plethora of misc., open source libraries and performed modification with out tracking
- To make this co-exist with design practices moving forward I did an 'orig/', a 'ref/' and then the LBR!

[11] Board

- Clear segmentation and modularization of design
- Consistent part selection
- Clean selection and placement for external I/O
- Design flexibility for future expansion

Switching  
From

[12] Board Layout

- This is interesting, sharing a board floorplan without layout, sharing a completed layout for reference

[12] Firmware

- My professional career has spent most efforts and time in firmware and would like to share with team

GRACKETS

- SLICES (3 per piece)

ADD CHECKPOINTS

WHEN FINISHING UP A POINT,  
ASK IF THERE ARE ANY QUESTIONS

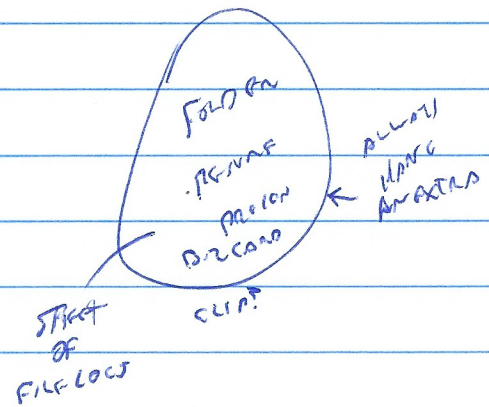
REFERENCES  
ARE  
THE LAST LAST  
STEP

PLEASE  
DON'T  
ALWAYS  
ON QUESTIONS  
APPLY IT TO THE

DON'T TRY TO  
THINK ON  
THE WALL + HOW YOU  
COMMUNICATE THAT

Relax

DRON  
SERIAL  
CHECK  
ON COMA



[LPA] OUTLINE, ONE OF MY FAVORITES IS A NATURAL RESULT OF  
THE OUTLINE OBSERVED HERE, THIS IS OFTEN WHY I AM BRACKET  
INTO A TEAM + PROJECT

INFORMATION  
REDUNDANCY  
CHECK  
ON

- DETECTS INTRO  
SLIDE  
- WHAT  
+ WHY, FOR  
INTRO

REF → WHAT, WHY + HOW  
BULLET POINTS