Pin Diagrams (PIC24FJ256GA702 Devices)
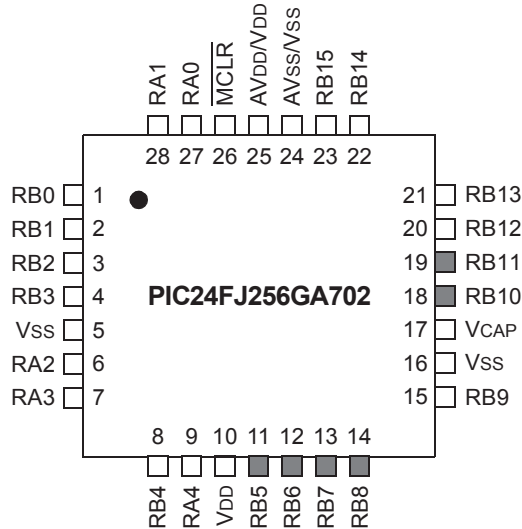
28-Pin QFN, UQFN

Legend: See Table 1 for a complete description of pin functions. Pinouts are subject to change.

Note: Gray shading indicates 5.5V tolerant input pins.

TABLE 1: COMPLETE PIN FUNCTION DESCRIPTIONS (PIC24FJ256GA702 QFN, UQFN)

Pin	Function	Pin	Function
1	PGD1/AN2/CTCMP/C2INB/**RP0**/RB0	15	TDO/C1INC/C2INC/C3INC/TMPRN/**RP9**/SDA1/T1CK/CTED4/RB9
2	PGC1/AN1-/AN3/C2INA/**RP1**/CTED12/RB1	16	Vss
3	AN4/C1INB/**RP2**/SDA2/CTED13/RB2	17	VCAP
4	AN5/C1INA/**RP3**/SCL2/CTED8/RB3	18	PGD2/TDI/**RP10**/OCM1C/CTED11/RB10
5	Vss	19	PGC2/TMS/REFI1/**RP11**/CTED9/RB11
6	OSCI/CLKI/C1IND/RA2	20	AN8/LVDIN/**RP12**/RB12
7	OSCO/CLKO/C2IND/RA3	21	AN7/C1INC/**RP13**/OCM1D/CTPLS/RB13
8	SOSCI/**RP4**/RB4	22	CVREF/AN6/C3INB/**RP14**/CTED5/RB14
9	SOSCO/PWRLCLK/RA4	23	AN9/C3INA/**RP15**/CTED6/RB15
10	VDD	24	AVSS/VSS
11	PGD3/**RP5**/ASDA1/OCM1E/RB5	25	AVDD/VDD
12	PGC3/**RP6**/ASCL1/OCM1F/RB6	26	MCLR
13	**RP7**/OCM1A/CTED3/INT0/RB7	27	VREF+/CVREF+/AN0/C3INC/**RP26**/CTED1/RA0
14	TCK/**RP8**/SCL1/OCM1B/CTED10/RB8	28	VREF-/CVREF-/AN1/C3IND/**RP27**/CTED2/RA1

Legend: **RPn** represents remappable pins for Peripheral Pin Select (PPS) functions.

Note: Pinouts are subject to change.