

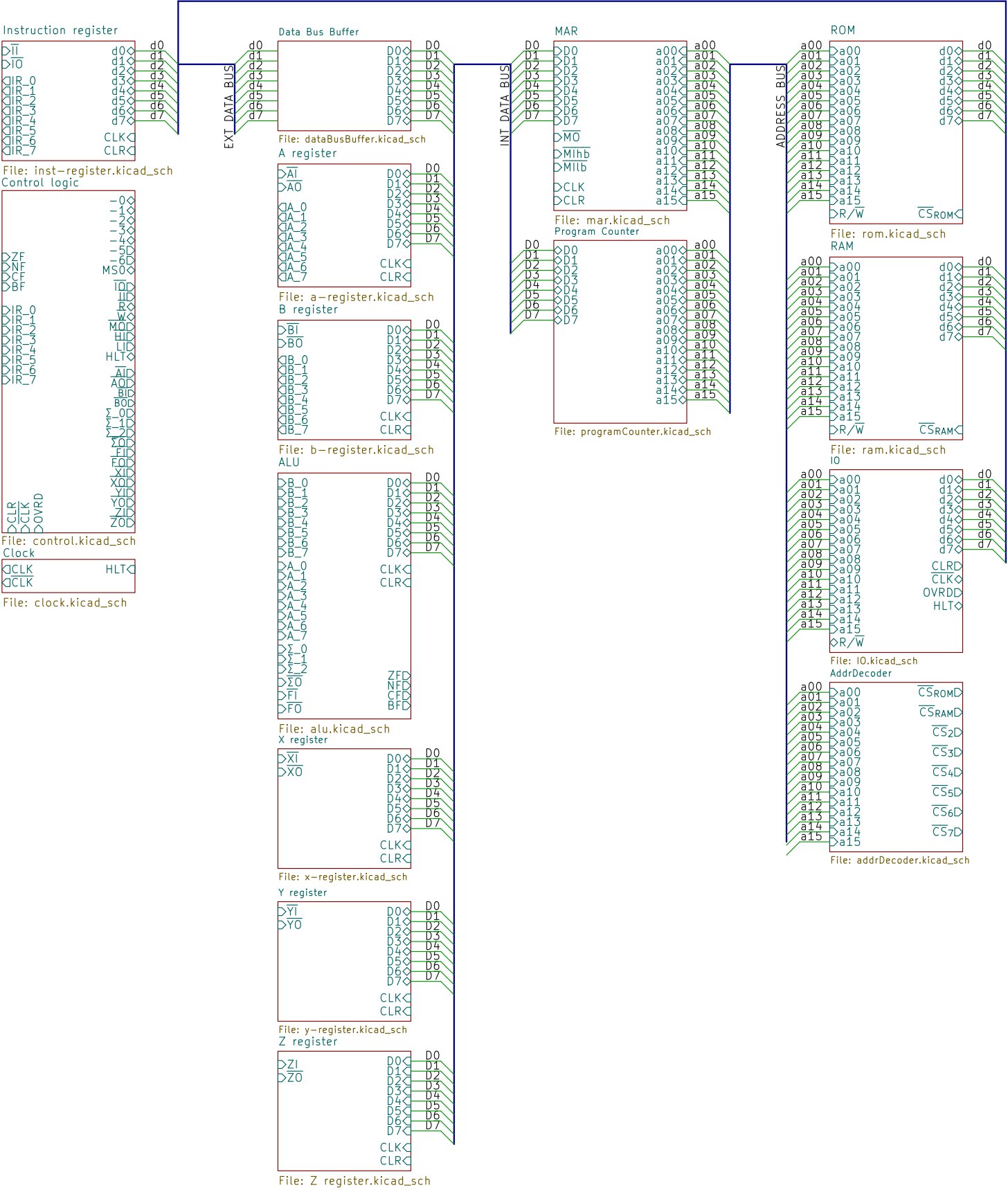
TODO:  
BOM  
R/WD

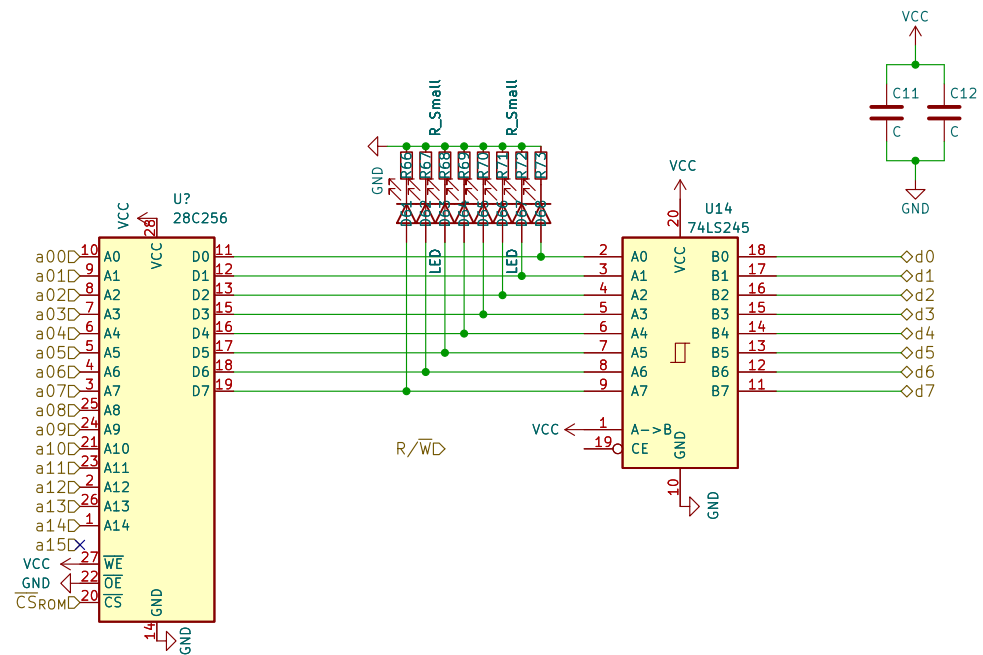
Control Section

Register Section

Address Section

Memory Section





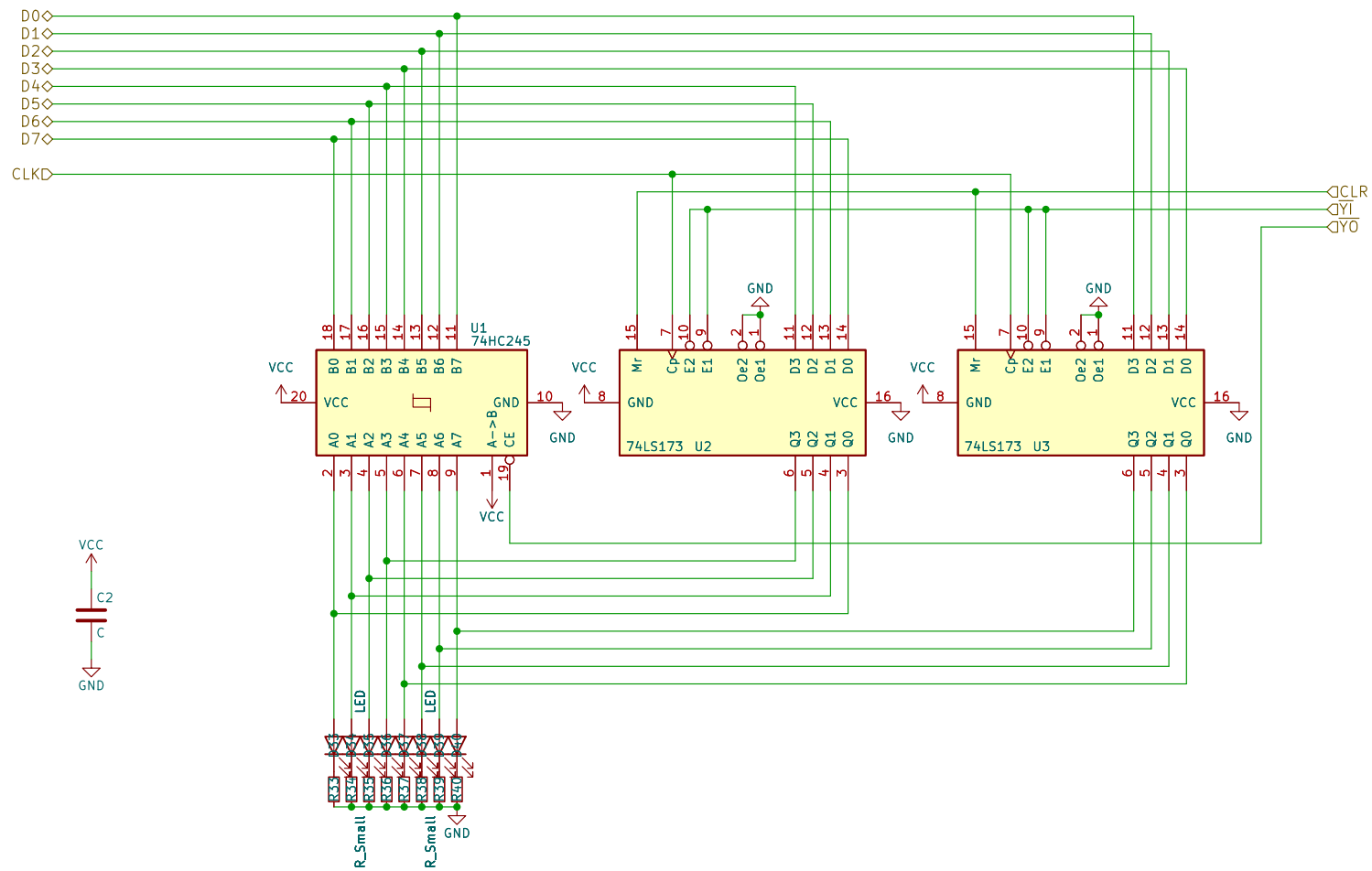
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KiCad E.D.A. kicad (6.0.8)

Date:  
Id: 2/17

Rev:



Sheet: /Y register/  
File: y-register.kicad\_sch

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Date:

**Rev:**

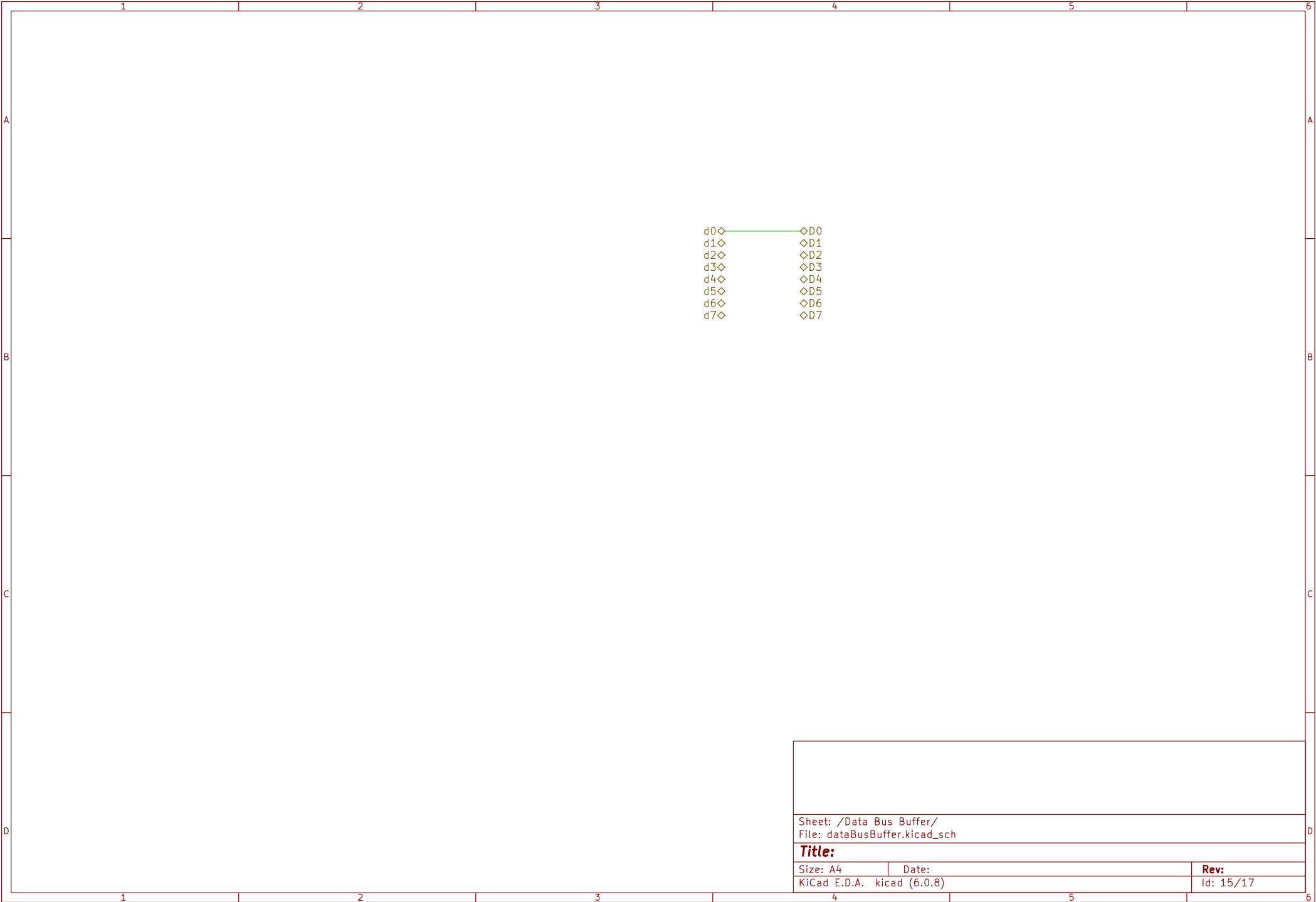
Id: 6/17

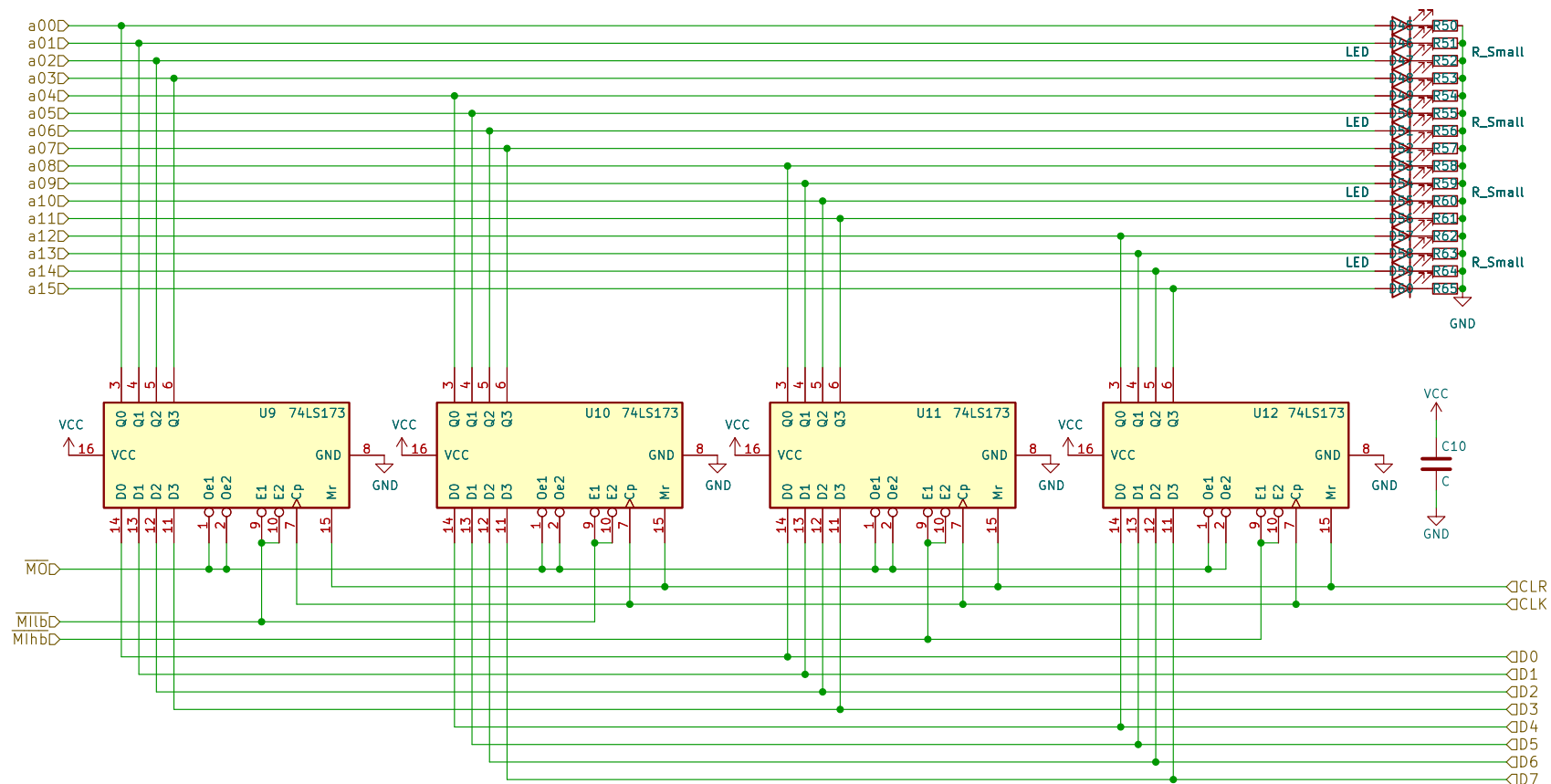


	1	2	3	4	5	6
A						
B						
C						
D						
	1	2	3	4	5	6

a00	◇	◇D0
a01	◇	◇D1
a02	◇	◇D2
a03	◇	◇D3
a04	◇	◇D4
a05	◇	◇D5
a06	◇	◇D6
a07	◇	◇D7
a08	◇	
a09	◇	
a10	◇	
a11	◇	
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a13	◇	
a14	◇	
a15	◇	

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Size: A4	Date:	Rev:
KiCad E.D.A. kicad (6.0.8)		Id: 14/17





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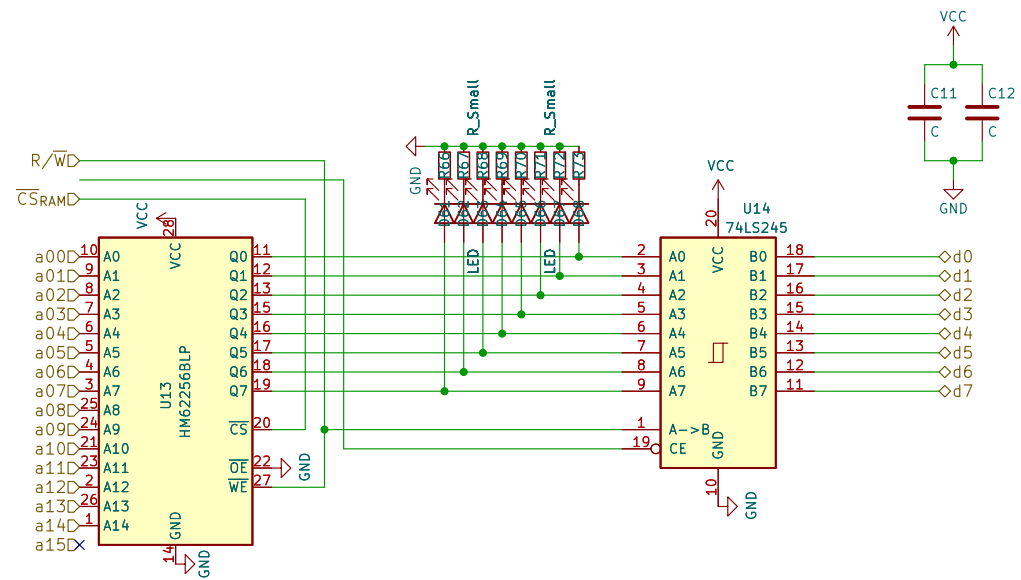
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Rev:

Id: 16/17







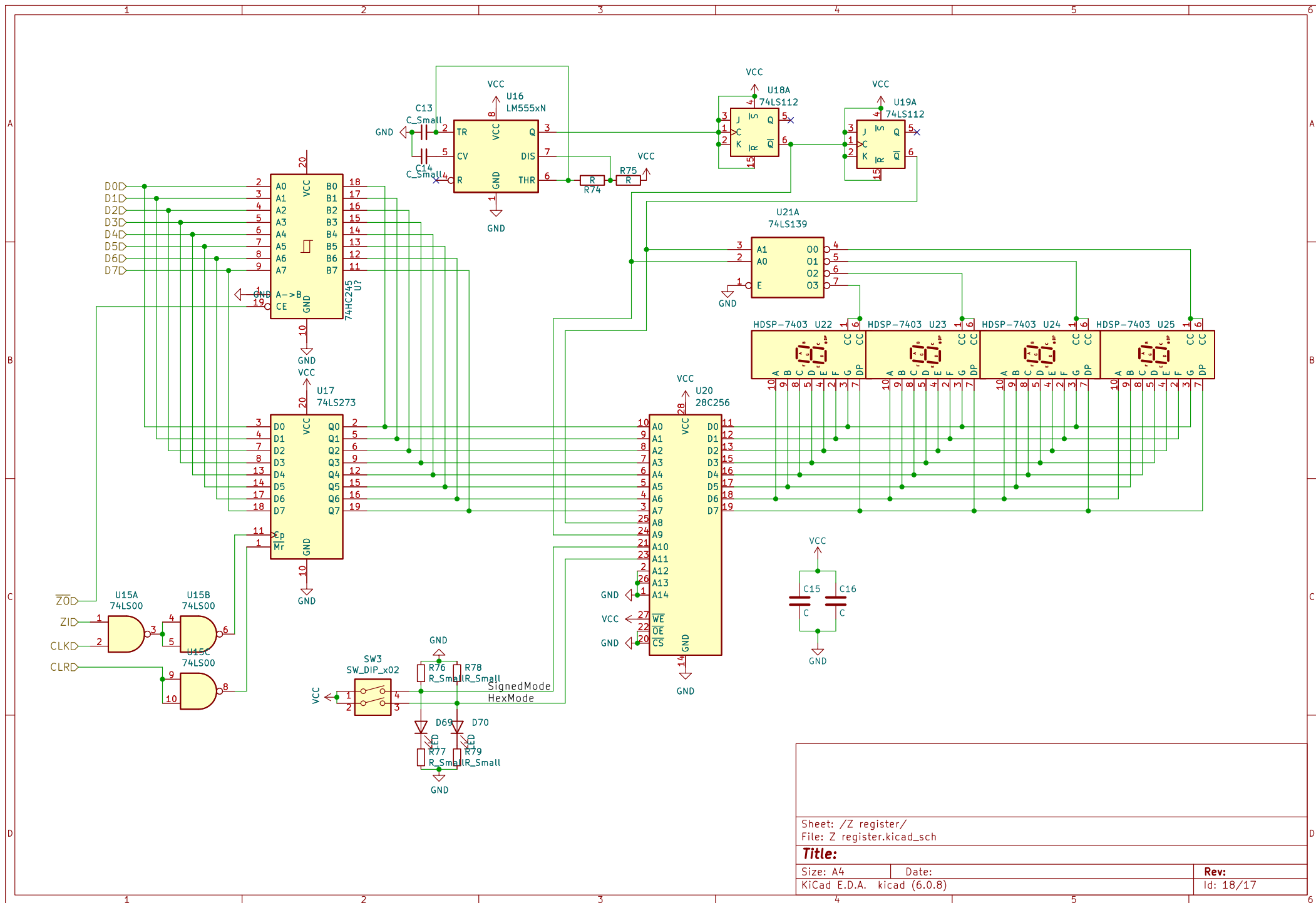
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Date:  
Id: 17/17

**Rev:**



Sheet: /Z register/  
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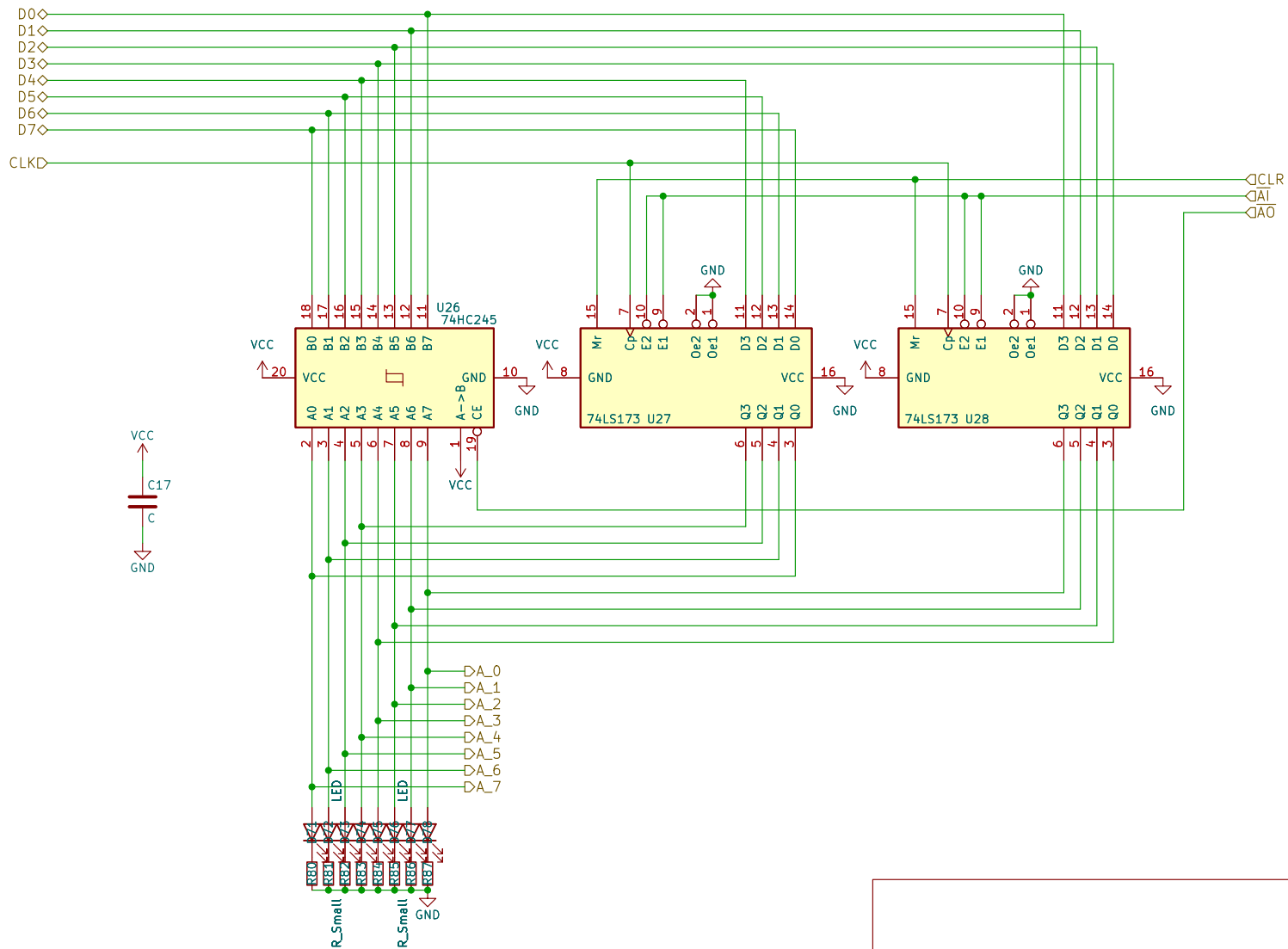
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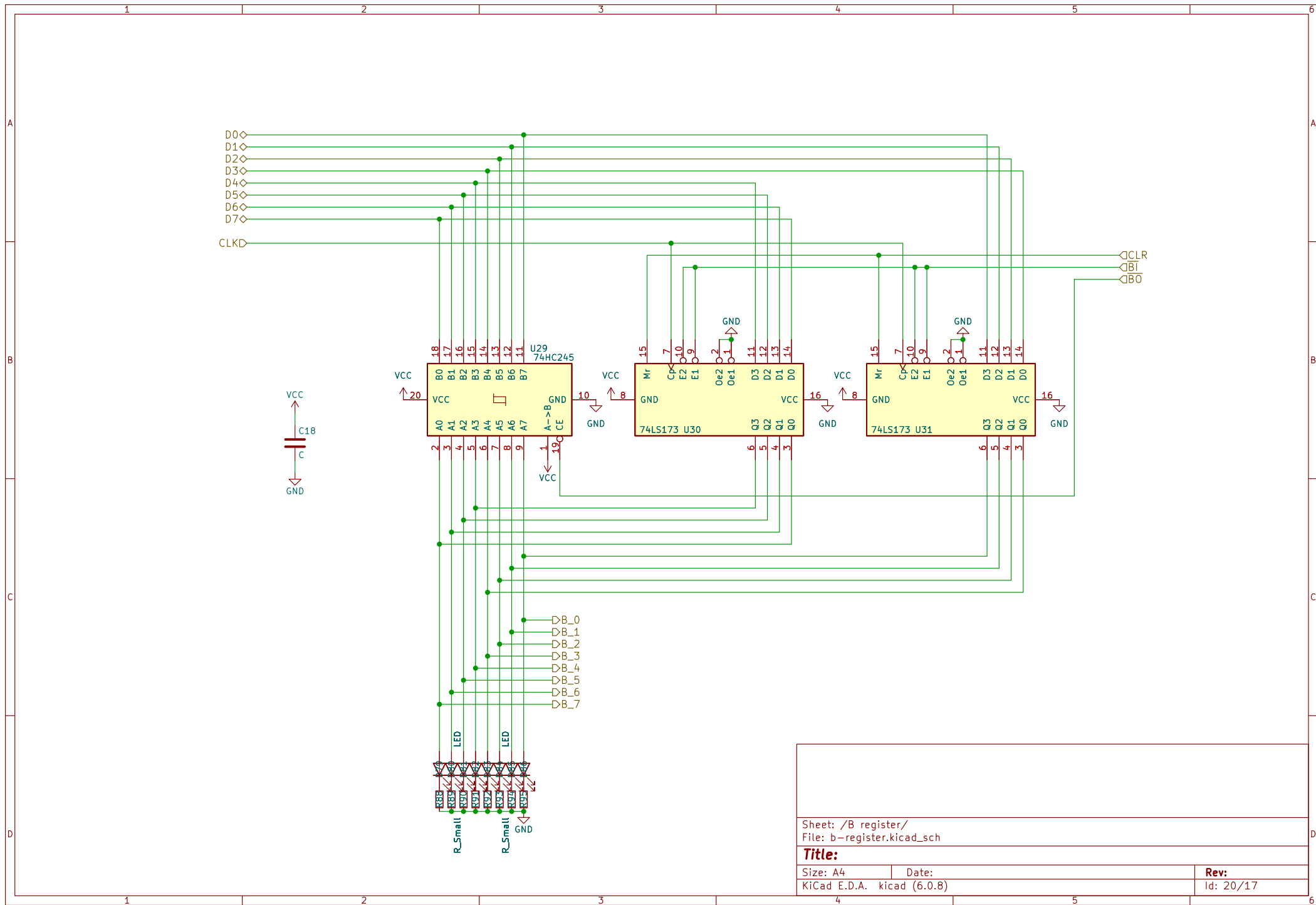
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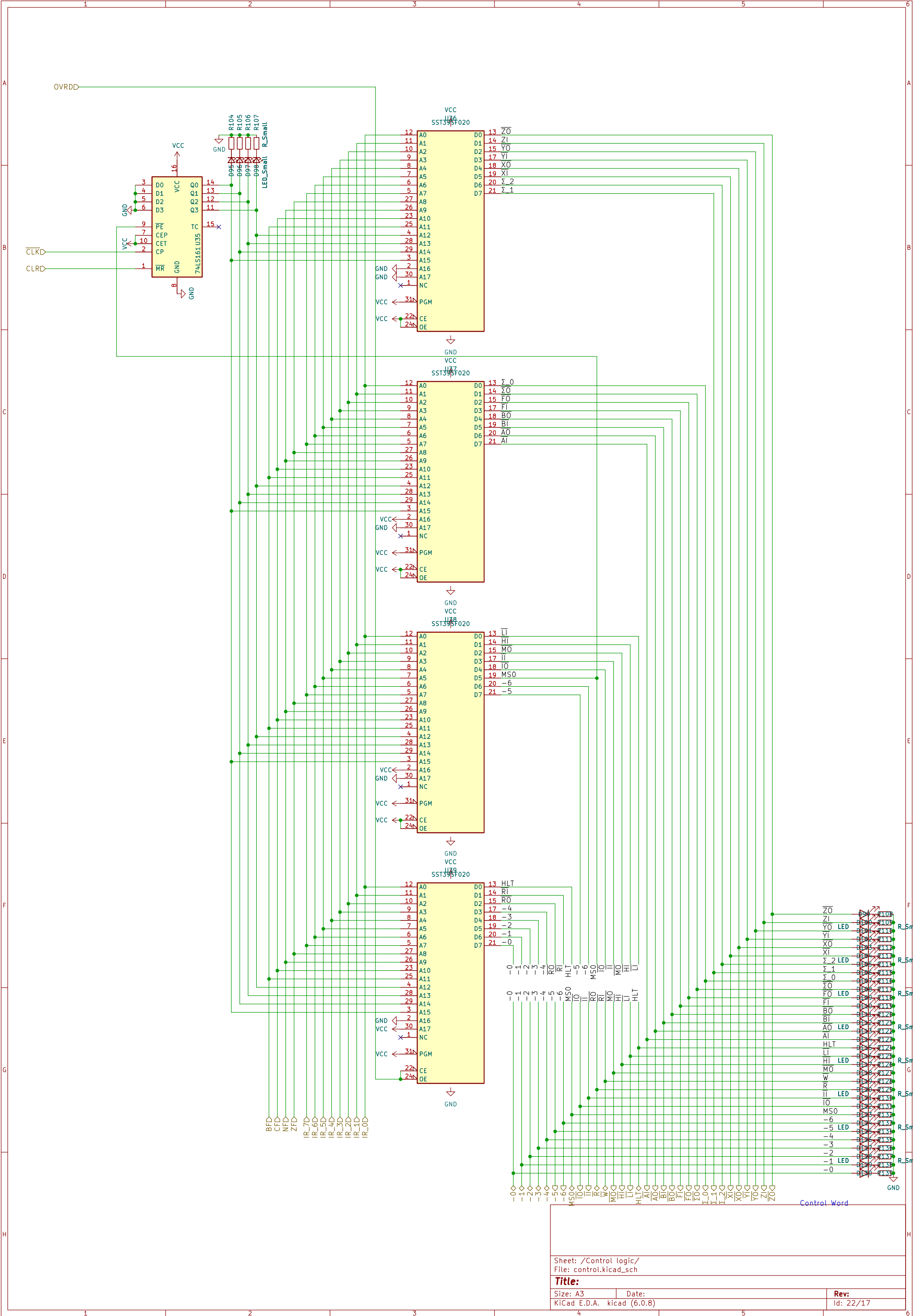
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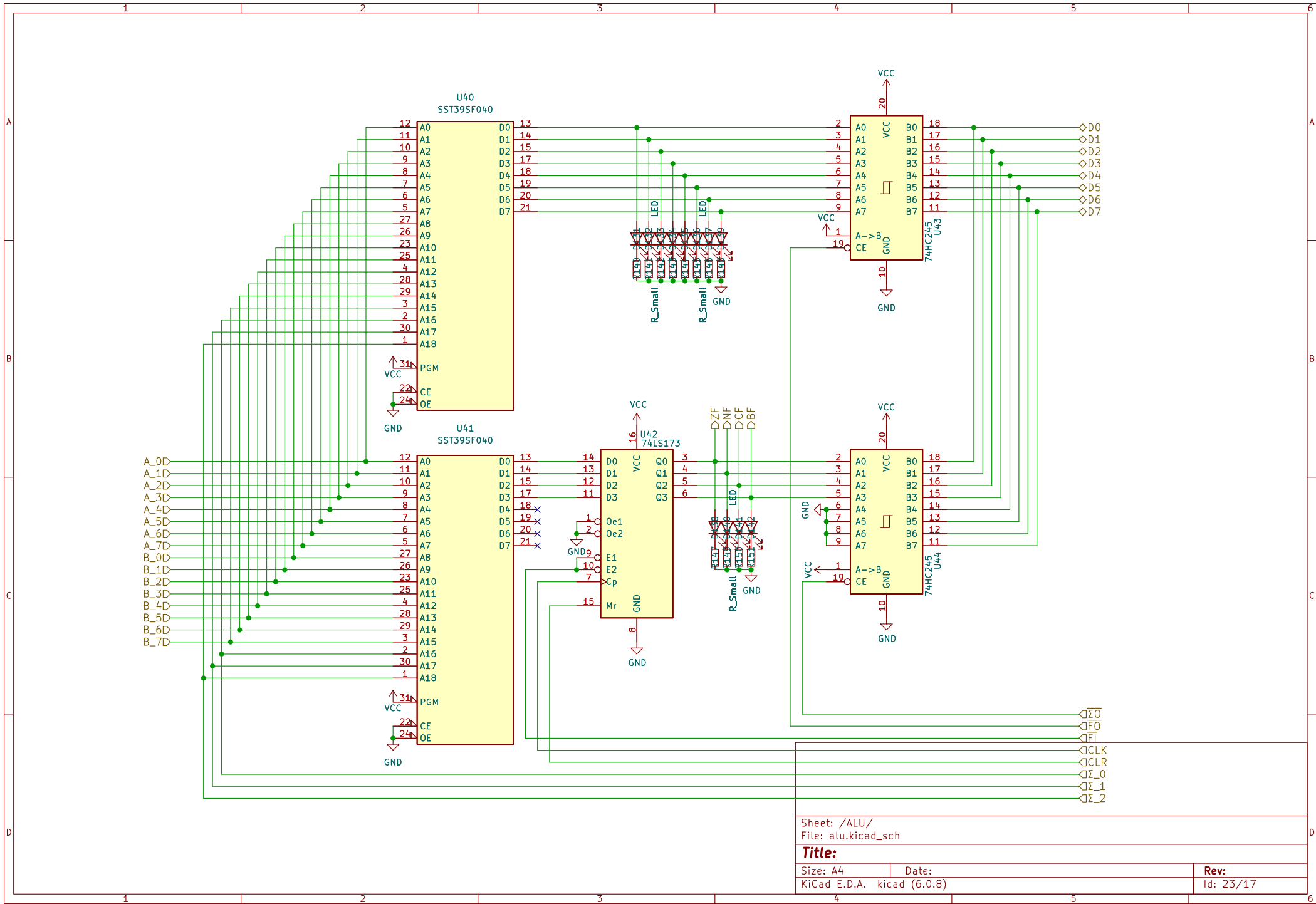
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Size: A4	Date:	Rev:
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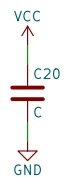
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**Rev:**

Id: 23/17



Rev:  
Id: 24/17



