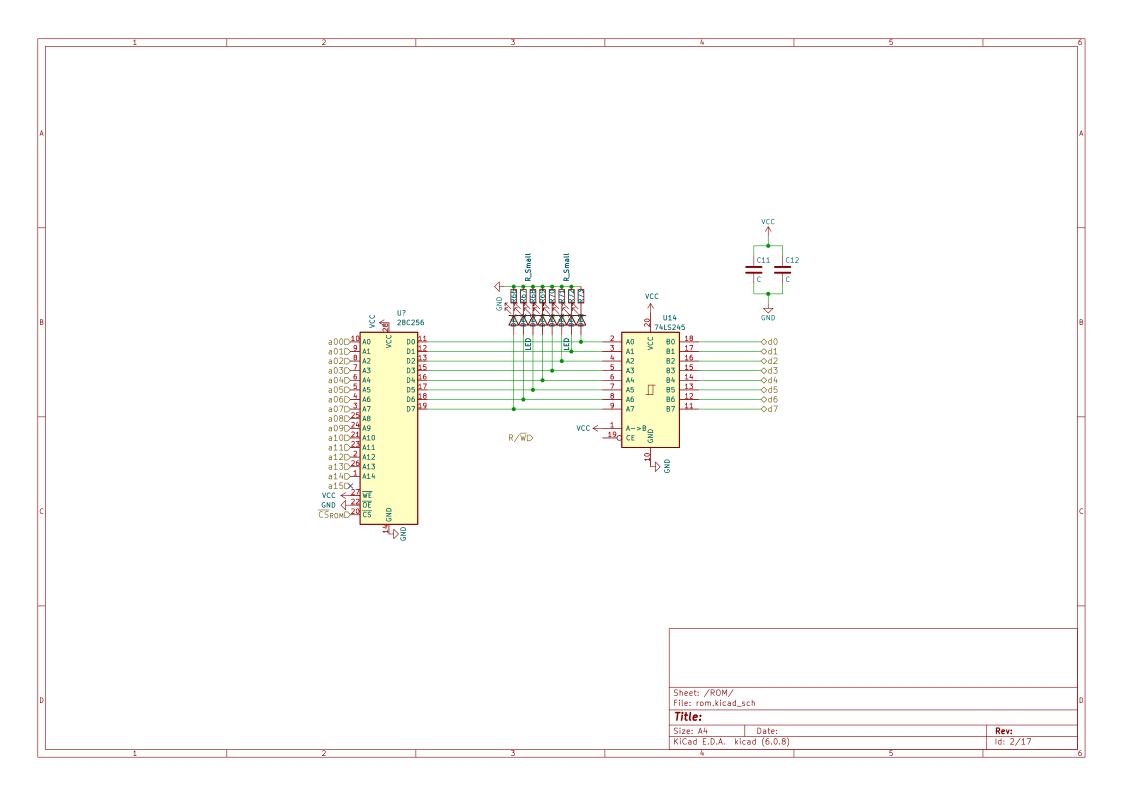
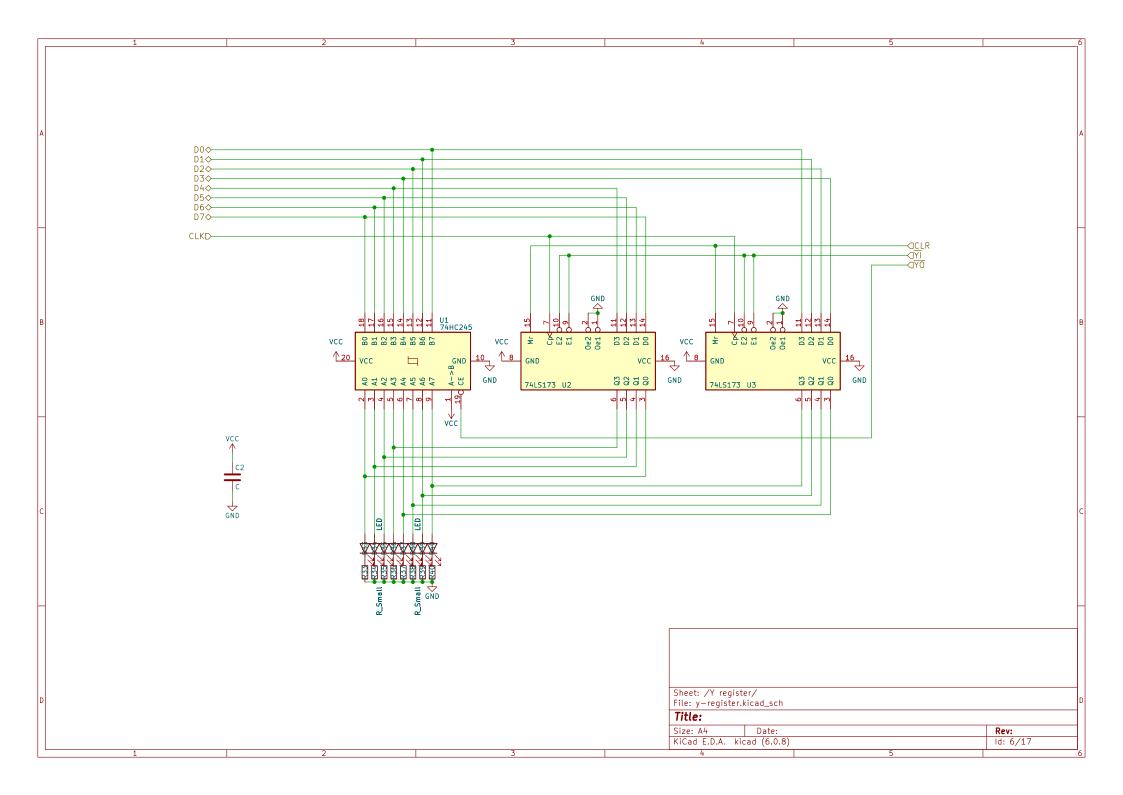
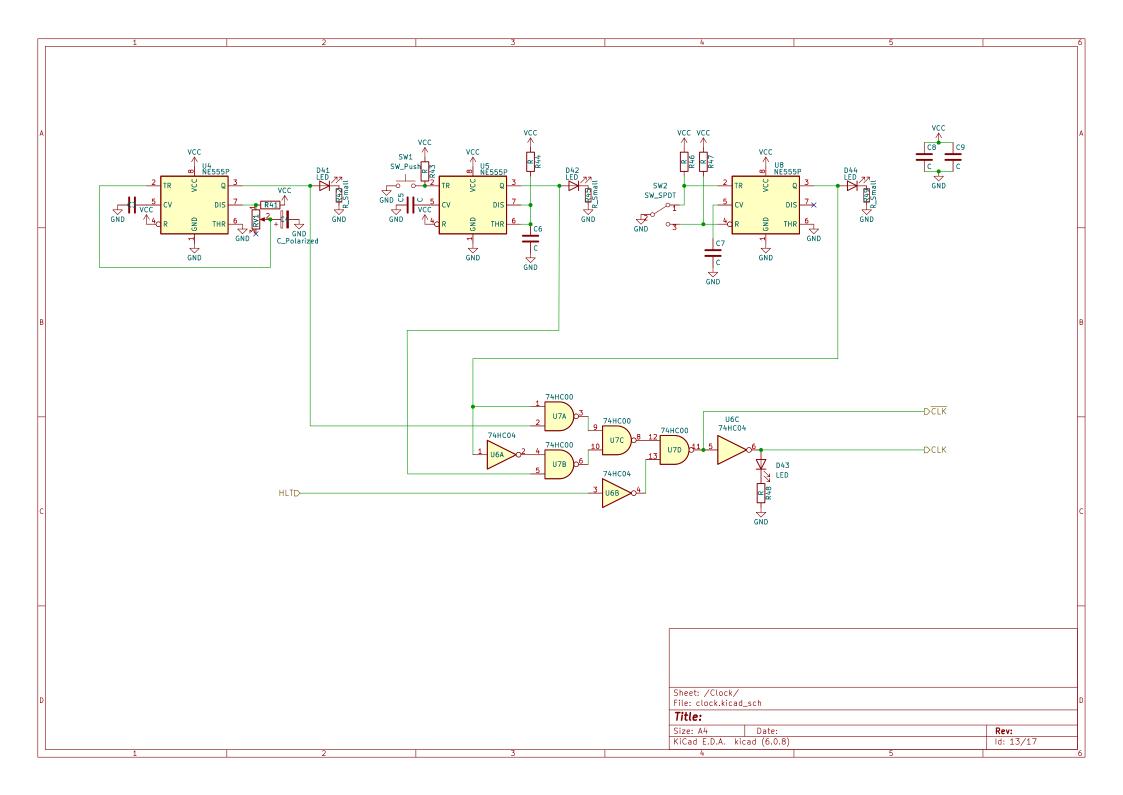
TODO: BOM $R/\overline{W} \triangleright$ Control Section Register Section Address Section Memory Section Instruction register Data Bus Buffer CLK< ⊳MO File: dataBusBuffer.kicad_sch >MIhb >MIlb A register File: inst-register.kicad_sch Control logic ><u>ĀĪ</u> >ĀŌ DCLK DCLR CS_{ROM}< File: mar.kicad_sch Program Counter File: rom.kicad_sch RAM CLK< File: a-register.kicad_sch B register ><u>BI</u> >B0 CLK< CS_{RAM}< File: programCounter.kicad_sch File: b-register.kicad_sch ALU File: ram.kicad_sch File: control.kicad_sch Clock CLK< acrk acrk HLT OVRDD HLT¢ File: clock.kicad_sch File: 10.kicad_sch AddrDecoder CSROMD CSRAMD File: alu.kicad_sch X register CS₂D CS₃D $\frac{\sqrt{x_1}}{\sqrt{x_0}}$ CS₄D CS₅D CS₆D CS₇D CLKC CLRC File: addrDecoder.kicad_sch File: x-register.kicad_sch Y register ><u>YI</u> ><u>YO</u> CLK< File: y-register.kicad_sch Z register ><u>ZI</u> >Z0 CLK< File: Z register.kicad_sch Sheet: / File: 8bit-computer.kicad_sch Title:
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