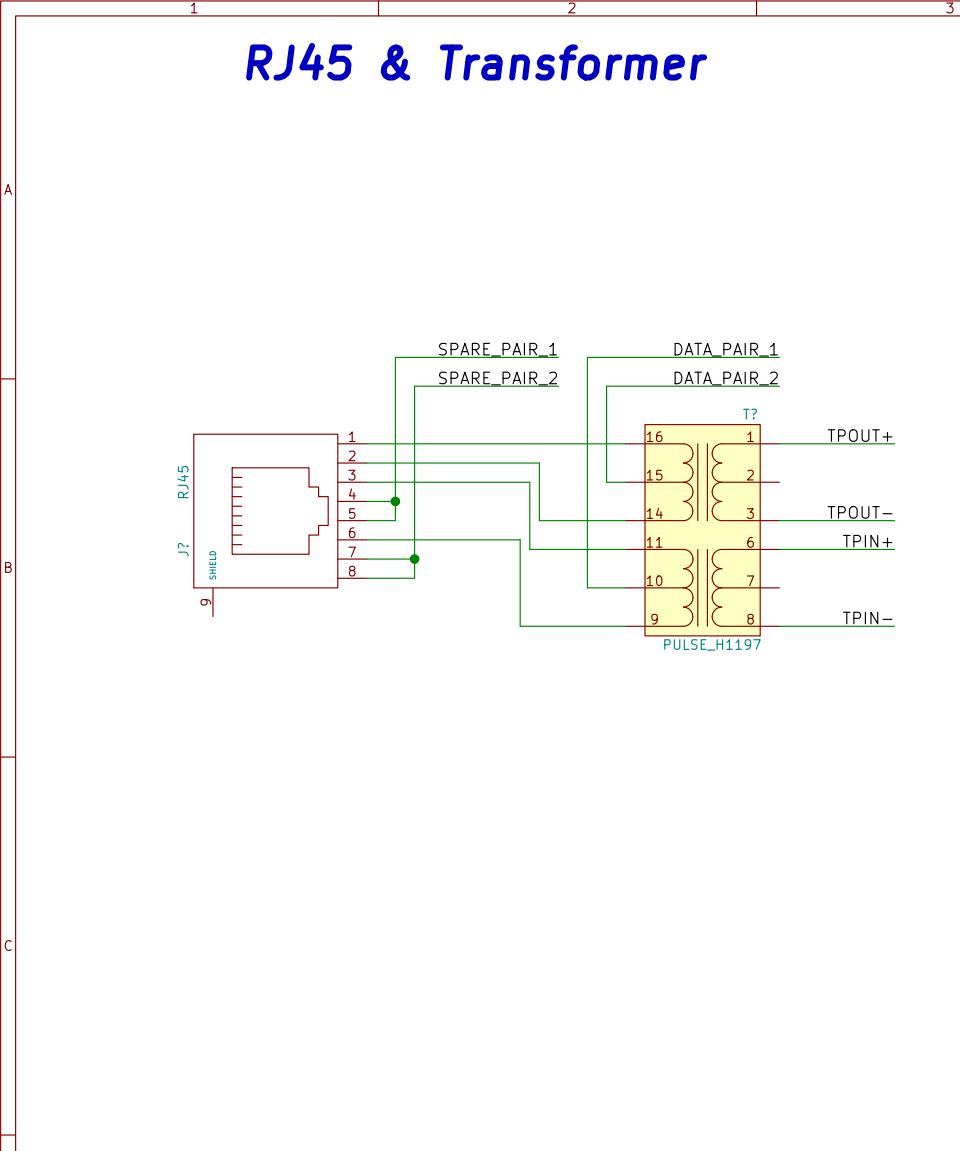
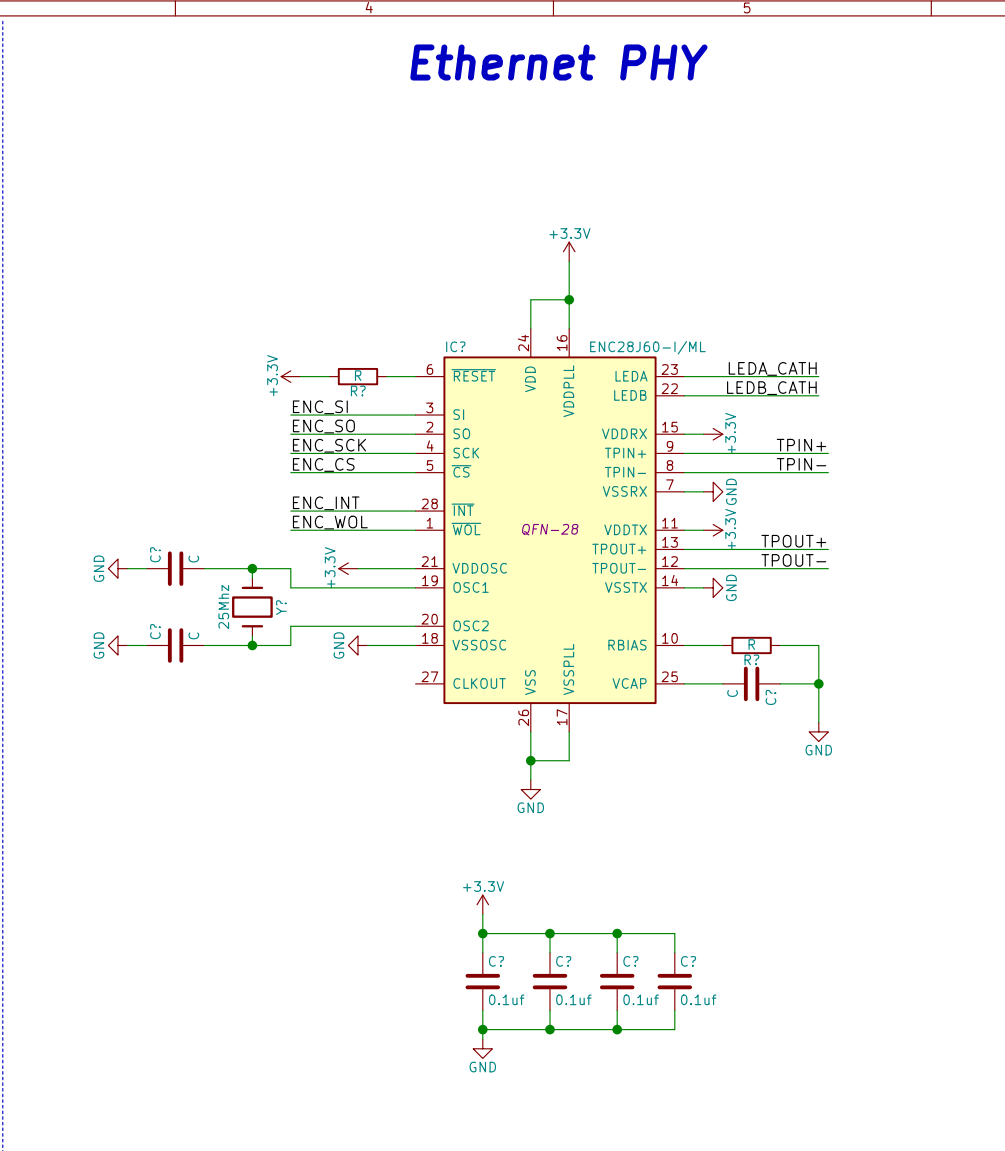
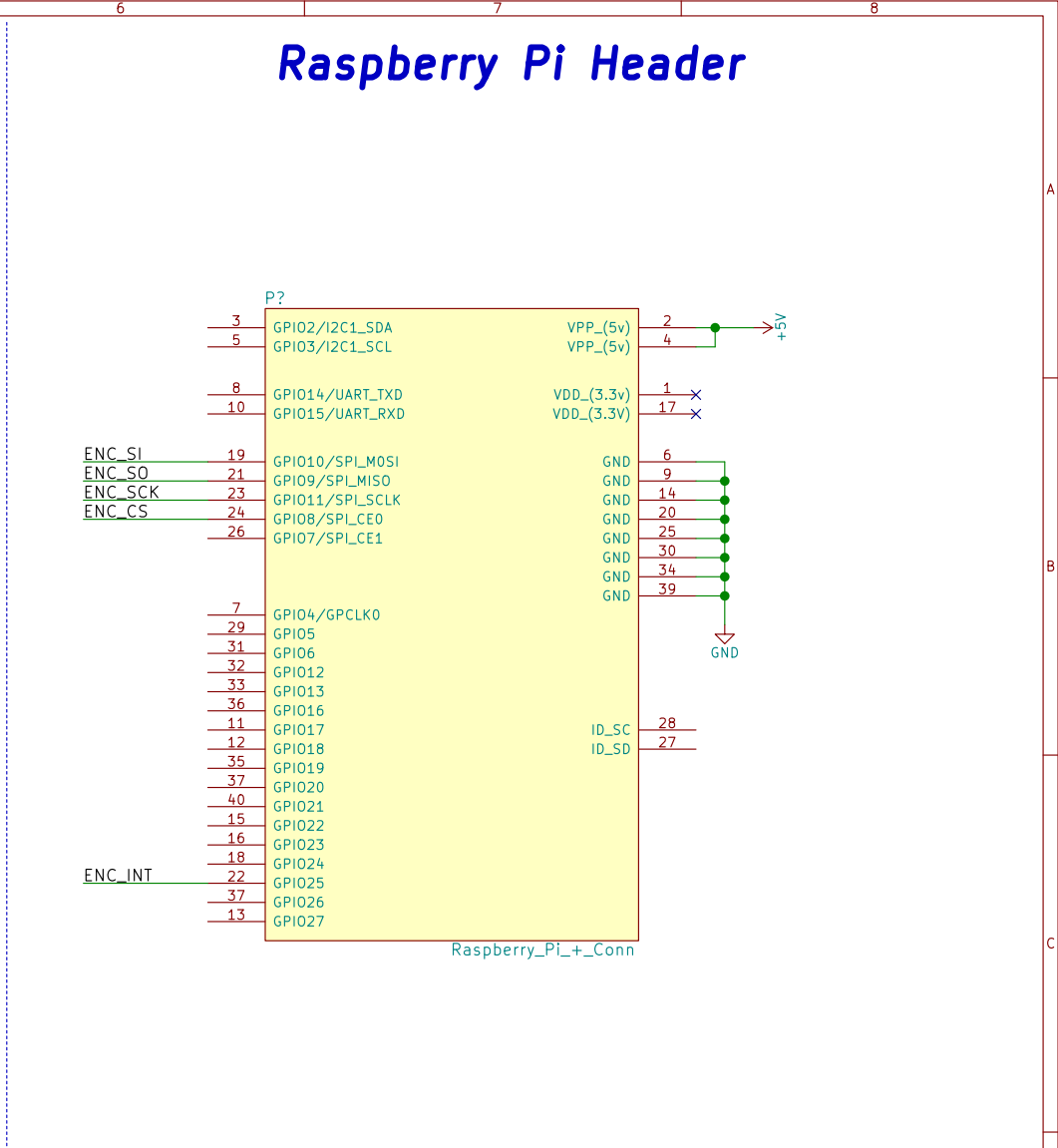


# RJ45 & Transformer

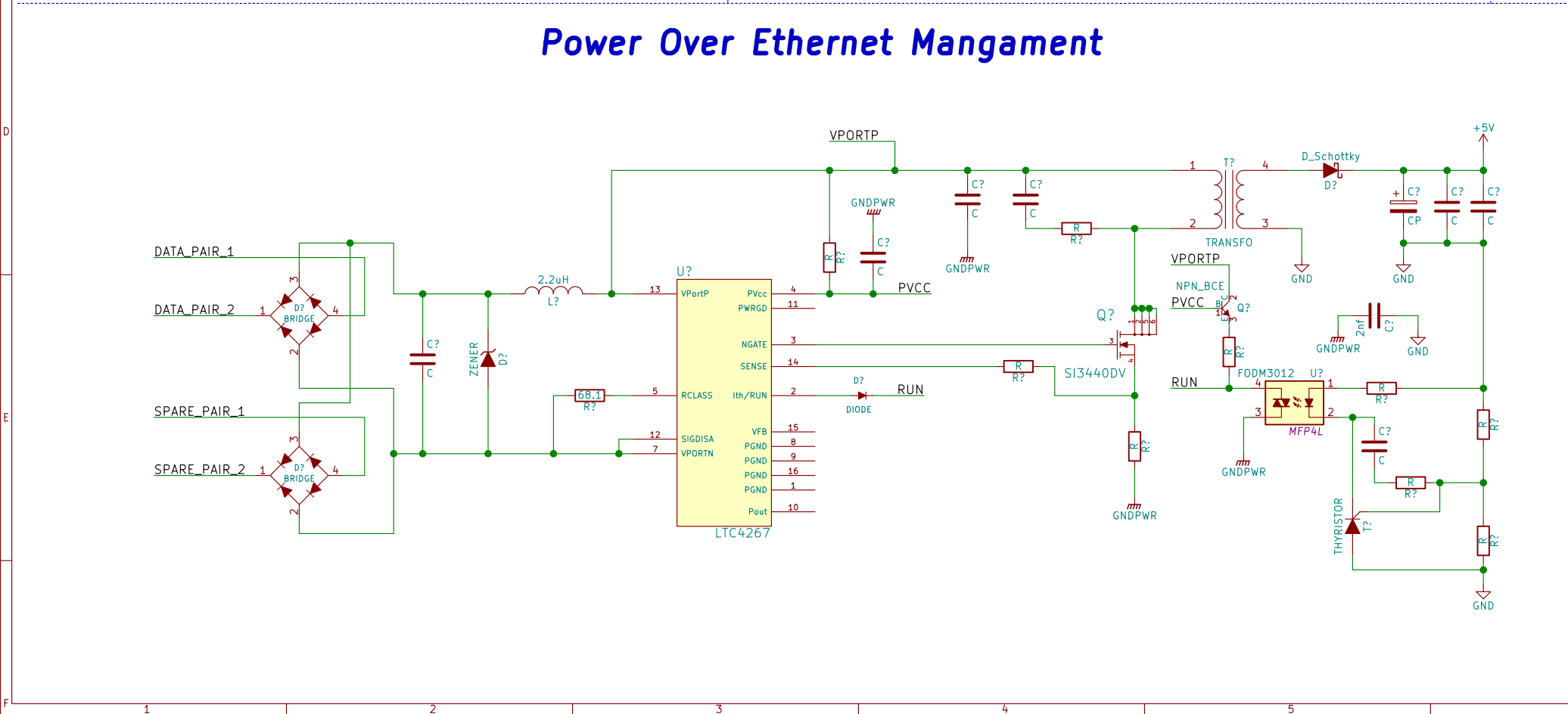


# Ethernet PHY

[illegible]

# Power Over Ethernet Mangament

The diagram illustrates a Power over Ethernet (PoE) management circuit. It features four differential input pairs: DATA\_PAIR\_1, DATA\_PAIR\_2, SPARE\_PAIR\_1, and SPARE\_PAIR\_2. Each pair is connected to a bridge circuit. The central component is the LTC4267 controller, which manages the power flow. The controller's outputs include VPORTP, GNDPWR, and PVCC. The VPORTP output is connected to a transformer (TRANSFO) and a Schottky diode (D\_Schottky). The GNDPWR output is connected to a transformer and a Schottky diode. The PVCC output is connected to a transformer and a Schottky diode. The circuit also includes a MOSFET (SI3440DV) and a thyristor (MFP4L) for power management. Various passive components like capacitors (C?), inductors (L?), resistors (R?), and a zener diode (ZENER) are used throughout the circuit.



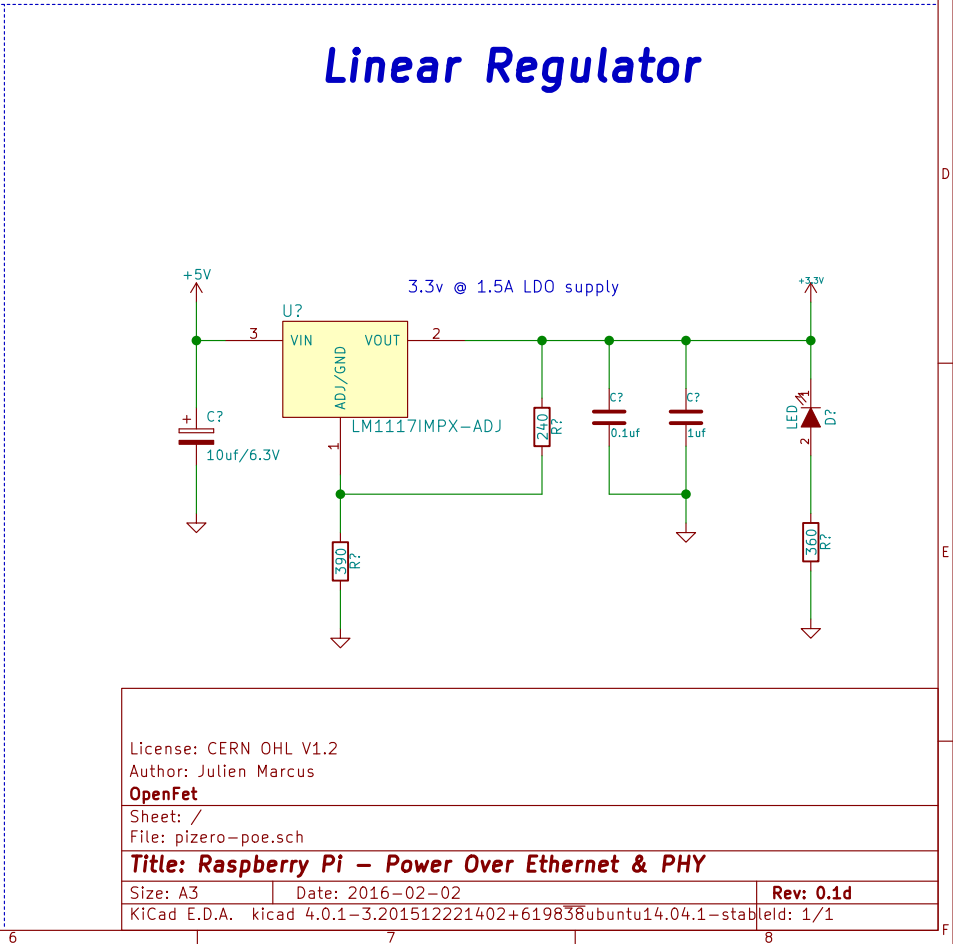
# Linear Regulator

The diagram shows a linear regulator circuit. A +5V input is connected to the VIN pin of an LM1117IMPX-ADJ regulator. The ADJ pin is connected to ground through a 39.0Ω resistor. The VOUT pin is connected to a 24.0Ω resistor, which is then connected to a 0.1μF capacitor to ground. A 1μF capacitor is connected to the output line after the 24.0Ω resistor. The output is labeled +3.3V. A 3.3V @ 1.5A LDO supply label is present. A 3.60Ω resistor is connected to the output line, and an LED is connected to ground through a 3.60Ω resistor. A 10μF/6.3V capacitor is connected to the +5V input line.

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**Title: Raspberry Pi – Power Over Ethernet & PHY**

Size: A3	Date: 2016-02-02	Rev: 0.1d
KiCad E.D.A. kicad 4.0.1-3.201512221402+619838ubuntu14.04.1-stableld: 1/1		



A number line from 0 to 10. The segment between 7 and 8 is highlighted in blue.