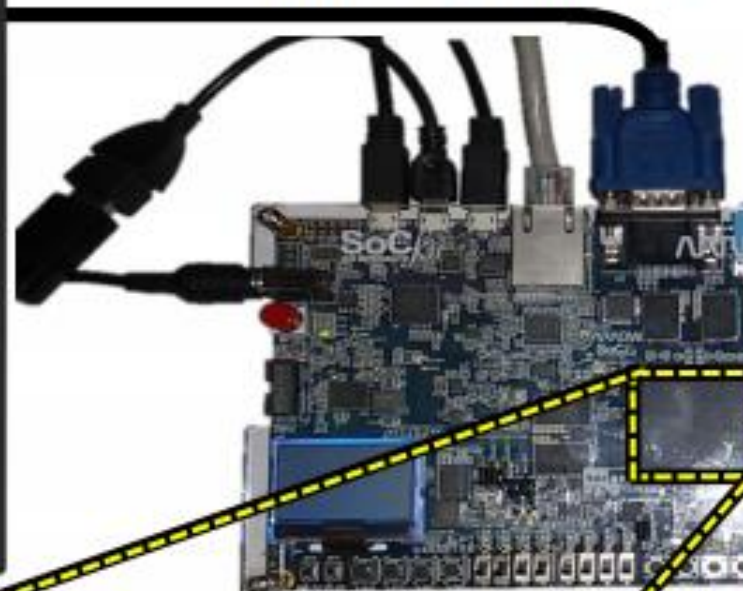


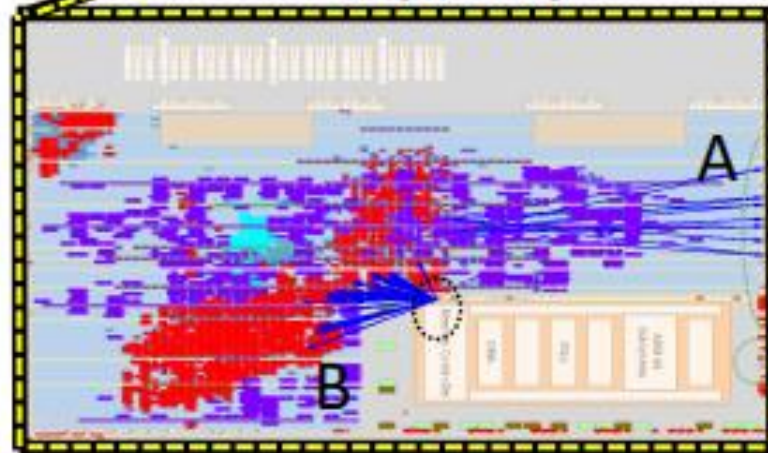
# FPGA Cyclone V Soc



Interface de usuário



FPGA + HPS



Chip Planner

Placa em teste



Gradientes

RX

TX

I/O

HSMT Connector

RF

Micro SD card