

# LM2903/LM29031,LM393/LM393A, LM293/LM293A

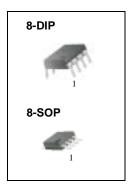
# **Dual Differential Comparator**

### **Features**

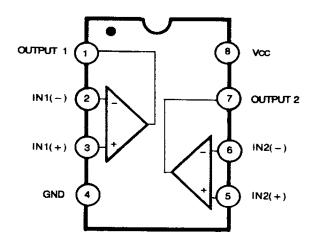
- Single Supply Operation: 2V to 36V
- Dual Supply Operation: ± 1V to ±18V
- · Allow Comparison of Voltages Near Ground Potential
- Low Current Drain 800µA Typ.
- · Compatible with all Forms of Logic
- Low Input Bias Current 25nA Typ.
- Low Input Offset Current ±5nA Typ.
- Low Offset Voltage ±1mV Typ.

# **Description**

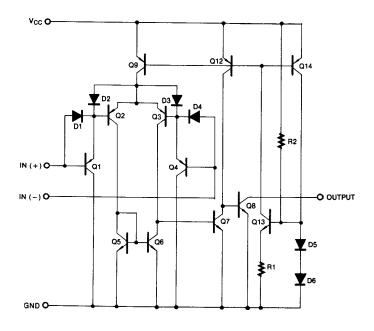
The LM2903/LM2903I, LM393/LM393A, LM293/ LM293A consist of two independent voltage comparators designed to operate from a single power supply over a wide voltage range.



## **Internal Block Diagram**



# **Schematic Diagram**



# **Absolute Maximum Ratings**

Parameter	Symbol	Value	Unit
Power Supply Voltage	Vcc	±18 or 36	V
Differential Input Voltage	VI(DIFF)	36	V
Input Voltage	Vı	- 0.3 to +36	V
Output Short Circuit to GND	-	Continuous	-
Power Dissipation, Ta = 25°C 8-DIP 8-SOP	PD	1040 480	mW
Operating Temperature LM393/LM393A LM2903 LM2903I LM293/LM293A	Topr	0 ~ +70 - 40 ~ +85 -40 ~ +105 -25 ~ +85	°C
Storage Temperature	TSTG	- 65 ~ +150	°C

# **Thermal Data**

Parameter	Symbol	Value	Unit
Thermal Resistance Junction-Ambient Max. 8-DIP 8-SOP	Rθja	120 260	°C/W

### **Electrical Characteristics**

(VCC = 5V, TA = 25°C, unless otherwise specified)

Parameter Symbo		Conditions		LM2	LM293A/LM393A			LM293/LM393			
Parameter	Symbol	Conditions		Min.	Тур.	Max.	Min.	Тур.	Max.	Unit	
Input Offset VIO		$VO(P) = 1.4V, RS = 0\Omega$		-	±1	±2	-	±1	±5	mV	
Voltage	VIO	V <sub>CM</sub> = 0 to1.5V	Note 1	-	-	±4.0	-	-	±9.0	IIIV	
Input Offset	nput Offset			-	±5	±50	-	±5	±50	nA	
Current	110		Note 1	-	-	±150	i	-	±150	IIA	
Input Bias	IBIAS			-	65	250	ı	65	250	nA	
Current	IBIAS		Note 1	-	-	400	i	-	400	ш	
Input Common Mode	-			0	-	VCC -1.5	0	-	VCC -1.5	V	
Voltage Range			Note 1	0	-	Vcc-2	0	-	Vcc-2		
Committee Comment		R <sub>L</sub> = ∞ , V <sub>CC</sub> = 5V		-	0.6	1	-	0.6	1	mA	
Supply Current	ICC	R <sub>L</sub> = ∞, V <sub>C</sub> C = 30V		-	0.8	2.5	-	0.8	2.5	IIIA	
Voltage Gain	Gv	VCC =15V, RL $\geq$ 15KΩ (for large VO(P-P)swing)		50	200	-	50	200	-	V/mV	
Large Signal Response Time	TLRES	$V_I$ =TTL Logic Swing $V_{REF}$ =1.4V, $V_{RL}$ = 5V, $R_L$ = 5.1K $\Omega$		-	350	-	-	350	-	nS	
Response Time	TRES	V <sub>RL</sub> =5V, R <sub>L</sub> =5.1KΩ		-	1.4	-	-	1.4	-	μS	
Output Sink Current	ISINK	$V_{I(-)} \ge 1V, \ V_{I(+)} = 0V, \ V_{O(P)} \le 1.5V$		6	18	-	6	18	-	mA	
Output		$VI(-) \ge 1V, VI(+)$	= 0V	-	160	400	-	160	400		
Saturation Voltage	VSAT	ISINK = 4mA	Note 1	-	-	700	-	-	700	mV	
Output		VI(-) = 0V,	V <sub>O</sub> (P) = 5V	-	0.1	-	-	0.1	-	nA	
Leakage Current	lo(LKG)	$V_{I(+)} = 1V$	VO(P) = 30V	-	-	1.0	-	-	1.0	μА	

### NOTE 1

$$\begin{split} LM393/LM393A: \ 0 &\leq T_A \leq +70^{\circ}C \\ LM2903: \ -40 &\leq T_A \leq +85^{\circ}C \\ LM2903I: \ -40 &\leq T_A \leq +105^{\circ}C \\ LM293/LM293A: \ -25 &\leq T_A \leq +85^{\circ}C \end{split}$$

### **Electrical Characteristics** (Continued)

(VCC = 5V, TA = 25°C, unless otherwise specified)

Davameter	Cymphol	Conditions		LM2903/LM2903I			l lesit	
Parameter	Symbol			Min.	Тур.	Max.	Unit	
Input Offcot Voltage	Vio	$VO(P) = 1.4V, RS = 0\Omega$		-	±1	±7	mV	
Input Offset Voltage		V <sub>CM</sub> = 0 to 1.5V	Note 1	-	±9	±15	1111	
Input Offcot Current	l. o			-	±5	±50	A	
Input Offset Current	lio	Note 1 - :		±50	±200	nA		
Input Bias Current	laa	<u>'</u>		-	65	250	n 1	
Input bias Current	IBIAS		Note 1	-	-	500	nA	
Input Common Mode	VI(R)			0	-	Vcc -1.5	V	
Voltage Range	,		Note 1	0	-	Vcc-2		
Supply Current	Icc	R <sub>L</sub> = ∞, V <sub>C</sub> C = 5V	R <sub>L</sub> = ∞, V <sub>C</sub> C = 5V		0.6	1	mA	
Зарріу Сапені	icc	R <sub>L</sub> = ∞, V <sub>C</sub> C = 30V		-	1	2.5	IIIA	
Voltage Gain	Gv	VCC =15V, RL≥15KΩ (for large VO(P-P)swing)		25	100	-	V/mV	
Large Signal Response Time	TLRES	$V_I$ =TTL Logic Swing $V_R$ EF =1.4 $V_T$ , $V_R$ L = 5 $V_T$ , $V_R$ L = 5.1 $V_T$		-	350	-	nS	
Response Time	T <sub>RES</sub>	$V_{RL} = 5V$ , $R_L = 5.1K\Omega$		-	1.5	-	μS	
Output Sink Current	ISINK	$V_{I(-)} \ge 1V, \ V_{I(+)} = 0V, \ V_{O(P)} \le 1.5V$		6	16	-	mA	
Output Saturation Voltage	VSAT	$V_{I(-)} \ge 1V, V_{I(+)} = 0V$		-	160	400	mV	
Output Saturation Voltage		ISINK = 4mA	Note 1	-	-	700	IIIV	
Output Leakage Current	IO(LKG)	VI(-) = 0V,	VO(P) = 5V	-	0.1	-	nA	
Output Leakage Outrett		V <sub>I(+)</sub> = 1V V <sub>O(P)</sub> = 30V		-	-	1.0	μΑ	

#### Note 1

LM393/LM393A:  $0 \le T_A \le +70^{\circ}C$ LM2903:  $-40 \le T_A \le +85^{\circ}C$ LM2903I:  $-40 \le T_A \le +105^{\circ}C$ LM293/LM293A:  $-25 \le T_A \le +85^{\circ}C$ 

# **Typical Performance Characteristics**

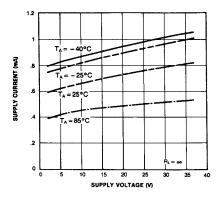
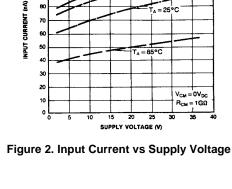


Figure 1. Supply Current vs Supply Voltage



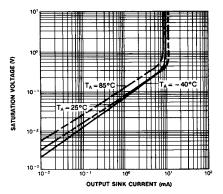


Figure 3. Output Saturation Voltage vs Sink Current

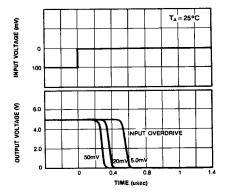


Figure 4. Response Time for Various Input **Overdrive-Negative Transition** 

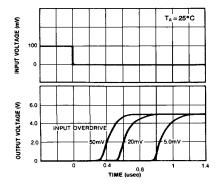
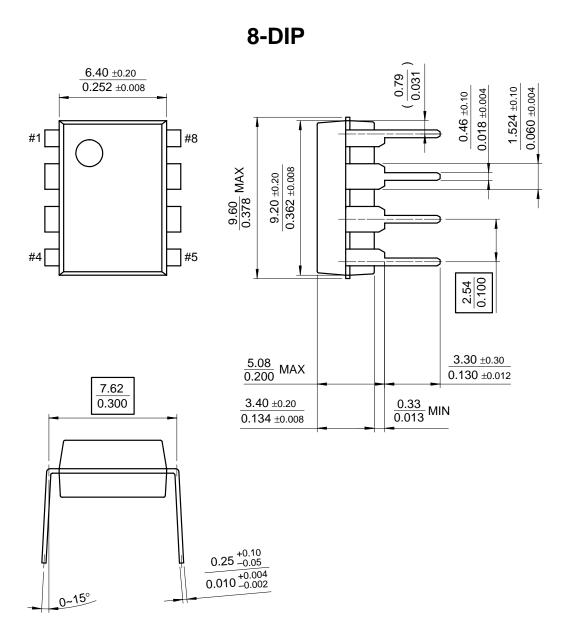


Figure 5. Response Time for Various Input Overdrive-Positive Transition

### **Mechanical Dimensions**

### **Package**

### **Dimensions in millimeters**

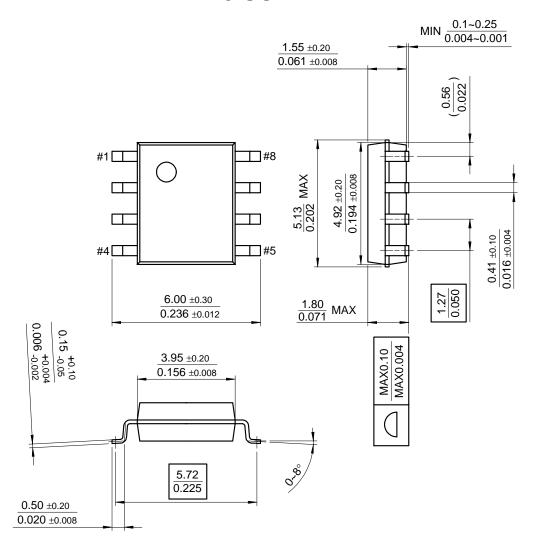


# **Mechanical Dimensions** (Continued)

### **Package**

### **Dimensions in millimeters**

# 8-SOP



### **Ordering Information**

Product Number	Package	Operating Temperature
LM393N	8-DIP	
LM393AN	0-DIF	0 ~ + 70°C
LM393M	8-SOP	0~+10 C
LM393AM	0-30F	
LM2903N	8-DIP	-40 ~ + 85°C
LM2903M	8-SOP	-40 ~ + 65 C
LM2903IN	8-DIP	-40 ~ + 105°C
LM2903IM	8-SOP	-40 ~ + 105 C
LM293N	8-DIP	
LM293AN	0-DIF	-25 ∼ + 85°C
LM293M	8-SOP	-23 ~ + 00 C
LM293AM	0-30F	

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