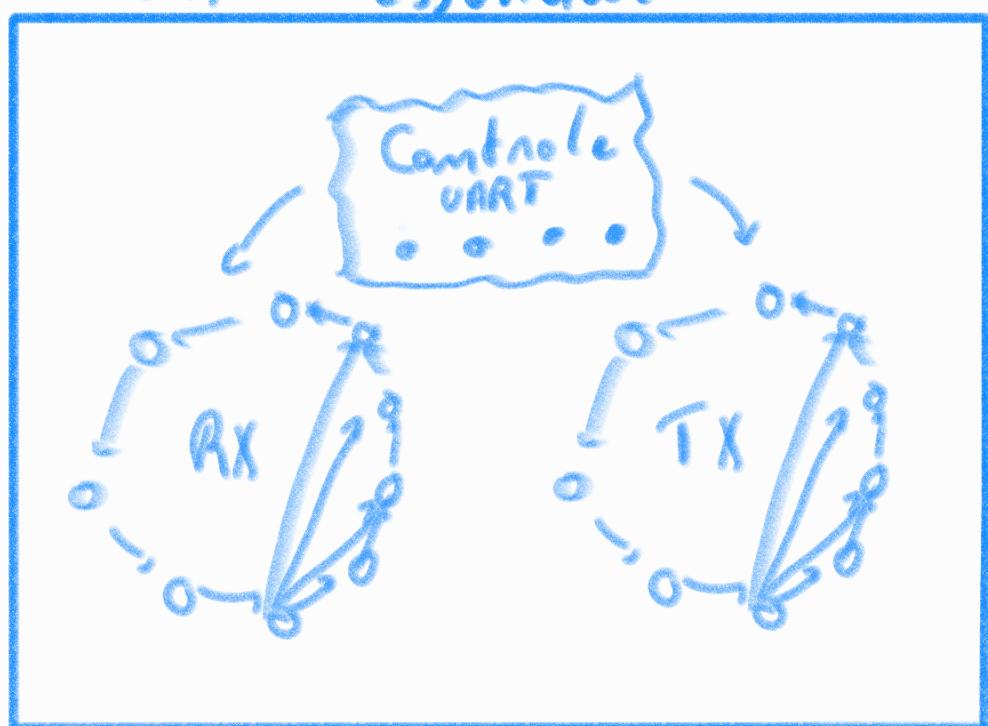


FPGA

A ideia é fazer um controlador serial com a FPGA.

UART - Essencial

MEF



Controlador Jogo

0 0 0 0 ← estados do jogo

1 ⇒ inativa (seq. de lcd)

2 ⇒ modo palpite

3 ⇒ win
4 ⇒ lose (seq. de lcd)

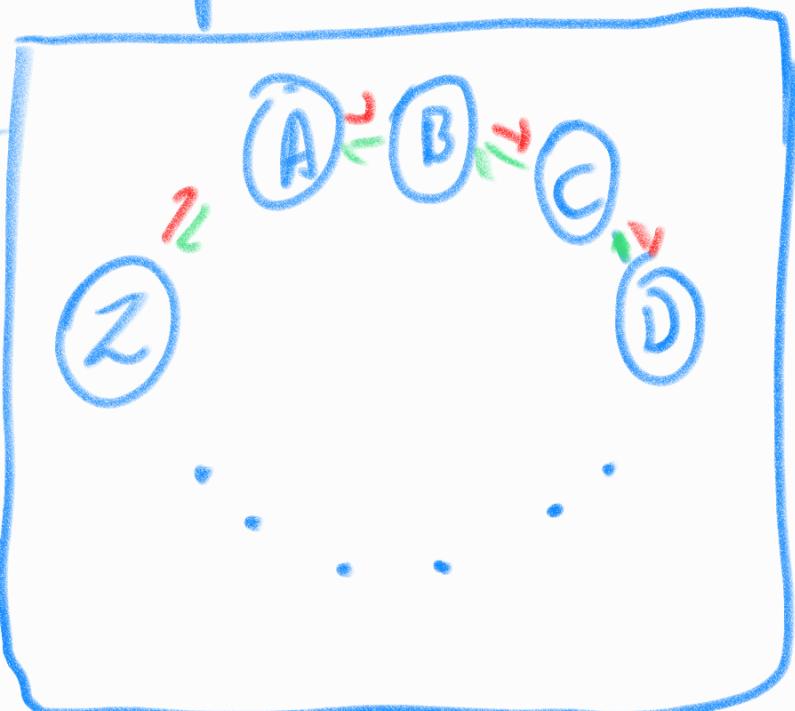
5 \Rightarrow ponto (exibição)
6 \Rightarrow Histone

2. \oplus recebe # videos \Rightarrow PinPon \Rightarrow LED

\hookrightarrow entende \Rightarrow cumulative - mult

00 \rightarrow 0001 # video fio de cada
01 \rightarrow 0011
10 \rightarrow 0101
11 \rightarrow 1111
no 0.

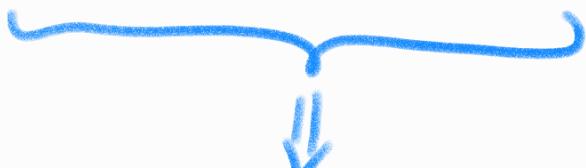
Loop letres :



• retorna

• avanca

- t \Rightarrow manda a letra
- l \Rightarrow manda o comando select



UART

① Sintno



(J)



(J)

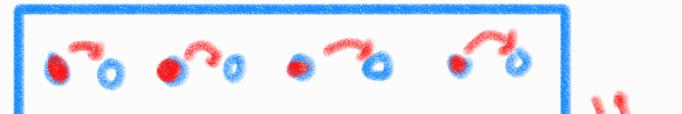


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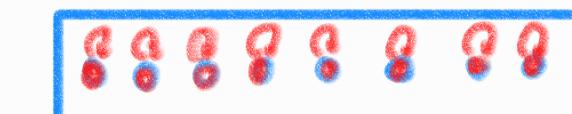
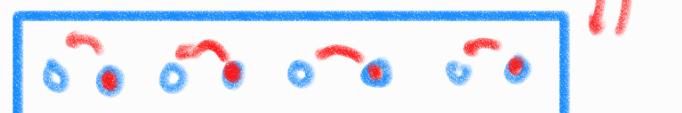


③ Wim

④ Lase



))

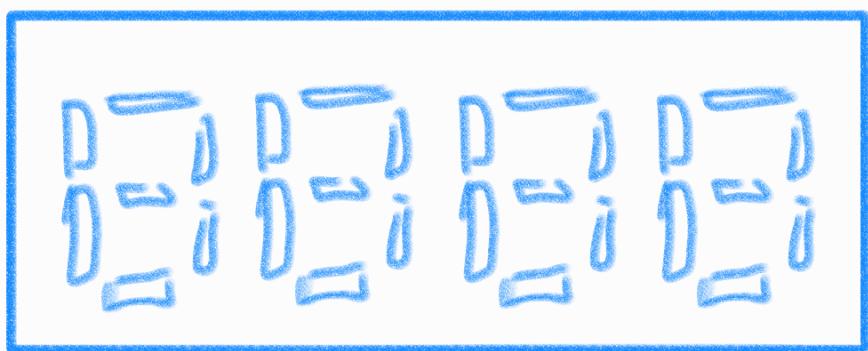


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516

Exibição



+ Som?

I D E I A



Datas