Michael Burton

Jocelyn Frechette

Jesse Haynes-Lewis

11/12/2023

**Phase 4 Individual Contributions**

**Jocelyn Frechette Phase 4 Contributions:**

* Initial Planning and Design
* Timer Implementation
* Introduction of Loss in the UDP channel
* Documentation
* Debugging and Testing of Phase 4 Code

**Michael Burton Phase 4 Contributions:**

* Initial Planning/Design
* Timer Implementation
* State Machine
* Debugging and Testing of Phase 4 Code
* Documentation

**Jesse Haynes-Lewis Phase 4 Contributions:**

* Documentation
* Performance Plots
* Initial Planning/Design