TIMING CONTROL

READ/WRITE OPERATIONS

READING DATA FROM LCM TO MPU

ITEM	SYMBOL	LIMIT (MIN)	LIMIT (MAX)	UNIT
ENABLE CYCLE TIME	T_{CYCE}	666	–	NS
ENABLE PULSE WIDTH (HIGH LEVEL)	PW_{EH}	300	–	NS
ENABLE RISE/FALL TIME	$T_{ER}T_{EF}$	–	25	NS
ADDREE SET-UP TIME TDSW (RS, R/W,E)	T_{AS}	100	–	NS
ADDRESS HOLD TIME	T_{AH}	10	–	NS
DATA SET-UP TIME	T_{DDR}	–	190	NS
DATA HOLD TIME	T_{DHR}	20	–	NS

WRITING DATA FROM MPU TO LCM

SYMBOL	LIMIT (MIN)	LIMIT (MAX)	UNIT
T_{CYCE}	666	–	NS
PW_{EH}	300	–	NS
$T_{ER}T_{EF}$	–	25	NS
T_{AS}	100	–	NS
T_{AH}	10	–	NS
T_{DSW}	100	–	NS
T_{H}	10	–	NS

READ OPERATIONS

NOTE: *VOL1 IS ASSUMED TO BE 0.8V AT 2 MHZ OPERATION
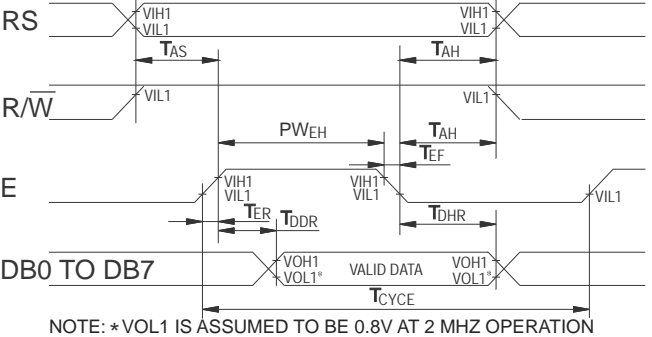
WRITE OPERATIONS

BUSY FLAG CHECK

8-BIT BUSY FLAG CHECK TIMING

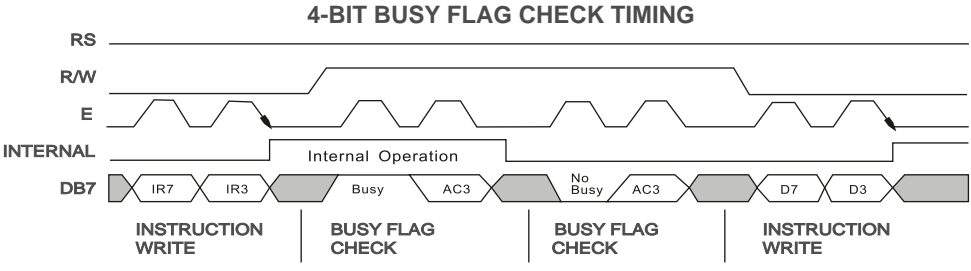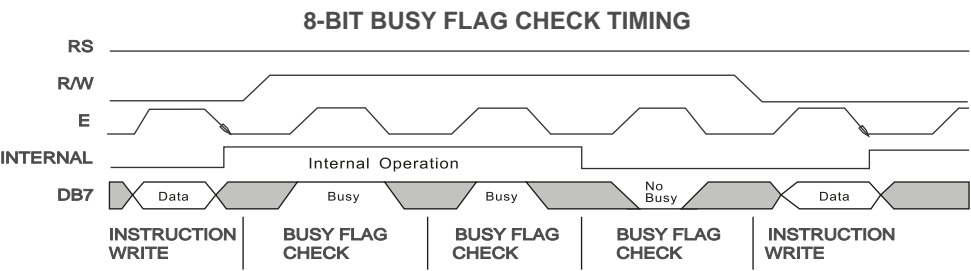### 4-BIT BUSY FLAG CHECK TIMING
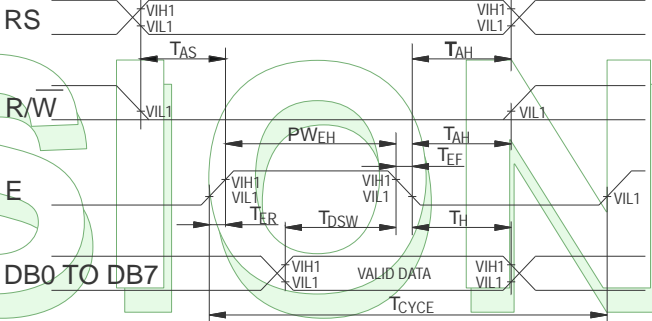
(NOTE)IR7, IR3: INSTRUCTION 7TH BIT, 3RD BIT; AC3: ADDRESS COUNTER 3RD BIT

THIS DATA SHEET IS FOR REFERENCE ONLY. WE RESERVE THE RIGHT TO CHANGES AND IMPROVMENTS WITHOUT PRIOR NOTICE.