TOSHIBA Photocoupler GaAs Ired & Photo-Transistor

TLP504A,TLP504A-2

Programmable Controllers AC / DC-Input Module Solid State Relay

The TOSHIBA TLP504A and TLP504A-2 consists of a photo-transistor optically coupled to a gallium arsenide infrared emitting diode.

The TLP504A offers two isolated channels in an eight lead plastic DIP package, while the TLP504A-2 provides four isolated channels in a sixteen plastic DIP package.

• Collector-emitter voltage: 55 V (min.)

• Current transfer ratio: 50% (min.)

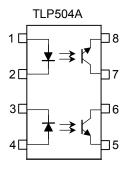
Rank GB: 100% (min.)

• Isolation voltage: 2500 Vrms (min.)

• UL recognized: UL1577,

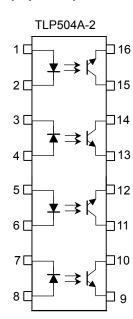
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Pin Configurations (top view)

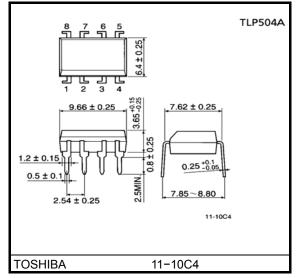


1, 4 : Anode 2, 3 : Cathode

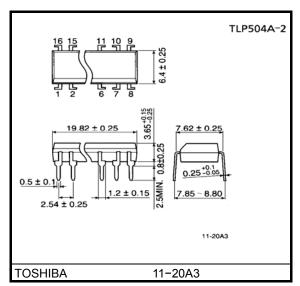
5, 8 : Emitter 6, 7 : Collector



1, 4, 5, 8 : Anode 2, 3, 6, 7 : Cathode 9, 12, 13, 16 : Emitter 10, 11, 14, 15 : Collector Unit in mm



Weight: 0.54 g (typ.)



Weight: 1.1 g (typ.)



Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Cumbal	Rat	Unit	
		Symbol	TLP504A	TLP504A-2	Unit
	Forward current	lF	60	50	mA
LED	Forward current derating	ΔI _F / °C	–0.7 (Ta ≥ 39°C)	–0.5 (Ta ≥ 25°C)	mA /°C
	Pulse forward current	I _{FP}	1 (100µs pulse, 100pps)		Α
	Reverse voltage	V _R	5		V
	Junction temperature	Tj	125		°C
	Collector-emitter voltage	V _{CEO}	5	55	
	Emitter-collector voltage	V _{ECO}	7	7	V
'n	Collector current	I _C	5	50	
Detector	Collector power dissipation (1 circuit)	PC	150	100	mW
	Collector power dissipation derating (1 circuit Ta ≥ 25°C)	ΔP _C / °C	-1.5	-1.0	mW /°C
	Junction temperature	Tj	12	125	
Sto	rage temperature range	T _{stg}	-55~150		°C
Оре	erating temperature range	T _{opr}	−55~100		°C
Lead soldering temperature		T _{sol}	260 (10 s)		°C
Total package power dissipation		R _T	250	250 150	
Total package power dissipation derating (Ta ≥ 25°C)		ΔP _T / °C	-2.5	-1.5	mW / °C
Isol	ation voltage	BVS	2500 (AC, 1min., R.H.≤ 60%) (Note 1)		Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Device considered a two terminal device: LED side pins shorted together and detector side pins shorted together.

Recommended Operating Conditions

Characteristics	Symbol	Min.	Тур.	Max.	Unit
Supply voltage	V _{CC}	_	5	24	V
Forward current	lF	_	16	20	mA
Collector current	IC	_	1	10	mA
Operating temperature	T _{opr}	-25	_	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.



Individual Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
ED	Forward voltage	V_{F}	I _F = 10 mA	1.0	1.15	1.3	V
	Reverse current	I _R	V _R = 5 V	_	_	10	μΑ
	Capacitance	C _T	V = 0, f = 1 MHz	_	30	_	pF
Detector	Collector–emitter breakdown voltage	V _(BR) CEO	I _C = 0.5 mA	55	_	-	V
	Emitter-collector breakdown voltage	V _(BR) ECO	I _E = 0.1 mA	7	_	_	V
	Collector dark current	lono	V _{CE} = 24 V	_	10	100	nA
	Collector dark current	ICEO	V _{CE} = 24 V, Ta = 85°C		2	50	μA
	Capacitance collector to emitter	C _{CE}	V = 0, f = 1 MHz	_	10	_	pF

Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Current transfer ratio	I _C / I _F	I _F = 5 mA, V _{CE} = 5 V Rank GB	50	_	600	- %
			100	_	600	
Saturated CTR	I _C / I _{F (sat)}	IF = 1 mA, V _{CE} = 0.4 V Rank GB	_	60	_	- %
Saluraled CTR			30	_	_	
Collector–emitter saturation voltage	VCE (sat)	I _C = 2.4 mA, I _F = 8 mA	_	_	0.4	
		I _C = 0.2 mA, I _F = 1 mA Rank GB	_	0.2	_	V
			_	_	0.4	

Isolation Characteristics (Ta = 25°C)

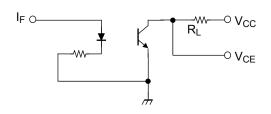
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Capacitance input to output	CS	V _S = 0, f = 1 MHz	_	0.8	_	pF
Isolation resistance	R _S	V _S = 500 V	5×10 ¹⁰	10 ¹⁴	_	Ω
		AC, 1 minute	2500	_	_	Vrms
Isolation voltage		AC, 1 second, in oil	_	5000	_	
		DC, 1 minute, in oil	_	5000	_	Vdc

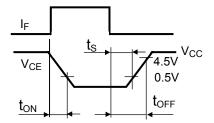
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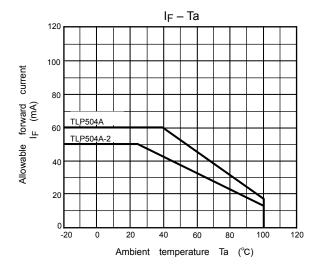
Switching Characteristics (Ta = 25°C)

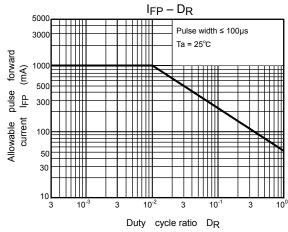
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Rise time	t _r		_	2	_	- µs
Fall time	t _f	V_{CC} = 10 V, I_{C} = 2 mA	_	3	_	
Turn-on time	t _{on}	$R_L = 100\Omega$	_	3	_	
Turn-off time	t _{off}		_	3	_	
Turn-on time	ton		_	2	_	
Storage time	ts	$R_L = 1.9 \text{ k}\Omega$ (Fig.1) $V_{CC} = 5 \text{ V}, I_F = 16 \text{ mA}$	_	15	_	μs
Turn-off time	t _{OFF}		_	25	_	

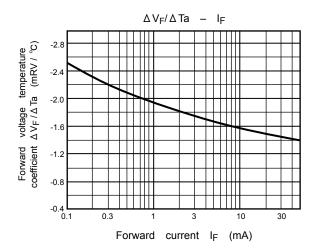
Fig. 1 Switching time test circuit

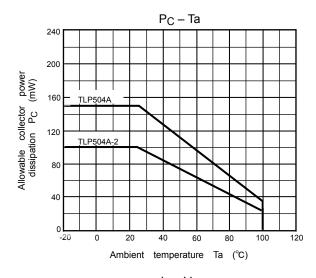


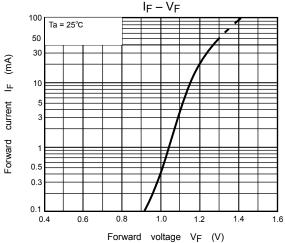


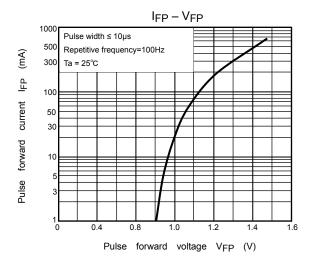


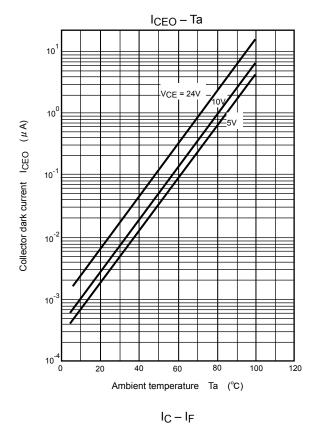


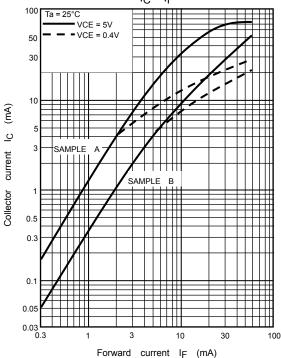


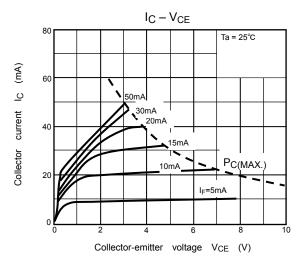


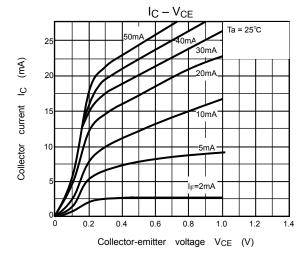


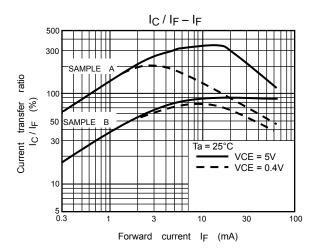


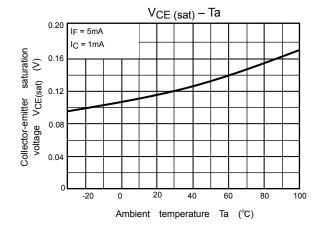


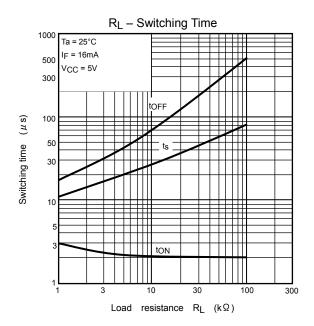


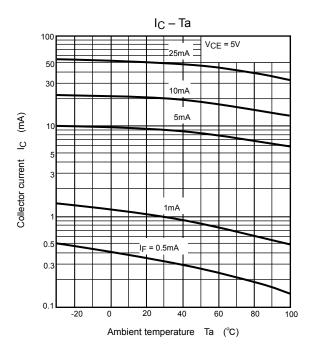












7

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