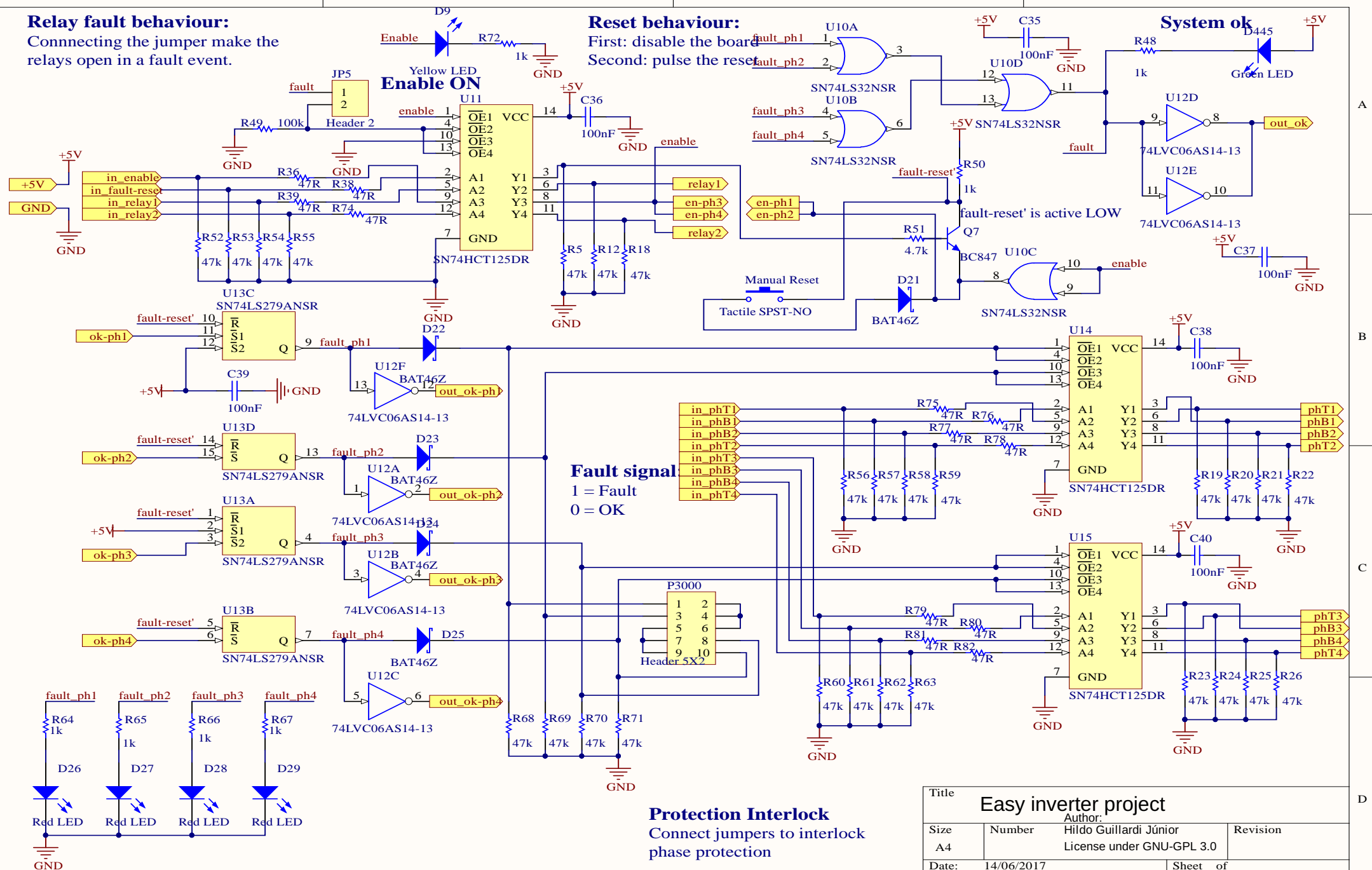


Title Easy inverter project			
Author: Joel Filipe Guerreiro			
License under GNU-GPL 3.0			
Date:	14/06/2017	Sheet	of
File:	C:\Users\...\MainPowerSupply.SchDoc	Drawn By:	

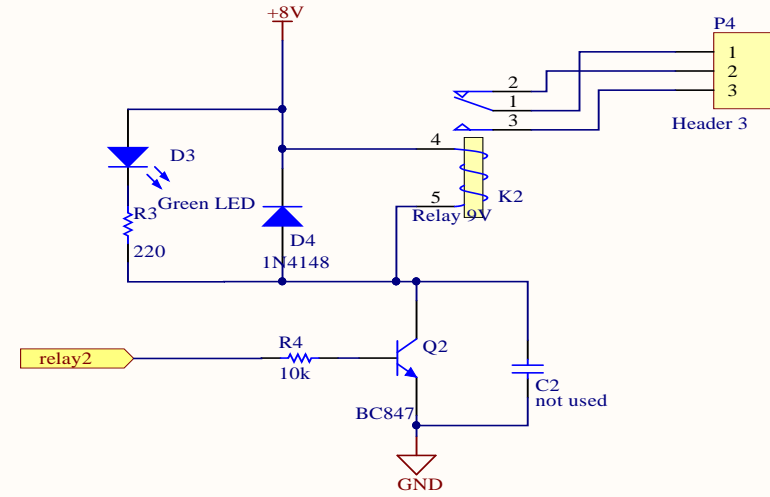
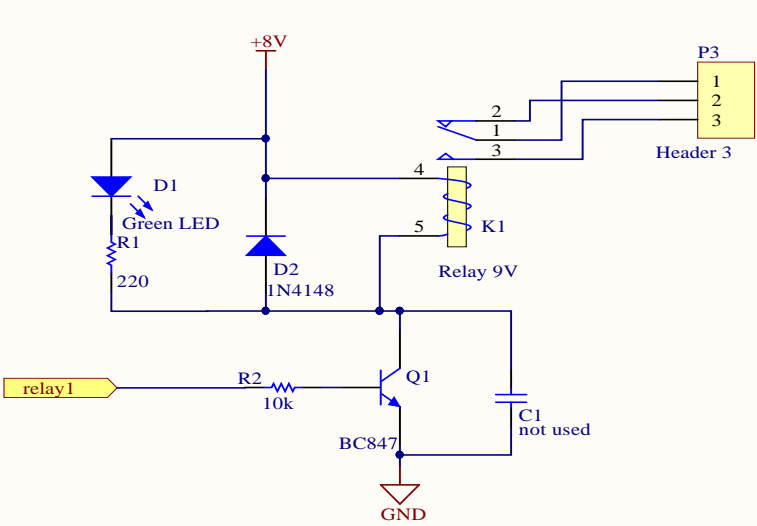
Connecting the jumper make the relays open in a fault event.



Protection Interlock

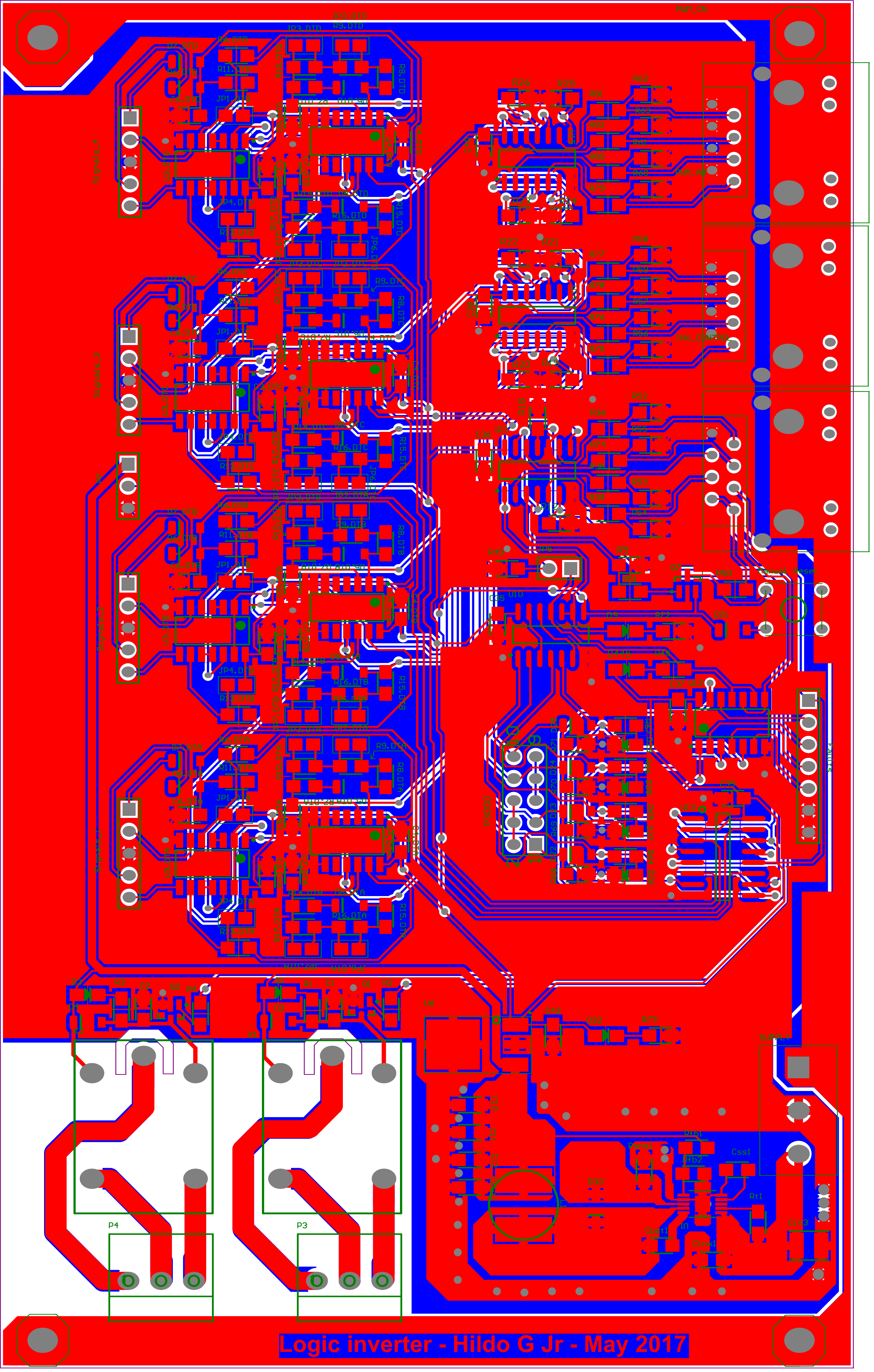
Connect jumpers to interlock phase protection

Title		Easy inverter project	
Size		Author:	
A4	Number	Hildo Guillard Junior	Revision
		License under GNU-GPL 3.0	
Date:	14/06/2017	Sheet of	
File:	C:\Users\...\LogicCircuits.SchDoc	Drawn By:	

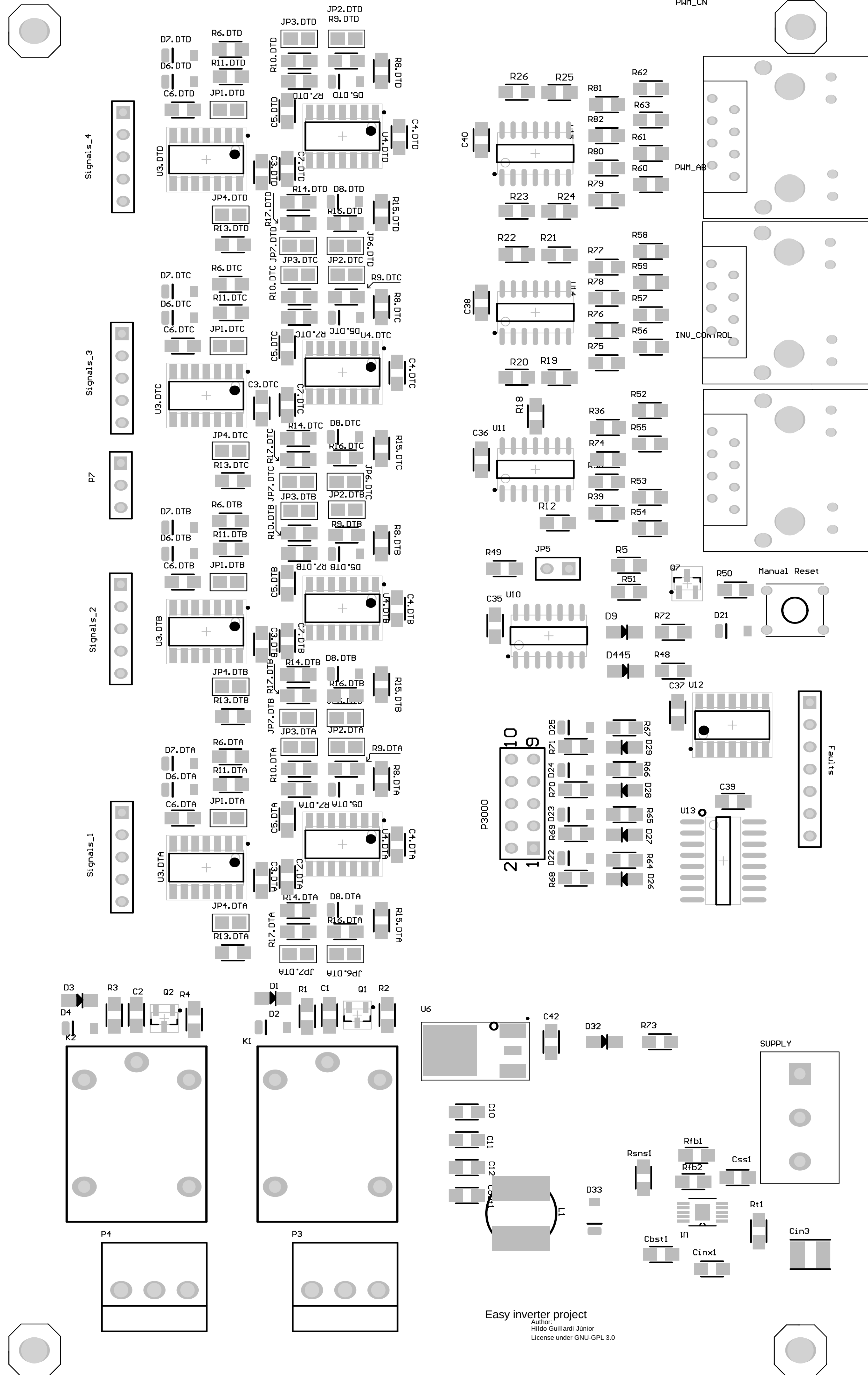


Author:
Hildo Guillard Junior
Fellipe Saldanha Garcia
License under GNU-GPL 3.0

Title Easy inverter project			
Author: Hildo Guillard Junior License under GNU-GPL 3.0			
Size A4	Number	Revision	
Date:	14/06/2017	Sheet of	
File:	C:\Users\...\Auxiliary.SchDoc	Drawn By:	



Logic inverter - Hildo G Jr - May 2017



Easy inverter project

Author:
Hildo Guillard Junior
License under GNU-GPL 3.0

