Joel Suddaby

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Objective Statement

Final year Electrical Engineering student seeking graduate positions in electronics engineering. Skilled in embedded software, schematic design, PCB layout, circuit analysis, prototyping, and testing. Passionate about expanding knowledge in both hardware and software domains. Excited to contribute to innovative projects.

Skills

- Circuit Design: Schematic design, PCB layout using KiCad and Altium.
- Embedded Systems: Software development in C, VHDL, Python.
- Testing: Experience with network analyzers, signal analyzers, oscilloscopes.
- Analysis: Circuit analysis using LT Spice, ADS.
- Software: Proficient in Microsoft Excel, Word; experience with ERP software (BAAN).

Education

Bachelor of Electrical Engineering (Expected Graduation: [April] [2025]) [University of Canterbury], [Christchurch]

- Steven Bly Memorial Scholarship recipient 2023.
- Current overall GPA: 6.46.

Experience

Tait Communications, [Christchurch]

RF Engineering Intern (2023/24)

- Designed, simulated, and tested base station components.
- Conducted hardware engineering tasks, including testing and prototyping.

RF Engineering Part-time Role (2024-present)

• Designing, simulating, and testing base station components.

Power Solutions, [Wanaka]

Electrical Technician (2022/23)

• Installed transformers, conducted oil samples, HV and LV work, cable jointing and laying.

Sports and Clubs

Canoe Slalom

- Member of CSNZ Under 18 Development Squad for 3 years.
- 1st Place C2 Men Under 18, 2019 Nationals.
- Represented New Zealand at Australian Open and Oceania Championships.
- Ranked 387th by ICF.
- Various top 10 finishes in national and international competitions.

Clubs

- Safety Officer, University of Canterbury Canoe Club.
- Member, UC Aerospace and Rocketry Club.
- Active member, University of Canterbury Skiing, Climbing, and Kayaking Club.

References

Available upon request.