

# **ENCODING SCHEME**

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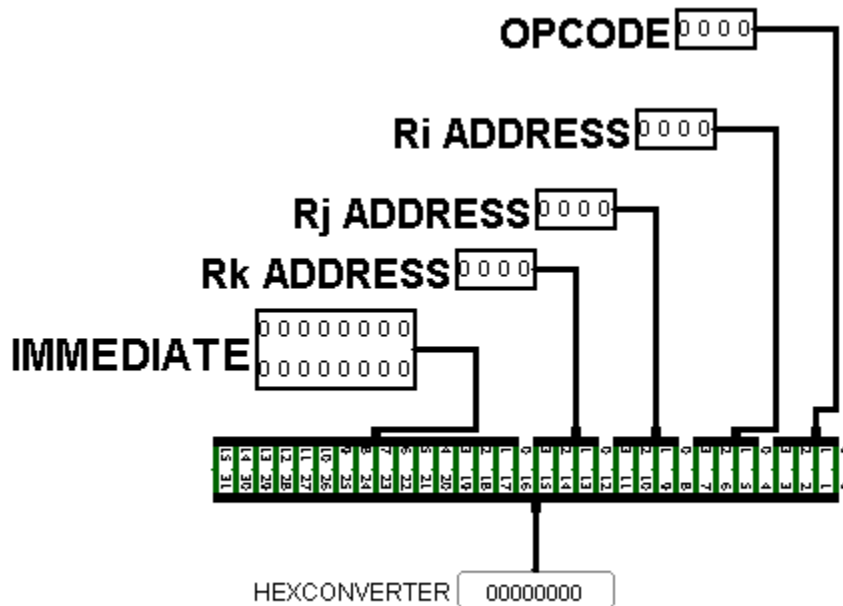
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- 1) **MOVE  $R_i, R_j$**  \\ The content of  $R_j$  is transferred to  $R_i$ .
  - 2) **MVI  $R_i, \text{Immediate (16-bit)}$**  \\ The immediate value (32-bit unsigned extended)
  - 3) **LOAD  $R_i, X(R_j)$**  \\ The content of memory location  $[[R_j] + X]$  is loaded into  $R_i$ , where  $X$  is a 16-bit unsigned immediate value.
  - 4) **STORE  $R_i, X(R_j)$**  \\ The content of register  $R_i$  is stored in memory  $[[R_j] + X]$ , where  $X$  is a 16-bit unsigned immediate value.
  - 5) **ADD  $R_i, R_j, R_k$**  \\  $R_i = R_j + R_k$ .
  - 6) **ADI  $R_i, R_j, \text{Immediate (16-bit)}$**  \\  $R_i = R_j + \text{Immediate Value}$  (32-bit unsigned extended)
  - 7) **SUB  $R_i, R_j, R_k$**  \\  $R_i = R_j - R_k$
  - 8) **SUI  $R_i, R_j, \text{Immediate (16-bit)}$**  \\  $R_i = R_j - \text{Immediate Value}$  (32-bit unsigned extended)
  - 9) **AND  $R_i, R_j, R_k$**  \\  $R_i = R_j \text{ AND } R_k$ .
  - 10) **ANI  $R_i, R_j, \text{Immediate (16-bit)}$**  \\  $R_i = R_j \text{ AND Immediate Value}$  (32-bit unsigned extended)
  - 11) **OR  $R_i, R_j, R_k$**  \\  $R_i = R_j \text{ OR } R_k$ .
  - 12) **ORI  $R_i, R_j, \text{Immediate (16-bit)}$**  \\  $R_i = R_j \text{ OR Immediate Value}$  (32-bit unsigned extended)
  - 13) **HLT** (Stops the execution).

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## OPCODE ENCODING

INSTRUCTION	OPCODE
<b>MOVE</b>	<b>0000</b>
<b>MVI</b>	<b>0001</b>
<b>LOAD</b>	<b>0010</b>
<b>STORE</b>	<b>0011</b>
<b>ADD</b>	<b>0100</b>
<b>ADI</b>	<b>0101</b>
<b>SUB</b>	<b>0110</b>
<b>SUI</b>	<b>0111</b>
<b>AND</b>	<b>1000</b>
<b>ANI</b>	<b>1001</b>
<b>OR</b>	<b>1010</b>
<b>ORI</b>	<b>1011</b>
<b>HLT</b>	<b>1100</b>

The below is the visual diagram of instruction



### How Encoding works

- 1) For each instruction one have to enter the binary code into respective slots in the **hex converter** (which is provided in the circuit )which will convert it into hexadecimal code
- 2) After converting every binary code into hexadecimal then enter the hexadecimal code into the memory

### Example :-

Suppose we have two instructions to perform

LOAD R1,X(R2)( where x is 2)

ADD R1,R2,R3

**Step 1:****LOAD R1,X(R2)**

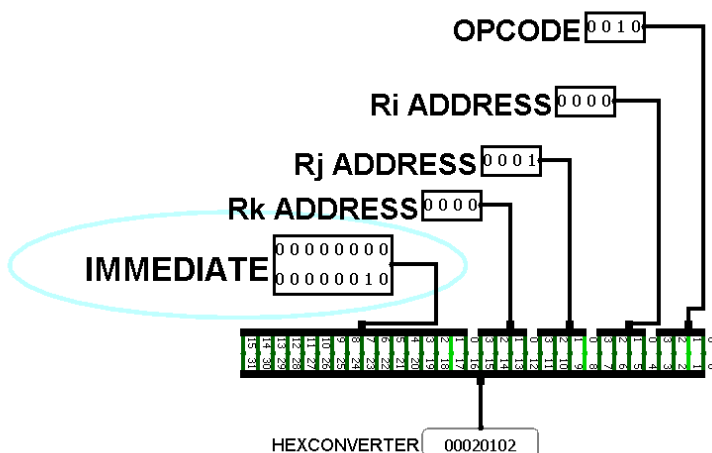
LOAD--&gt;0010

Ri ADDRESS=R1=0000(fill it in the Ri ADDRESS slot in Hex converter )

Rj ADDRESS=R2=0001(fill it in the Rj ADDRESS slot in Hex converter )

Rk ADDRESS=Not present=keep it as 0000(fill it in the Rk ADDRESS slot in Hex converter ) (It is not used any way)

Immediate =X=00000010(fill it in the immediate slot in Hex converter )

**HEXCONVERTOR****!!NOTE THIS IS JUST FOR CONVERSION PURPOSE  
AFTER Converting we have to enter it into memory**

## ADD R1,R2,R3

ADD-->0100

Ri ADDRESS=R1=0000(fill it in the Ri ADDRESS slot in Hex converter )

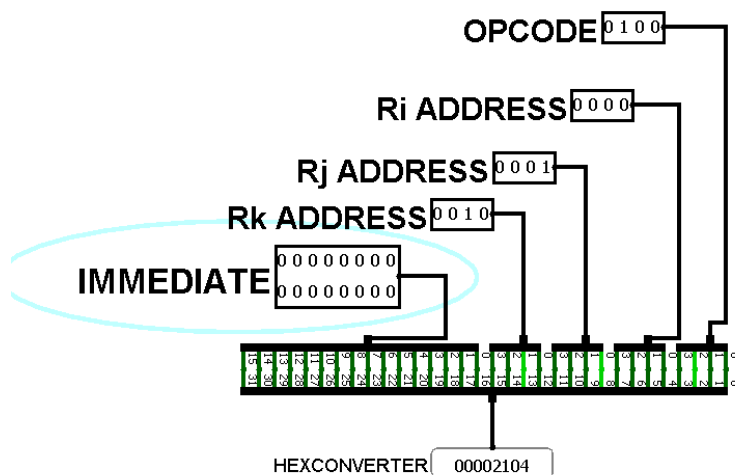
Rj ADDRESS=R2=0001(fill it in the Rj ADDRESS slot in Hex converter )

Rk ADDRESS=R3=0010(fill it in the Rk ADDRESS slot in Hex converter )

Immediate =not present=keep it as 00000000(fill it in the immediate slot in Hex converter )

## HEXCONVERTOR

!!NOTE THIS IS JUST FOR CONVERSION PURPOSE  
AFTER Converting we have to enter it into memory



**Points to be noted**

- 1) If we don't use a certain slot in the converter then make sure it is filled with only zero's**
- 2) Ex:- suppose in hlt we only use opcode slot ,so remaining all slots should be zeros.**
- 3) Ex2:- In MVI we use Ri and immediate slots, so remaining all slots should be only filled with zeros**

