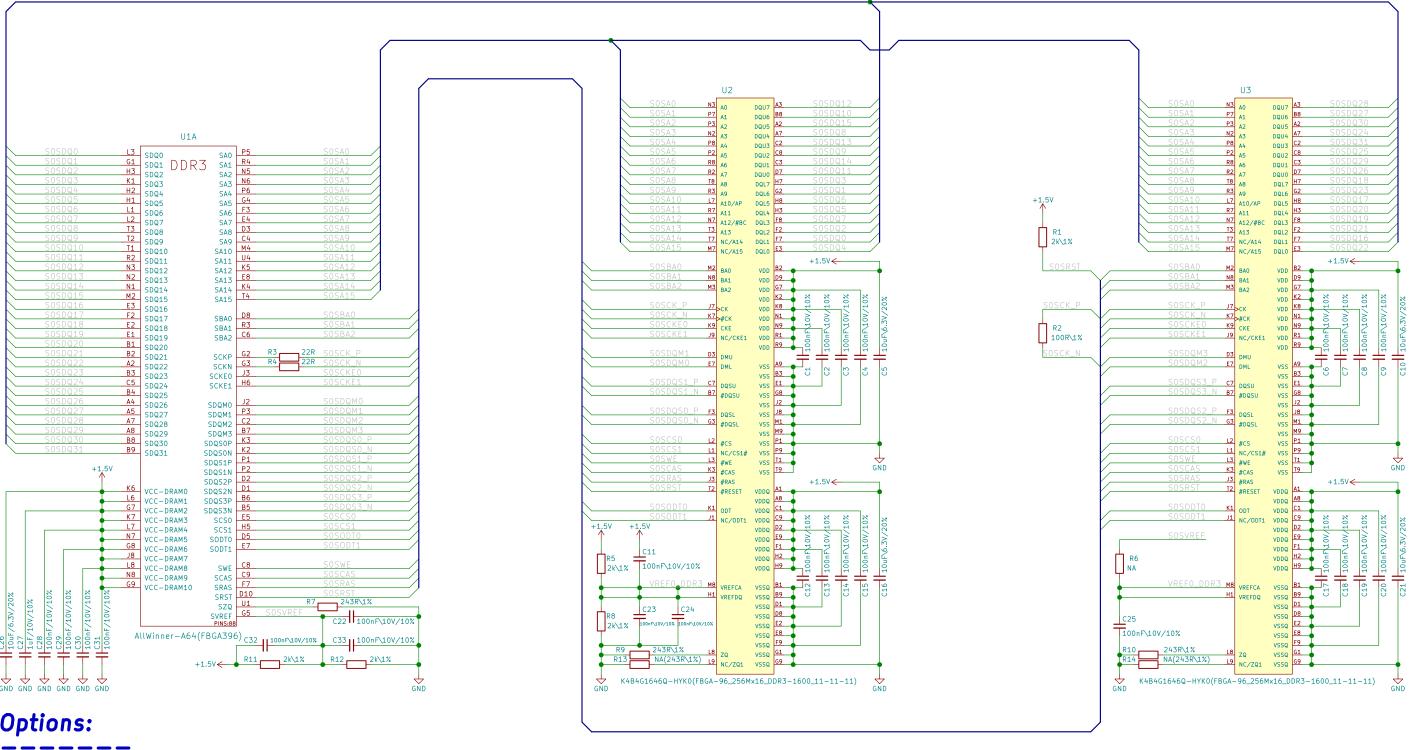
DDR3:1GByte



Options:

- 1. Use $2(DDR3\ 256Mx16\ Memory\ chips)x4Gb = 1GByte$, i.e. $2xH5TQ4G63MFR-PBC(or\ K4B4G1646Q-HYK0) -> Default 2.$ Use $2(DDR3\ 512Mx16\ Memory\ chips)x8Gb = 2GBytes$, i.e. $2xH5TC8G63AMR-PBA(or\ K4B8G1646Q-MYK0)$
- Note:

We have used a number of fully compatible, but different DDR3 memories due to supply unavailability. In such cases the memory part name in the schematic might remain outdated. It is recommended to always refer to the exact memory name printed on the component itself.

DDR3 Memory	
<c> 2020</c>	
OLIMEX LTD, Bulgaria	
Sheet: /	
File: A64-OlinuXino_Rev_G.sch	
Title: A64-OLinuXino	
Size: A3 Date: 2020-02-11	Rev: G
KiCad E.D.A. kicad 5.1.0-rc2-unknown-036be7d80ubuntu16.0	4.1 ld: 1/4

NAND Flash

eMMC

SPI Flash

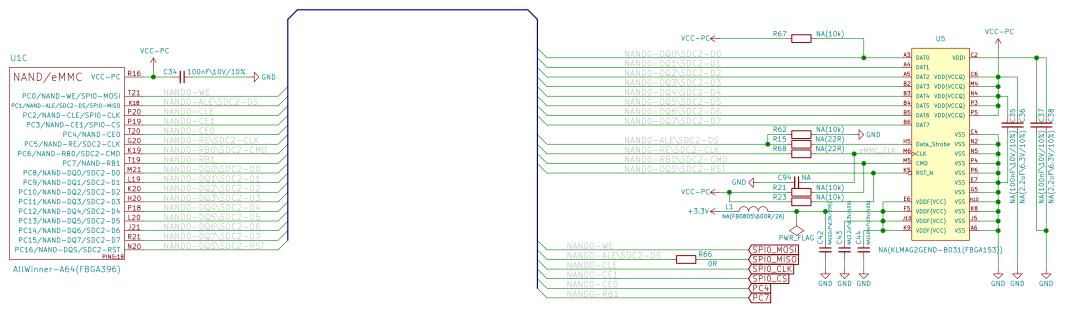
Was removed from the design

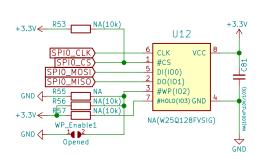
16G Bytes eMMC

Present in -eXG

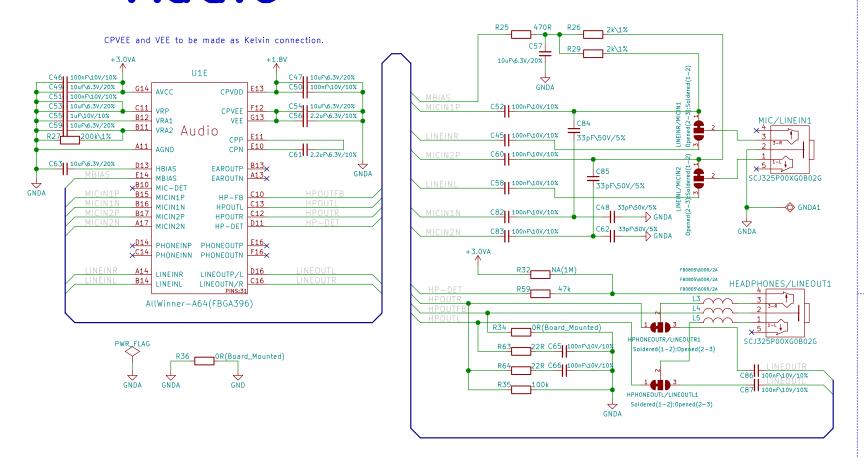
16M Bytes SPI Flash

Present in -sXM





Audio



T-Card

