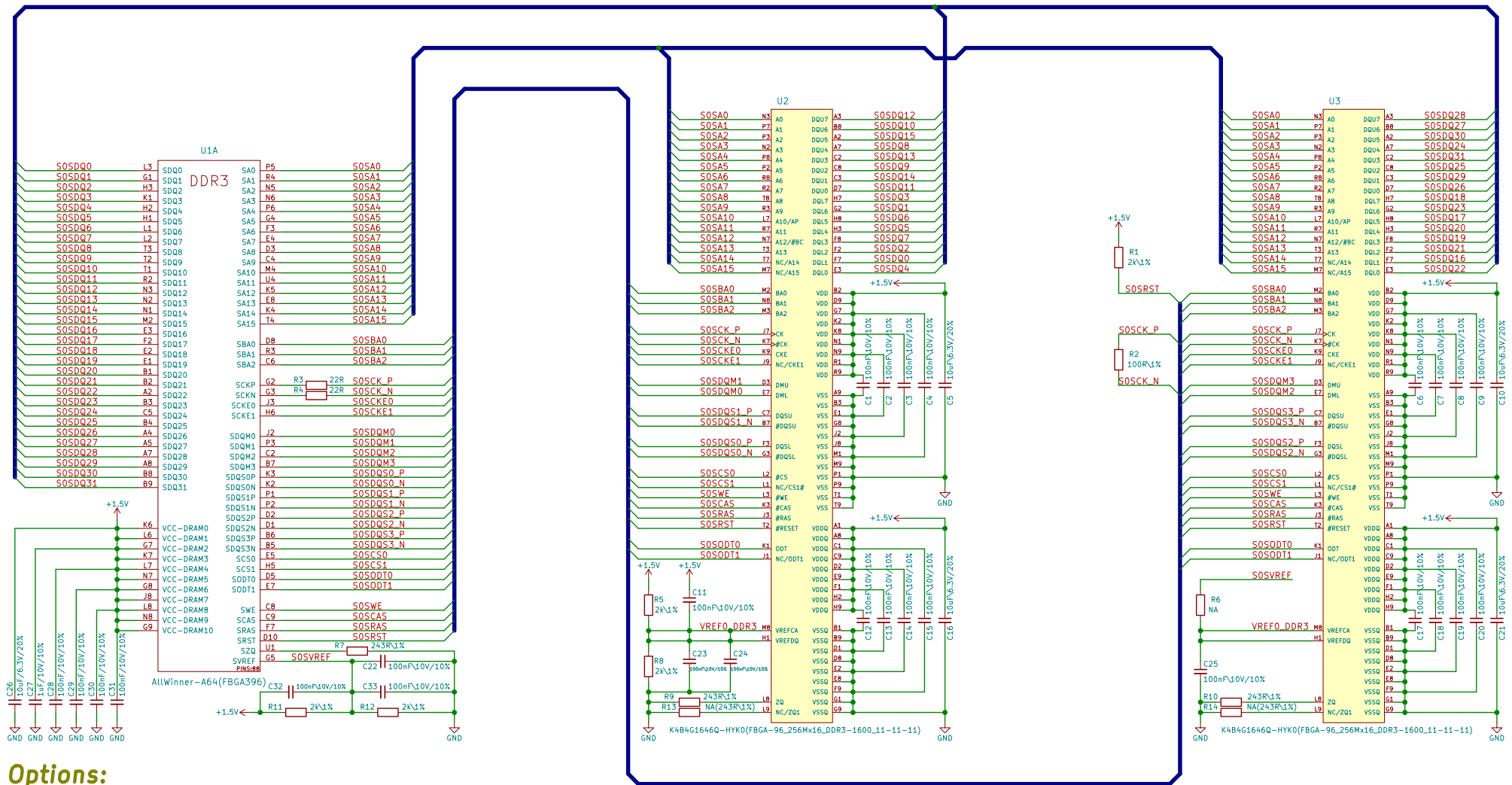


# DDR3:1GByte



## Options:

1. Use 2(DDR3 256Mx16 Memory chips)x4Gb = 1GByte, i.e. 2xH5TQ4G63MFR-PBC(or K4B4G1646Q-HYK0) -> Default
2. Use 2(DDR3 512Mx16 Memory chips)x8Gb = 2GBytes, i.e. 2xH5TC8G63AMR-PBA(or K4B8G1646Q-MYK0)

## Note:

We have used a number of fully compatible, but different DDR3 memories due to supply unavailability. In such cases the memory part name in the schematic might remain outdated. It is recommended to always refer to the exact memory name printed on the component itself.

**OLIMEX**

open source  
hardware

<c> 2025  
OLIMEX LTD, Bulgaria

Sheet: /  
File: A64-OlinuXino\_Rev\_H.sch

Title: A64-OlinuXino

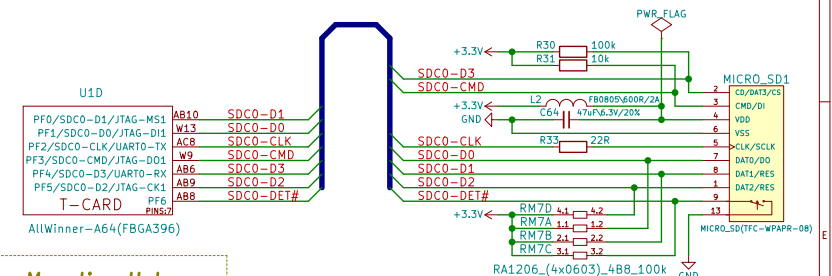
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Rev: H  
Id: 1/4

## SPI Flash

Present in -sXM

## T-Card



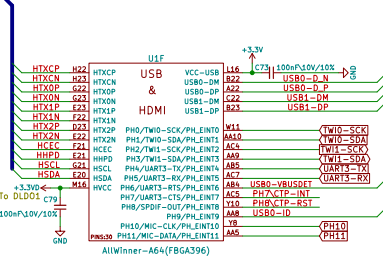
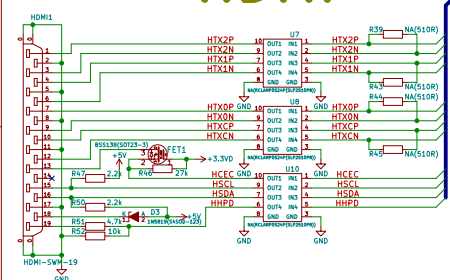
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Mounting\_hole2  
Mounting\_hole3  
Mounting\_hole



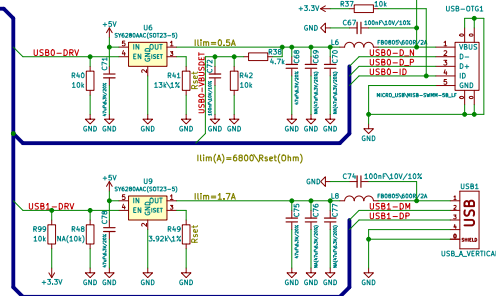
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hardware

Rev: H  
Id: 2/4

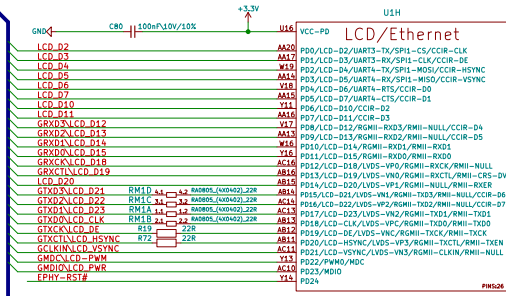
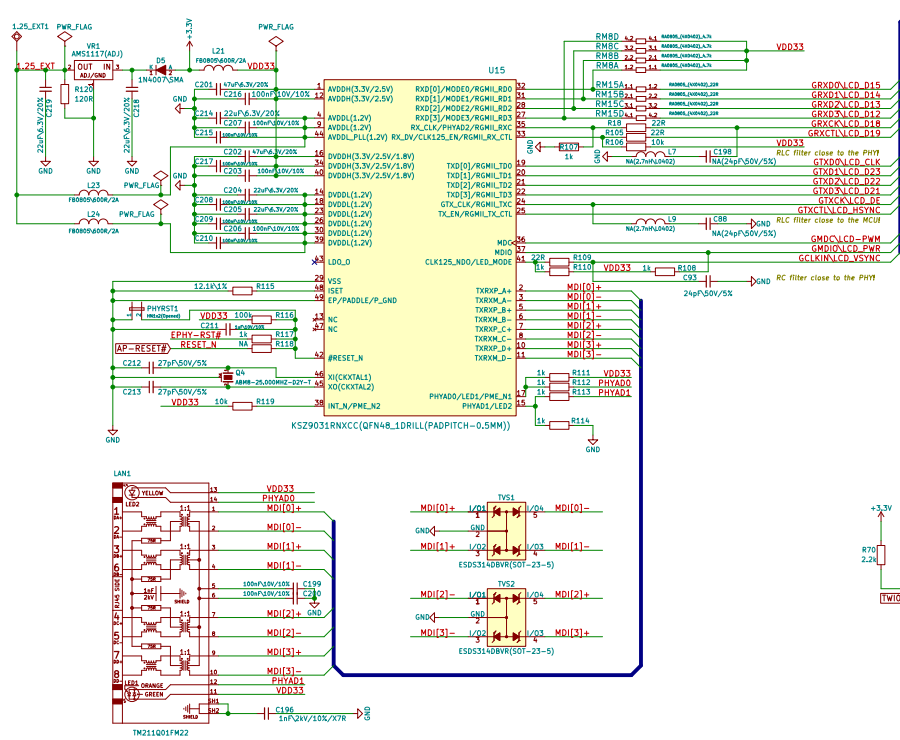
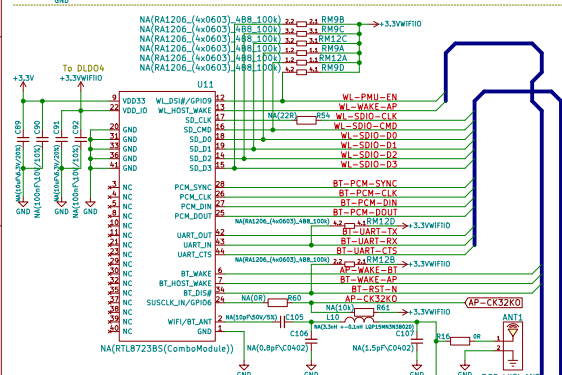
## HDMI



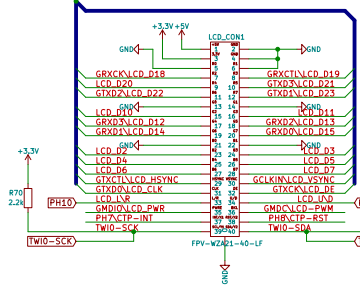
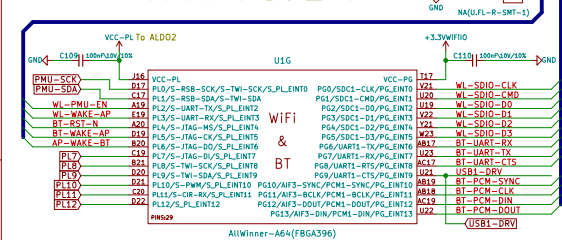
# USB



## Gigabit Ethernet or LCD



## WiFi&BT



## EEPROM

MAC Address  
I2C Address: 0xA0

