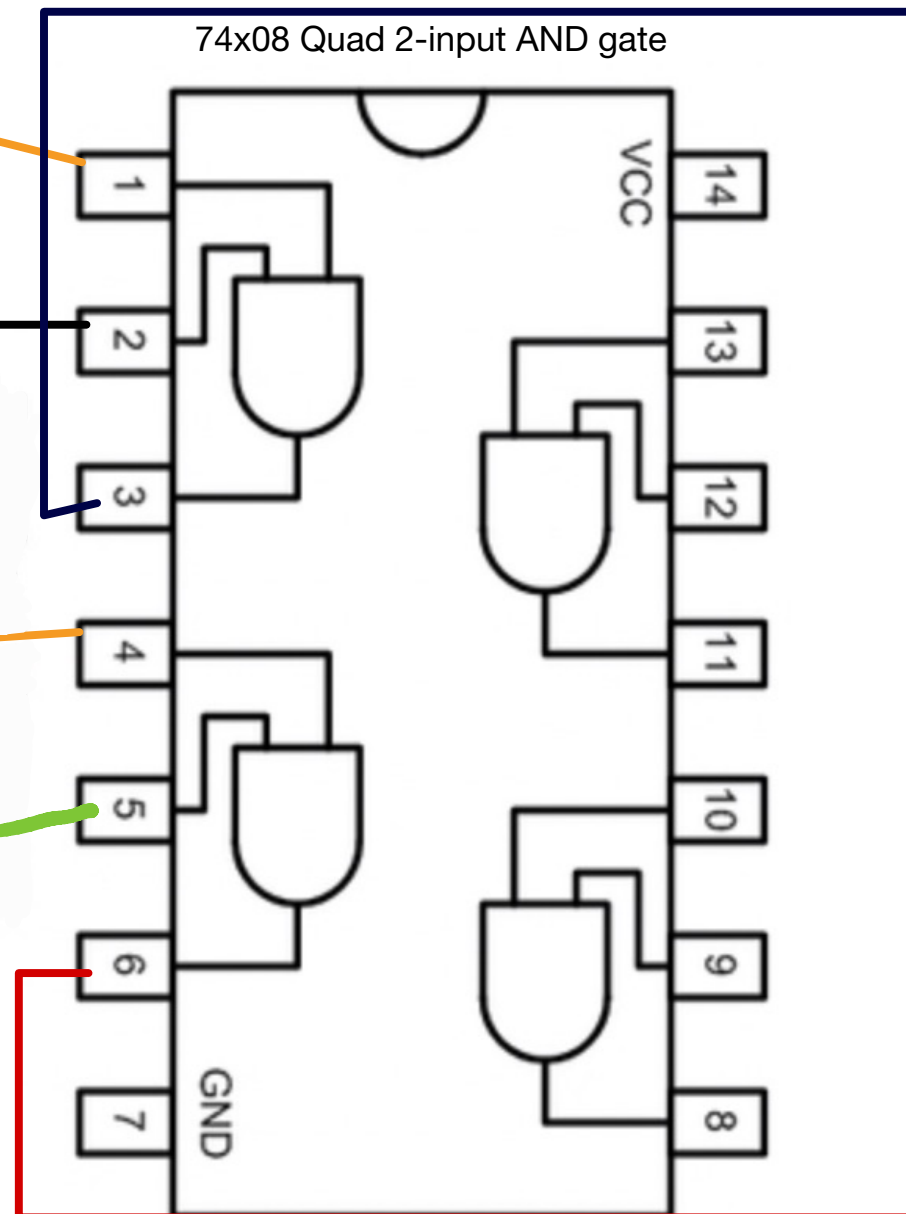
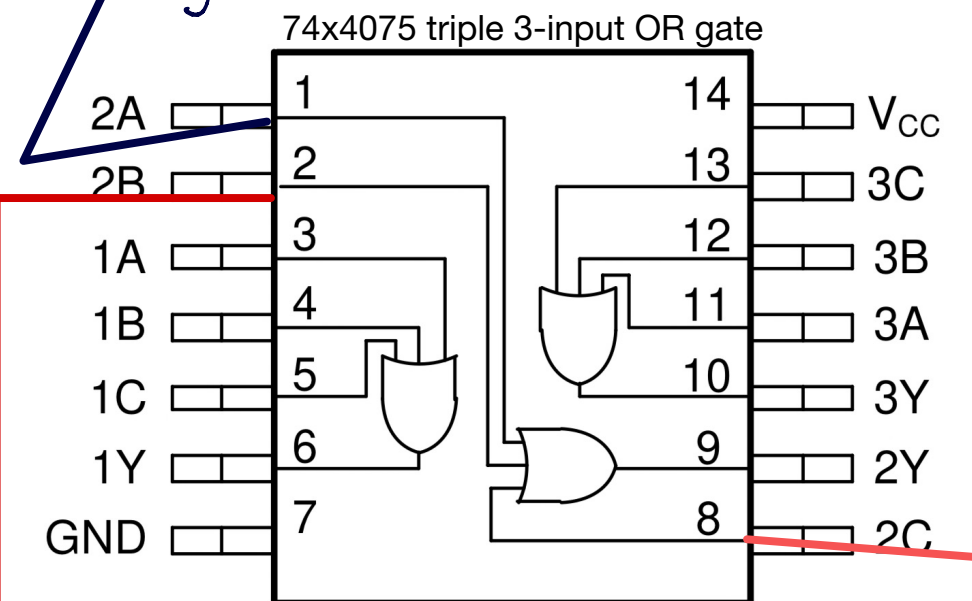


result from (D) goes to 2 And gates.



result from C AND B goes to pin 2B.

result from D And A goes to or Gate 2A.



Functional pinout

result from C goes to pin 2C.