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## Engbedded Atmel AVR® Fuse Calculator

### Device selection

Select the AVR device type you want to configure. When changing this setting, default fuse settings will automatically be applied. Presets (hexadecimal representation of the fuse settings) can be reviewed and even be set in the last form at the bottom of this page.

AVR part name: ATmega328P  (141 parts currently listed)

### Feature configuration

This allows easy configuration of your AVR device. All changes will be applied instantly.

#### Features

Int. RC Osc. 8 MHz; Start-up time PWRDWN/RESET: 6 CK/14 CK + 65 ms; [CKSEL=00]

- Clock output on PORTB0; [CKOUT=0]
- Divide clock by 8 internally; [CKDIV8=0]
- Boot Reset vector Enabled (default address=\$0000); [BOOTRST=0]

Boot Flash section size=256 words Boot start address=\$3F00; [BOOTSZ=11]

- Preserve EEPROM memory through the Chip Erase cycle; [EESAVE=0]
- Watch-dog Timer always on; [WDTON=0]
- Serial program downloading (SPI) enabled; [SPIEN=0]
- Debug Wire enable; [DWEN=0]
- Reset Disabled (Enable PC6 as i/o pin); [RSTDISBL=0]

Brown-out detection disabled; [BODLEVEL=111]

### Manual fuse bits configuration

This table allows reviewing and direct editing of the AVR fuse bits. All changes will be applied instantly.

Note:  means unprogrammed (1);  means programmed (0).

Bit	Low	High	Extended
7	<input checked="" type="checkbox"/> <b>CKDIV8</b>	<input type="checkbox"/> <b>RSTDISBL</b>	
	Divide clock by 8	External reset disable	
6	<input type="checkbox"/> <b>CKOUT</b>	<input type="checkbox"/> <b>DWEN</b>	
	Clock output	debugWIRE Enable	
5	<input type="checkbox"/> <b>SUT1</b>	<input checked="" type="checkbox"/> <b>SPIEN</b>	
	Select start-up time	Enable Serial programming and Data Downloading	
4	<input checked="" type="checkbox"/> <b>SUTO</b>	<input type="checkbox"/> <b>WDTON</b>	
	Select start-up time	Watchdog Timer Always On	
3	<input checked="" type="checkbox"/> <b>CKSEL3</b>	<input checked="" type="checkbox"/> <b>EESAVE</b>	
	Select Clock Source	EEPROM memory is preserved through chip erase	
2	<input checked="" type="checkbox"/> <b>CKSEL2</b>	<input type="checkbox"/> <b>BOOTSZ1</b>	<input type="checkbox"/> <b>BODLEVEL2</b>
	Select Clock Source	Select boot size	Brown-out Detector trigger level
1	<input type="checkbox"/> <b>CKSEL1</b>	<input type="checkbox"/> <b>BOOTSZ0</b>	<input type="checkbox"/> <b>BODLEVEL1</b>
	Select Clock Source	Select boot size	Brown-out Detector trigger level
0	<input checked="" type="checkbox"/> <b>CKSEL0</b>	<input type="checkbox"/> <b>BOOTRST</b>	<input type="checkbox"/> <b>BODLEVEL0</b>
	Select Clock Source	Select reset vector	Brown-out Detector trigger level

### Current settings

These fields show the actual hexadecimal representation of the fuse settings from above. These are the

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